

Service Manual



Service Manual

KE970/ME970



Model : KE970/ME970



REVISED HISTORY

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* The information in this manual is subject to change without notice and should not be construed as a commitment by LGE Inc. Furthermore, LGE Inc. reserves the right, without notice, to make changes to equipment design as advances in engineering and manufacturing methods warrant.

* This manual provides the information necessary to install, program, operate and maintain the ME970.

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1. INTRODUCTION

1.1 Purpose

This manual provides the information necessary to repair, calibration, description and download the features of the ME970.

1.2. Regulatory Information

A. Security

Toll fraud, the unauthorized use of telecommunications system by an unauthorized part (for example, persons other than your company's employees, agents, subcontractors, or person working on your company's behalf) can result in substantial additional charges you're your telecommunications services. System users are responsible for the security of own system.

There are may be risks of toll fraud associated with your telecommunications system. System users are responsible for programming and configuring the equipment to prevent unauthorized use. LGE does not warrant that this product is immune from the above case but will prevent unauthorized use of common-carrier telecommunication service of facilities accessed through or connected to it. LGE will not be responsible for any charges that result from such unauthorized use.

B. Incidence of Harm

If a telephone company determines that the equipment provided to customer is faulty and possibly causing harm or interruption in service to the telephone network, it should disconnect telephone service until repair can be done. A telephone company may temporarily disconnect service as long as repair is not done.

C. Changes in Service

A local telephone company may make changes in its communications facilities or procedure. If these changes could reasonably be expected to affect the use of the ME970 or compatibility with the network, the telephone company is required to give advanced written notice to the user, allowing the user to take appropriate steps to maintain telephone service.

D. Maintenance Limitations

Maintenance limitations on the ME970 must be performed only at the LGE or its authorized agents. The user may not make any changes and/or repairs except as specifically noted in this manual. Therefore, note that unauthorized alternations or repair may affect the regulatory status of the system and may void any remaining warranty.

1. INTRODUCTION

E. Notice of Radiated Emissions

The ME970 complies with rules regarding radiation and radio frequency emission as defined by local regulatory agencies. In accordance with these agencies, you may be required to provide information such as the following to the end user.

F. Pictures

The pictures in this manual are for illustrative purposes only; your actual hardware may look slightly different.

G. Interference and Attenuation

An ME970 may interfere with sensitive laboratory equipment, medical equipment, etc. Interference from unsuppressed engines or electric motors may cause problems.

H. Electrostatic Sensitive Devices

ATTENTION

Boards, which contains Electrostatic Sensitive Device(ESD), are indicated by the sign.

Following information is ESD handling: Service personnel should ground themselves by using a wrist strap when exchange system boards.

When repairs are made to a system board, they should spread the floor with anti-static mat which is also grounded. Use a suitable, grounded soldering iron. Keep sensitive parts in these protective packages until these are used. When returning system boards or parts such as EEPROM to the factory, use the protective package as described.

1.3 ABBREVIATION

For the purposes of this manual, following abbreviations apply:

APC	Automatic Power Control
BB	Baseband
BER	Bit Error Ratio
CC-CV	Constant Current - Constant Voltage
CLA	Cigar Lighter Adapter
DAC	Digital to Analog Converter
DCS	Digital Communication System
dBm	dB relative to 1 milli-watt
DSP	Digital Signal Processing
EEPROM	Electrical Erasable Programmable Read-Only Memory
EL	Electroluminescence
ESD	Electrostatic Discharge
FPCB	Flexible Printed Circuit Board
GMSK	Gaussian Minimum Shift Keying
GPIB	General Purpose Interface Bus
GPRS	General Packet Radio Service
GSM	Global System for Mobile Communications
IPUI	International Portable User Identity
IF	Intermediate Frequency
LCD	Liquid Crystal Display
LDO	Low Drop Output
LED	Light Emitting Diode
LGE	LG Electronics

1. INTRODUCTION

OPLL	Offset Phase Locked Loop
PAM	Power Amplifier Module
PCB	Printed Circuit Board
PGA	Programmable Gain Amplifier
PLL	Phase Locked Loop
PSTN	Public Switched Telephone Network
RF	Radio Frequency
RLR	Receiving Loudness Rating
RMS	Root Mean Square
RTC	Real Time Clock
SAW	Surface Acoustic Wave
SIM	Subscriber Identity Module
SLR	Sending Loudness Rating
SRAM	Static Random Access Memory
STMR	Side Tone Masking Rating
TA	Travel Adapter
TDD	Time Division Duplex
TDMA	Time Division Multiple Access
UART	Universal Asynchronous Receiver/Transmitter
VCO	Voltage Controlled Oscillator
VCTCXO	Voltage Control Temperature Compensated Crystal Oscillator
WAP	Wireless Application Protocol

2. PERFORMANCE

2.1 H/W Feature

Item	Feature	Comment
Standard Battery	Li-ion, 800mAh	
AVG TCVR Current	280mA	PL5
Standby Current	<2.7mA	@PP9
Talk time	3hours (GSM TX Level 7)	
Standby time	277 hours (Paging Period:9, RSSI: -85dBm)	
Charging time	3 hours	
RX Sensitivity	GSM900 : -105dBm, DCS/PCS : -105dBm	
TX output power	GSM900: 32dBm (Level 5) DCS/PCS: 29dBm (Level 0)	
GPRS compatibility	Class 10	
SIM card type	3V Small	
Display	320 x 240 pixels, 2.2 inch wide, 265K color, TFT	
Status Indicator	Soft icons Key Pad 0 ~ 9, #, *, END/PWR, SEND, CLEAR Key Side Key Up/Down, AF/Camera double action key	
ANT	Built in antenna	
EAR Phone Jack	18pin multi port Headset jack with Remote controller	
PC Synchronization	Yes	
Speech coding	EFR/FR/AMR	
Data and Fax	Yes	
Vibrator	Yes	
Buzzer	No	
Voice Recoding	Yes	
C-Mic	Yes	
Receiver	Yes	
Travel Adapter	Yes	
Options	Bluetooth hands-free kit, Data Kit	

2. PERFORMANCE

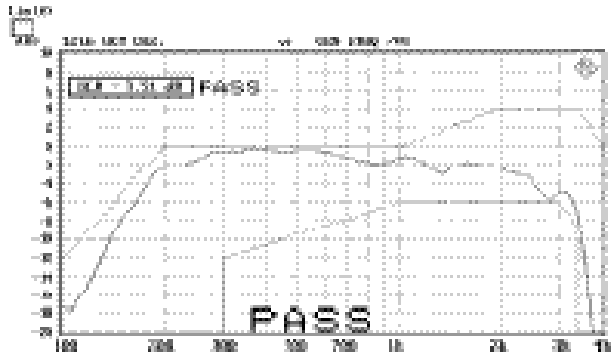
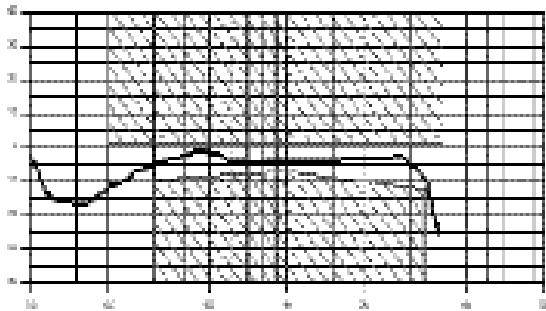
2.2 Technical specification

Item	Description	Specification					
1	Frequency Band	GSM900 • TX: 890 + 0.2 x n MHz • RX: 935 + 0.2 x n MHz (n = 1 ~ 124) EGSM • TX: 890 + 0.2 x (n-1024) MHz • RX: 935 + 0.2 x (n-1024) MHz (n = 975 ~ 1023) DCS1800 • TX: 1710 + (n-511) x 0.2 MHz (n = 512 ~ 885) • RX: TX + 95 MHz PCS1900 • TX: 1850.2 + (n-512) x 0.2 MHz (n = 512 ~ 810) • RX: TX + 80MHz					
2	Phase Error	RMS < 5 degrees Peak < 20 degrees					
3	Frequency Error	< 0.1ppm					
4	Power Level	GSM900/EGSM					
		Level	Power	Toler.	Level	Power	Toler.
		5	33 dBm	±2dB	13	17 dBm	±3dB
		6	31 dBm	±3dB	14	15 dBm	±3dB
		7	29 dBm	±3dB	15	13 dBm	±3dB
		8	27 dBm	±3dB	16	11 dBm	±5dB
		9	25 dBm	±3dB	17	9 dBm	±5dB
		10	23 dBm	±3dB	18	7 dBm	±5dB
		11	21 dBm	±3dB	19	5 dBm	±5dB
		12	19 dBm	±3dB			
		DCS1800/PCS1900					
		Level	Power	Toler.	Level	Power	Toler.
		0	30 dBm	±2dB	8	14 dBm	±3dB
		1	28 dBm	±3dB	9	12 dBm	±4dB
		2	26 dBm	±3dB	10	10 dBm	±4dB
		3	24 dBm	±3dB	11	8 dBm	±4dB
		4	22 dBm	±3dB	12	6 dBm	±4dB
		5	20 dBm	±3dB	13	4 dBm	±4dB
		6	18 dBm	±3dB	14	2 dBm	±5dB
7	16 dBm	±3dB	15	0 dBm	±5dB		

2. PERFORMANCE

Item	Description	Specification	
5	Output RF Spectrum (due to modulation)	GSM900/EGSM	
		Offset from Carrier (kHz).	Max. dBc
		100	+0.5
		200	-30
		250	-33
		400	-60
		600~ <1,200	-60
		1,200~ <1,800	-60
		1,800~ <3,000	-63
		3,000~ <6,000	-65
		6,000	-71
		DCS1800/PCS1900	
		Offset from Carrier (kHz).	Max. dBc
		100	+0.5
		200	-30
		250	-33
		400	-60
		600~ <1,200	-60
		1,200~ <1,800	-60
		1,800~ <3,000	-65
		3,000~ <6,000	-65
		6,000	-73
6	Output RF Spectrum (due to switching transient)	GSM850	
		Offset from Carrier (kHz)	Max. (dBm)
		400	-19
		600	-21
		1,200	-21
		1,800	-24

2. PERFORMANCE

Item	Description	Specification	
6	Output RF Spectrum (due to switching transient)	DCS1800/PCS1900	
		Offset from Carrier (kHz).	Max. (dBm)
		400	-22
		600	-24
		1,200	-24
		1,800	-27
7	Spurious Emissions	Conduction, Emission Status	
8	Bit Error Ratio	GSM850 BER (Class II) < 2.439% @-102dBm	
		DCS1800/PCS1900 BER (Class II) < 2.439% @-100dBm	
9	Rx Level Report accuracy	± 3 dB	
10	SLR	8 ± 3 dB	
11	Sending Response		
12	RLR	-15 ± 3 dB	
13	Receiving Response		
		* Mean that Adopt a straight line in between 300 Hz and 1,000 Hz to be Max. level in the range.	

2. PERFORMANCE

Item	Description	Specification	
14	STMR	> 17 dB	
15	Stability Margin	> 40 dB	
16	Idle Noise Sending	< -64dB	
17	Idle Noise Receiving	< -47dB	
18	Side tone Distortion	Three stage distortion < 10%	
19	<Change> System frequency (26 MHz) tolerance	$\leq 2.5\text{ppm}$	
20	<Change>32.768KHz tolerance	$\leq 30\text{ppm}$	
21	Power consumption	Standby - Normal $\leq 5.2\text{mA}$ (Mix. power)	
22	Talk Time	GSM900/Lvl 7(Battery Capacity 800mA) : Min. 2.5 hr GSM900/Lvl 12(Battery Capacity 800 mA) : Min. 3hr	
23	Standby Time	Under conditions, at least Min. 250 hr 1. Brand new and full 800mAh battery 2. Full charge, no receive/send and keep GSM in idle mode. 3. Broadcast set off. 4. Signal strength display set at 3 level above. 5. Backlight of phone set off.	
24	Ringer Volume	At least 65 dB under below conditions: 1. Ringer set as ringer. 2. Test distance set as 50 cm	
25	Charge Voltage	Fast Charge : < 450 mA Slow Charge: < 55mA	
26	Antenna Display	Antenna Bar Number	Power
		5	-85 dBm ~
		4	-90 dBm ~ -86 dBm
		3	-95 dBm ~ -91 dBm
		2	-100 dBm ~ -96 dBm
		1	-105 dBm ~ -101 dBm
		0	~ -105 dBm

2. PERFORMANCE

Item	Description	Specification	
27	Battery Indicator	Battery Bar Number	Voltage($\pm 0.05V$)
		4	3.86V~4.2V
		3	3.75V~3.85V
		2	3.75V~3.69V
		1	3.69V~3.62V
		0	3.62V~
28	Low Voltage Warning	3.62V↓ $\pm 0.05V$ (Call)	
		3.50V↓ $\pm 0.05V$ (Standby)	
29	Forced shut down Voltage	3.35± 0.05 V	
30	Battery Type	1 Li-ion Battery Standard Voltage = 3.7 V Battery full charge voltage = 4.2 V Capacity: 800mAh	
31	Travel Charger	Switching-mode charger Input: 100 ~ 240 V, 50/60Hz Out put: 4.8, 0.9A	

3. TECHNICAL BRIEF

3.1. ME970 Component Block diagram.

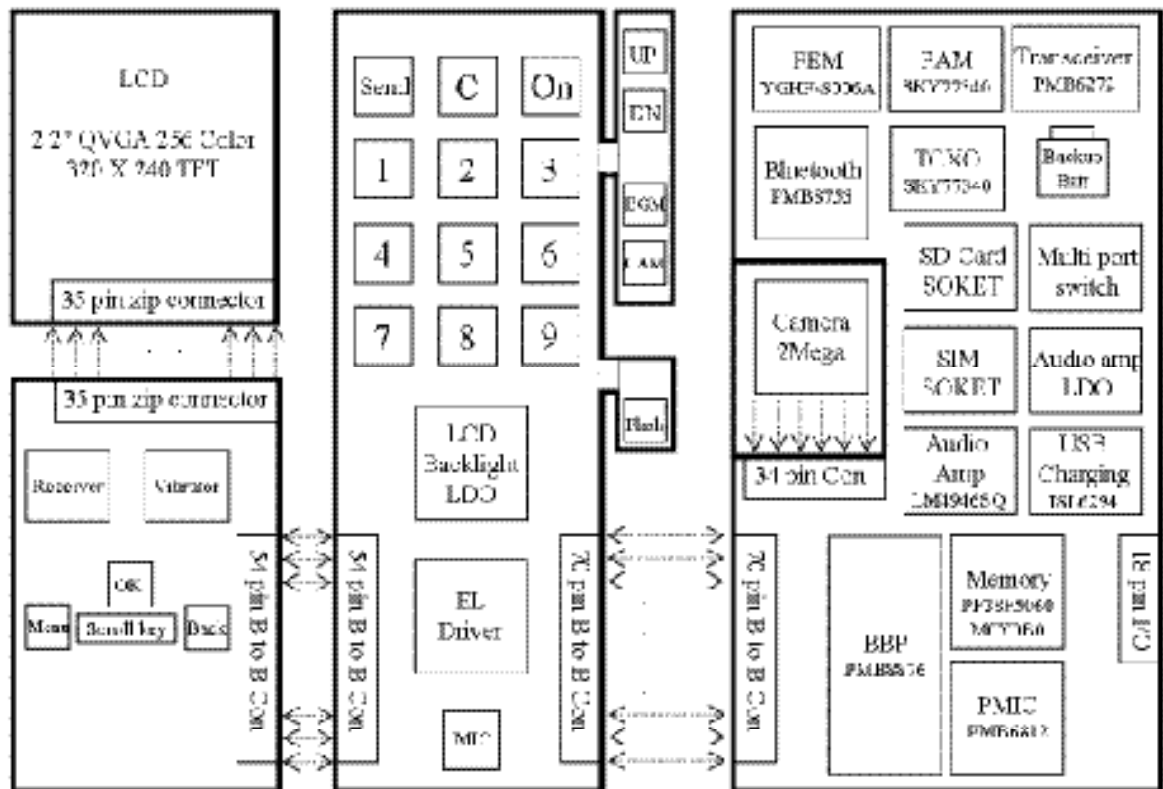


Figure 1. ME970 Hardware architecture

ME970 is composed with 3 different PCB part such as main PCB, keypad FPCB and slide FPCB.

3. TECHNICAL BRIEF

3.2. Baseband Processor (BBP) Introduction

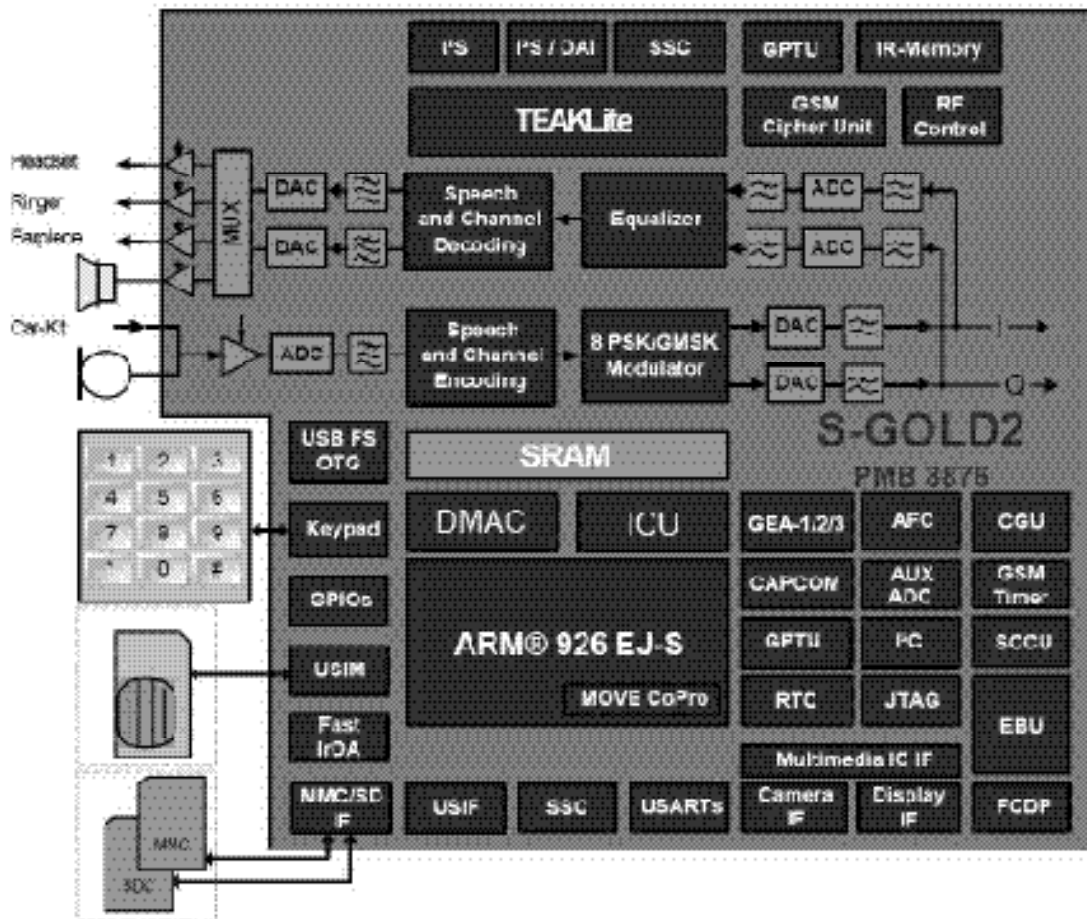


Figure 2. Top level block diagram of the S-GOLD2™ (PMB8876)

3.2.1 General Description

S-GOLD2™ is a GSM/EDGE single chip mixed signal Baseband IC containing all analog and digital functionality of a cellular radio. Additionally S-GOLD2™ Provides multimedia extensions such as camera, software MIDI, MP3 sound. It is designed as a single chip solution, integrating the digital and mixed signal portions of the base band in 0.13um, 1.5V technology. The chip will fully support the FR, EFR, HR and AMR-NB vocoding. S-GOLD2™ support multi-slot operation modes HSCSD (up to class 10), GPRS for high speed data application (up to class 12) and EGPRS (up to class 12) without additional external hardware.

3.2.2. Block Description

- Processing core

ARM926EJ-S 32 bit processor core for controller functions. The ARM926EJ-S includes an MMU, and the Jazelle Java extension for Java acceleration.

 - TEAKLite DSP core
- ARM-Memory
 - 32k Byte Boot ROM on the AHB
 - 96k Byte SRAM on the AHB, flexibly usable as program or data RAM
 - 16k Byte Cache for Program (internal)
 - 8k Byte tightly coupled memory for Program(internal)
 - 8k Byte Cache for Data(internal)
 - 8k Byte tightly coupled memory for Data(internal)
- DSP-Memory
 - 104K x 16bit Program ROM
 - 8k x 16bit Program RAM
 - 60k x 16bit Data ROM
 - 37k x 16bit Data RAM
 - Incremental Redundancy(IR) Memory of 35904 words of 16bit
- Shared Memory Block

1.5K x 32bit Shared RAM(dual ported) between controller system and TEAKLite.
- Controller Bus system

The processor cores and their peripherals are connected by powerful buses. Multi-layer AHB for connecting the ARM and the other master capable building blocks with the internal and external memories and with the peripheral buses.
- Clock system

The clock system allows widely independent selection of frequencies for the essential parts of the S-GOLD2. Thus power consumption and performance can be optimized for each application.
- Functional Hardware block
 - CPU and DSP Timers
 - MOVE coprocessor performing motion estimation for video encoding algorithms (H.263, MPEG-4)
 - Programmable PLL with additional phase shifters for system clock generation
 - GSM Timer Module that off-loads the CPU from radio channel timing
 - GMSK / 8-PSK Modulator according to GSM-standard 05.04 (5/2000)
 - GMSK Modulator: gauss-filter with $B \cdot T = 0.3$
 - EDGE Modulator: 8PSK-modulation with linearized GMSK-Pulse-Filter
 - Hardware accelerators for equalizer and channel decoding.
 - Incremental Redundancy memory for EDGE class 12 support
 - A5/1, A5/2, A5/3 Cipher unit
 - GEA1, GEA2, GEA3 Cipher Unit to support GPRS data transmission

3. TECHNICAL BRIEF

- Advanced static and dynamic power management features including TDMA-Frame synchronous low power mode and enhanced CPU modes(idle and sleep modes)
- Pulse Number Modulation output for Automatic Frequency Correction(AFC)
- Serial RF Control interface: support of direct conversion RF
- A Universal Serial Interface(USIF) enabling asynchronous (UART) or synchronous (SPI) serial data transmission
- 1 Serial Synchronous SPI compatible interfaces in the controller domain
- 1 Serial Synchronous SPI compatible interface in the TEAKLite domain
- 2 USART with autobaud detection, hardware flow control and integrated IrDA controller supporting IrDA's SIR standard (up to 115.2Kbps)
- A dedicated Fast IrDA Controller supporting IrDA's SIR, MIR and FIR standards (up to 4Mbps)
- I2C-bus interface (e.g. connection to S/M power)
- A fast display interface supporting serial and parallel interconnection
- An ITU-R BT.656 compatible Camera interface.
- Programmable clock output for a camera
- An multimedia/Secure Digital Card Interface (MMC/SD:SDIO capable)

3.2.3. External Devices connected to memory interface

Table 1 Memory interface

Device	Name	Maker	Remark
FLASH	PF38F5060M0Y0B0	Intel	Synchronous / Asynchronous
SDRAM	PF38F5060M0Y0B0	Intel	Synchronous 104MHz
LCD	IL220DBN1A	LGIT	8bit access 3times transmission
Melody IC	Not Used	S/W	Infineon Software CODEC

3.2.4. RF Interface (T_OUT)

S-Gold2 uses this interface to control RF IC and Peripherals. 13 signals are provided switch on/off RF ICs Periodically each TDMA frame.

Table 2 RF Interface Spec.

T_OUT		
Resource	Interconnection	Description
T_OUT0	TXON_PA	PAM Power on
T_OUT1	VIBRATOR_EN	VIBRATOR ON-
T_OUT2	PA_BAND	TX RF band select
T_OUT3	ANT_SW1	FEM control
T_OUT4	ANT_SW2	FEM control
T_OUT5	ANT_SW3	FEM control
T_OUT6	MODE	PAM Mode select

3.2.5. USART Interface

ME970 have two UART Drivers as follow :

- USART1 : Hardware Flow Control / SW upgrade / Calibration
- USART2 : SW debug trace.

Table 3 USART Interface Spec.

USART_0(USART1)		
Resource	Name	Remark
USART0_TXD	TXD_0	Transmit Data
USART0_RXD	RXD_0	Receive Data
USART0_CTS	CTS_0	Clear To Send
USART0_RTS	RTS_0	Request To Send
	DSR	N.C.
USART_1(USART2)		
USART1_TXD	TX_DEBUG	Trace data tx
USART1_RXD	RX_DEBUG	Trace data rx
USART1_CTS	N.C.	N.C.
USART1_RTS	N.C.	N.C.

3.2.6. ADC channel

BBP ADC block is composed of 7 external ADC channel . This block operates charging process and other related process by reading battery voltage and other analog values.

Table 4 S-Gold2 ADC channel usage

ADC channel		
Resource	Interconnection	Description
M0	BATT_TEMP	Battery temperature measure
M1	RF_TEMP	RF block temperature measure
M2	JACK_TYPE	Accessory type detect
M7	H/W VERSION	S-Gold2 H/W version detect
M8	VSUPPLY	Battery supply voltage measure
M9	I_MONITOR	Current consumption measure
M10	REMOTE_ADC	Remote control key detect

3. TECHNICAL BRIEF

3.2.7. GPIO map

Over a hundred allowable resources, ME970 is using as follows except dedicated to SIM and Memory. ME970 GPIO(General Purpose Input/Output) Map, describing application, I/O state, and enable level, is shown in below table.

Table 5 S-Gold2 GPIO pin Map

Port function	KE260 Net Name	Description
KEY MATRIX		
KP_IN0	KP_IN0	Refer to Key Matrix
KP_IN1	KP_IN1	Refer to Key Matrix
KP_IN2	KP_IN2	Refer to Key Matrix
KP_IN3	KP_IN3	Refer to Key Matrix
KP_IN4	KP_IN4	Refer to Key Matrix
KP_IN5	KP_IN5	Refer to Key Matrix
KP_OUT5	KP_OUT5	Refer to Key Matrix
KP_OUT0	KP_OUT0	Refer to Key Matrix
KP_OUT1	KP_OUT1	Refer to Key Matrix
KP_OUT2	KP_OUT2	Refer to Key Matrix
KP_OUT3	KP_OUT3	Refer to Key Matrix
USART_0		
USART0_RXD	RXD_0	UART0, RS232 Data
USART0_TXD	TXD_0	UART0, RS232 Data
USART0_RTS_N	CTS_0	UART0, RS232 RTS
USART0_CTS_N	RTS_0	UART0, RS232 CTS
CC1CC6IO	FM_INT	For FM Radio Interrupt
USART_1		
USART1_RXD	TX_DEBUG	For debugging
USART1_TXD	RX_DEBUG	For debugging
USART1_RTS_N	Not Use	
USART1_CTS_N	Not Use	
USB		
USB_DPLUS	USB_DP	USB data
USB_DMINUS	USB_DM	USB data

3. TECHNICAL BRIEF

MEMORY & CLK		
GPIO_20	F_DPD	For INTEL Memory
CLK32K	CLK32K	For FM Radio & BLUETOOTH
GPIO_22	Not Use	
CAMERA I/F		
CIF_D0	CIF_D(0)	Camera DATA[0]
CIF_D1	CIF_D(1)	Camera DATA[1]
CIF_D2	CIF_D(2)	Camera DATA[2]
CIF_D3	CIF_D(3)	Camera DATA[3]
CIF_D4	CIF_D(4)	Camera DATA[4]
CIF_D5	CIF_D(5)	Camera DATA[5]
CIF_D6	CIF_D(6)	Camera DATA[6]
CIF_D7	CIF_D(7)	Camera DATA[7]
CIF_PCLK	CIF_PCLK	Camera pixel clock
CIF_HSYNC	CIF_HS	Camera H sync
CIF_VSYNC	CIF_VS	Camera V sync
CLKOUT	CIF_MCLK	Camera main clock
CIF_PD	CIF_PD	Camera power down(active high)
CIF_RESET	CIF_RESET	Camera reset
LCD IF/		
DIF_D0	DIF_D(0)	LCD data[0]
DIF_D1	DIF_D(1)	LCD data[1]
DIF_D2	DIF_D(2)	LCD data[2]
DIF_D3	DIF_D(3)	LCD data[3]
DIF_D4	DIF_D(4)	LCD data[4]
DIF_D5	DIF_D(5)	LCD data[5]
DIF_D6	DIF_D(6)	LCD data[6]
DIF_D7	DIF_D(7)	LCD data[7]
DIF_CS1	DIF_CS	LCD chip select
GPIO_96	FM_BBP_SEL	Audio amp inuput select(High: FM sound, Low: BBP sound)
DIF_CD	DIF_CD	Command Data switch
DIF_WR	MM_WR	LCD Write
DIF_RD	MM_RD	LCD Read

3. TECHNICAL BRIEF

GPIO_99	_USB_CHG_EN	USB charging (High: charge disable, Low: enable)
DIF_VD (in)	_TF_PWR_EN	Trans-Flash card power enable(active low)
DIF_RESET1_GPIO	DIF_RESET1	LCD Reset
EINT6	REMOTE_INT	For Remote Control Headset
I2c		
I2C_SCL	SCL	For SM-Power, FM Radio, Audio AMP
I2C_SDA	SDA	"
PM_INT (EINT)	PM_INT	SM-Power interrupt
SIM CARD		
CC_IO	SIM_IO	SIM CARD I/O
CC_CLK	SIM_CLK	SIM CARD CLOCK
CC_RST	SIM_RST	SIM CARD RESET
I2S		
I2S2_CLK0	Not Use	
GPIO_102	_WP	Not Connected
I2S2_RX	Not Use	
I2S2_TX	Not Use	
I2S2_WA0	Not Use	
I2S2_WA1	Not Use	
EXTERNAL MEMORY		
MMCI_CMD	TF_CMD	For T-Flash
MMCI_DAT[0]	TF_DAT0	"
MMCI_CLK	TF_CLK	"
BT I/F		
USIF_TXD_MTSR	USIF_TXD	For Bluetooth
USIF_RXD_MRST	USIF_RXD	"
GPIO_109	_USB_EOC	USB End of charging detect(High: EOC, Low: charging)
GPIO_110	RPWRON	Remote power on detect (High: Remote , Low: Normal
GPIO_111	SPK_RCV_SEL	Audio pass select(high: Speaker, Low: Receiver)
I2S		
I2S1_CLK0	I2S1_CLK	For Bluetooth
GPTU0_0	FLASH_EN	For Camera Flash LED

3. TECHNICAL BRIEF

I2S1_RX	I2S1_RX	For Bluetooth
I2S1_TX	I2S1_TX	"
I2S1_WA0	I2S1_WA0	"
MMC		
MMCI_DAT[1]	TF_DAT1	For T-Flash
MMCI_DAT[2]	TF_DAT2	"
MMCI_DAT[3]	TF_DAT3	"
AUDIO I/F		
EPN1	RCV_N	For Receiver
EPP1	RCV_P	"
EPPA1	BBP_SND_L	For Speaker
EPPA2	BBP_SND_R	For Speaker
MICN1	MIC1_N	For Mic
MICP1	MIC1_P	"
MICN2	MIC2_N	For Headset Mic
MICP2	MIC2_P	"
VMICP	VMICP	For Mic
VMICN	VMICN	"
RF I/F		
PAOUT1		
PAOUT2		
BB_I		
BB_IX		
BB_Q		
BB_QX		
ADC		
M_0	BAT_TEMP	Battery temperature detect
M_1	RF_TEMP	RF Power amp reference temperature detect
M_2	JACK_TYPE	For 18Pin Cable Type Detect
M_7		HW revision indication
M_8		Battery voltage measurement
M_9	I_MONITOR	Current consumption measurement
M_10	REMOTE_ADC	For Remote Control Headset Key detect with REMOTE_INT
Reference		
VREF		
IREF		
JTAG		
TDO	TDO	For JTAG & ETM Interface
TDI	TDI	"
TMS	TMS	"
TCK	TCK	"
TRST_n	TRSTn	"
RTCK	RTCK	"

3. TECHNICAL BRIEF

ETM		
TRIG_IN	TRIG_IN	"
MON1	MON1	"
MON2	MON2	"
TRACESYNC	TRACESYNC	"
TRACECLK	TRACECLK	"
PIPESTAT[2]	PIPESTAT[2]	"
PIPESTAT[1]	PIPESTAT[1]	"
PIPESTAT[0]	PIPESTAT[0]	"
TRACEPKT[0]	TRACEPKT[0]	"
TRACEPKT[1]	TRACEPKT[1]	"
TRACEPKT[2]	TRACEPKT[2]	"
TRACEPKT[3]	TRACEPKT[3]	"
TRACEPKT[4]	TRACEPKT[4]	"
TRACEPKT[5]	TRACEPKT[5]	"
TRACEPKT[6]	TRACEPKT[6]	"
TRACEPKT[7]	TRACEPKT[7]	"
Memory		
EBU_AD[0]	D(0)	Data bus[0]
EBU_AD[1]	D(1)	Data bus[1]
EBU_AD[2]	D(2)	Data bus[2]
EBU_AD[3]	D(3)	Data bus[3]
EBU_AD[4]	D(4)	Data bus[4]
EBU_AD[5]	D(5)	Data bus[5]
EBU_AD[6]	D(6)	Data bus[6]
EBU_AD[7]	D(7)	Data bus[7]
EBU_AD[8]	D(8)	Data bus[8]
EBU_AD[9]	D(9)	Data bus[9]
EBU_AD[10]	D(10)	Data bus[10]
EBU_AD[11]	D(11)	Data bus[11]
EBU_AD[12]	D(12)	Data bus[12]
EBU_AD[13]	D(13)	Data bus[13]
EBU_AD[14]	D(14)	Data bus[14]
EBU_AD[15]	D(15)	Data bus[15]
EBU_WR_n	_WR	Write strobe

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EBU_RD_n	_RD	Read strobe
EBU_BC0_n	_BC0	
EBU_BC1_n	_BC1	
EBU_A[0]	A(0)	Address bus[0]
EBU_A[1]	A(1)	Address bus[1]
EBU_A[2]	A(2)	Address bus[2]
EBU_A[3]	A(3)	Address bus[3]
EBU_A[4]	A(4)	Address bus[4]
EBU_A[5]	A(5)	Address bus[5]
EBU_A[6]	A(6)	Address bus[6]
EBU_A[7]	A(7)	Address bus[7]
EBU_A[8]	A(8)	Address bus[8]
EBU_A[9]	A(9)	Address bus[9]
EBU_A[10]	A(10)	Address bus[10]
EBU_A[11]	A(11)	Address bus[11]
EBU_A[12]	A(12)	Address bus[12]
EBU_A[13]	A(13)	Address bus[13]
EBU_A[14]	A(14)	Address bus[14]
EBU_A[15]	A(15)	Address bus[15]
EBU_A[16]	A(16)	Address bus[16]
EBU_A[17]	A(17)	Address bus[17]
EBU_A[18]	A(18)	Address bus[18]
EBU_A[19]	A(19)	Address bus[19]
EBU_A[20]	A(20)	Address bus[20]
EBU_A[21]	A(21)	Address bus[21]
EBU_A[22]	A(22)	Address bus[22]
EBU_A[23]	A(23)	Address bus[23]
EBU_A[24]	A(24)	Address bus[24]
EBU_CS0_n	_FLASH1_CS	Flash ROM chip select
EBU_CS1_n	_RAM_CS	SDRAM Chip select
EBU_CS2_n	_FLASH2_CS	Not used
EBU_CS3_n	_CS3	Not used
EBU_ADV_n	_ADV	
EBU_RAS_n	_RAS	
EBU_CAS_n	_CAS	

3. TECHNICAL BRIEF

EBU_WAIT_n	_WAIT	
EBU_SDCLKO	SDCLKO	
EBU_SDCLKI	SDCLKI	
EBU_BFCLKO	BFCLKO	
EBU_BFCLKI	BFCLKI	
EBU_CKE	CKE	
Memory		
FCDP_RBn	F_DPD	
TDMA RF I/F		
T_OUT0	TXON_PA	RF Power amp turn on
GPIO_44	VIBRATOR_EN	Vibrator enable(High: enable, Low:disable)
T_OUT2	PA_BAND	RF band select
T_OUT3	ANT_SW1	RF FEM control signal 1
T_OUT4	ANT_SW2	RF FEM control signal 2
EINT3	ANT_SW3	RF FEM control signal 3
T_OUT6	MODE	For RF
GPIO_50	KP_OUT(4)	Key pad
EINT7	JACK_DETECT	Headset Detect(High: unplugged, Low: plugged)
CC1CC3IO	LCD BACKLIGHT	LCD Backlight Control
GPIO_53	LCD ID	Neodis : L
GPIO_54	_FM_RESET	FM Radio chip reset
GPIO_55	AU_PWR_EN	Audio amp power enable(active high)
RF I/F		
RF_STR0	EN	RF Transceiver chip enable
GPIO_57	TF_DETECT	Micro SD card detect (High: inserted, Low: ejected)
RF_DATA	DA	RF Transceiver chip data
RF_CLK	CLK	RF Transceiver chip clock
System port		
AFC	AFC	Automatic Frequency control DAC output for 26MHz VCTCXO
CLKOUT0 [<=26MHz]	Not Use	
F26M	26MHZ_MCLK	Baseband processor PLL input Main clock

3. TECHNICAL BRIEF

F32K		Sleep crystal 32.768KHz
OSC32K		Sleep crystal 32.768KHz
RESET_n	_RESET	Baseband processor reset
CC1CC1IO	TRIG_OUT	For JTAG & ETM Interface
RTC_OUT	RTC_OUT	Wake up signal to alarm (High; wake up, Low: Power off)
VCXO_EN	VCXO_EN	26MHz clock enable
DSP		
DSPIN0	_BT_RESET	Bluetooth chip reset
GPIO_62	MIC_GAIN_SEL	Microphone gain select (High: 12dB, Low: 0dB)
GPIO_63	_SIM_EN	SIM card power enable

3. TECHNICAL BRIEF

3.3. Power management IC

3.3.1. General Description

SM-POWER is a highly integrated Power and Battery Management IC for mobile handsets. It has been specially designed for usage with S-Gold2. Although optimized for usage with the Infineon S-GOLD baseband device it is suitable for the S-GOLDlite and the E-GOLD+ baseband devices as well. It also supports the cellular RF devices like SMARTi-DC, SMARTi-DC+, SMARTi-SD and the Bluemoon Single, Infineon's single chip solution for Bluetooth. If used with S-GOLD2 it provides all power supply functions (except for the RF PA) for a complete advanced GSM Edge smart phone minimizing external device count.

Block Description

- Highly efficient step-down converter for main digital baseband supply including Core, DSP and memory interface (External Bus Unit).
- Support of S-GOLD standby power-down concept
- Low-drop-out (LDO) regulators for Flash and mobile RAM memory devices
- Voltage independent switching of two SIM cards
- LDO regulators for baseband I/O supply
- LDO regulator for analog mixed-signal section of S-GOLD
- Low-noise LDO regulators for RF devices
- Supply for Bluemoon Single, Infineon's single chip solution for Bluetooth
- Audio amplifier 8 Ohms for handsfree operation and ringing
- Charge Control for charging Li-Ion/Polymer batteries under software control
- Pre-charge current generator with selectable current level
- RTC regulator with ultra-low quiescent current
- USB interface support for peripheral and mini-host mode
- Backlight LEDs driver with current selection and PWM dimming function
- Two single LED driver outputs for signaling
- Vibrator driver with adjustable voltage
- Fully controllable by software via I2C - Bus
- Temperature and battery voltage sensors
- Interrupt channels for peripherals
- System debug mode
- VQFN 48 package with heat sink and non-protruding leads
- Compatible with the Infineon E-GOLD+ V2 and V3



- SM-POWER's DC/DC converter makes up to 40 % reduction of battery current for smart phone functions (e.g. organizer functions, games, MP3 decoding) possible.
- SDBB has high efficiency up to 95% and also a power save mode.
- Memory Interface is directly supported by the SDBB
- SDBB can also act as main supply voltage for E-GOLD+ or S-GOLDlite baseband devices.
- For S-GOLD two linear regulators for DSP and Core are cascaded after the SDBB.

3. TECHNICAL BRIEF

SM-POWER supports the standby power-down concept of S-GOLD by temporarily switching off the linear regulator LBB1 for the DSP during mobile standby whenever this subsystem is not used. In this phase the ARM controller and most peripherals including parts of the on-chip SRAM are kept powered-up with power being supplied by the other linear regulator LBB2.

SM-POWER includes a fully differential audio amplifier able to drive loads down to a nominal value of 8 Ohm for usage in hands-free phones and for ringing

- 400 mW maximum output power
- adjustable gain
- mute switch
- click and pop - protection

SM-POWER also integrates a charging function for Li-Ion, Li-Polymer batteries

- Precharge current source with two current levels
- Constant current / constant voltage charging with 3 different termination voltages
- Programable charge current limitation for use with different batteries
- Freely programable pulse charging to reduce the thermal power dissipation in the constant voltage charging phase
- Top-off charge current sensing

SM-POWER completes the USB interface of S-GOLD

- Regulated voltage for S-GOLD USB interface including reverse current and overvoltage protection
- Switch to supply USB pull-up resistor
- Mini-host pull down resistor functionality
- Charge pump with internal switching capacitor for USB host VBUS supply voltage

SM-POWER fully supports LED and Vibra Motor functionality

- no external components needed
- driver for backlight LEDs adjustable in steps up to 140mA and with soft turn on and off by PWM dimming
- two driver outputs for single LEDs for precharge indication and signaling with i.e. change of colour
- driver for Vibra Motor with adjustable voltages, soft startup / shutdown and current limitation

SM-POWER offers several control functions

- Power-on Reset Generator with logic state machine
- I2C bus interface
- I2C bus configurable mode control logic with ON (push-button or RTC), VCXOEN and LRF3EN (wake-up by Bluetooth) inputs
- Programable interrupt channels to handle peripherals like SIM, MMC and USB
- Monitoring of charging functions
- Undervoltage Shut-Down
- Errorflags (volatile or non-volatile) from many power-supply functions and thermal sensor in order to debug system
- Overtemperature Shut-Down
- Overtemperature Warning
- Support of S-GOLD standby power-down concept
- Support of S-GOLD Power-Down Pad Tristate Function

3. TECHNICAL BRIEF

Table 6 LDO Output Table of SM-Power

LDO	Net name	Output Voltage	Output Current	Usage
SDBB	1V8_MEM	1.8V	850mA	Memory & for LDO
LRFC	1V5_RF	1.5V	120mA	RF transceiver
LBB1	1V5_DSP	1.5V	170mA	DSP in BBP
LBB2	1V5_CORE	1.5V	300mA	ARM core in BBP
LINT	2V72_IO	2.72V	135mA	Peripherals
LSIM	2V85_SIM	2.85V	22mA	SIM card
LSIM2	2V85_IO2	2.85V	200mA	Peripherals
LMMC	2V85_CARD	2.85V	135mA	SD card
LANA	2V65_ANA	2.65V	220mA	Analog block in BBP
LRTC	2V11_RTC	2.11V	0.3mA	RTC block & Backup battery
LRF1	2V85_RF	2.85V	250mA	RF IC
LRF2	2V7_RF	2.7V	10mA	RF IC
LRF3	2V65_BT	2.65V	150mA	BT IC(Blue moon)
USB	3V1_USB	3.1V	45mA	USB I/F

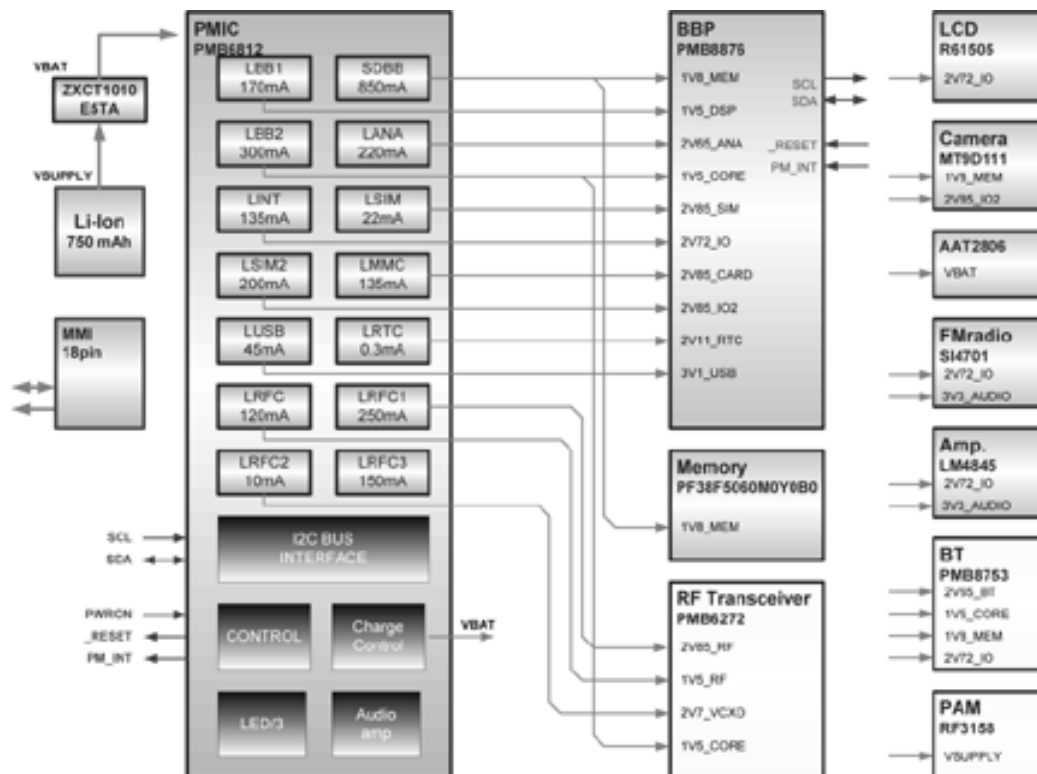


Figure 4 Power domain block diagram of ME970

3. TECHNICAL BRIEF

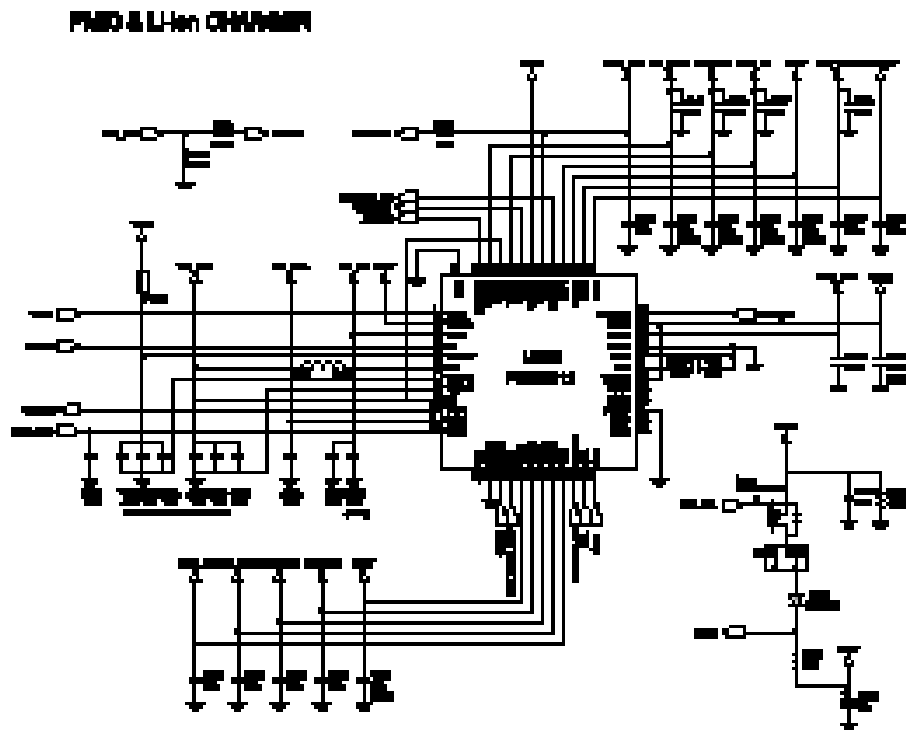


Figure 5 SM-Power circuit diagram with charging part

3.3.2. Charging

SM-POWER provides together with an external p-channel FET Siliconix Si3455 an external AC-adaptor a complete charge control function for charging of Li-Ion or Li-Ion-Polymer batteries. Either a 1-cell Li-Ion or Li-Ion-Polymer battery with 4.1, 4.2 or 4.4 Volts may be used.

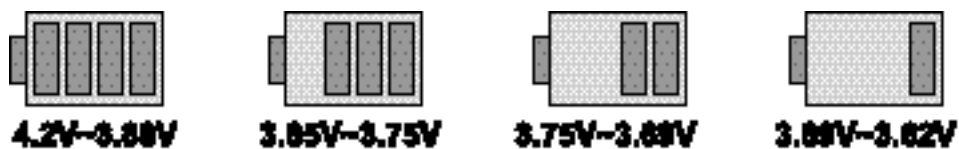


Figure 6 Battery Block Indication

1. Charging method : CC-CV
2. Charger detect voltage : 4.0V
3. Charging time : 3h
4. Charging current : 500mA
5. CV voltage : 4.2V
6. Cutoff current : 100mA
7. Full charge indication current (icon stop current) : 100mA

- ## .4. Power ON/OFF

- ▶ Power-ON : Power key detect (SM-Power's ON port
- ▶ Power-ON-charging : Charger detect.
- ▶ Power-ON-remote : remote power on detect (Factory use only)



3. TECHNICAL BRIEF

Input ON is a power-on input for SM-POWER with 2 active high levels (see Figure 8). It might be triggered by a push button or by the RTCOUT output of the S-GOLD device as well. To detect if the push-button is pressed during system operation the logical level at pin ON or its change (if Bit 1 EION in INTCTRL2 is asserted) is recorded in bit LON of the ISF register. If the high level of voltage at pin ON does not reach VIHdet ($V_{bat}-0.8 \sim V_{bat}-0.3$) the above-mentioned bit won't be set.

To support Remote power on function for factory mass production, applied an analog switch as following figure. As monitoring the RPWRON(GPIO_110) and Key matrix KP_OUT(1) & KP_IN(5), ME970 system recognize whether remote power on or End-key pushed



Figure8 Remote power on and End-key power on circuit

3.5. SIM interface

ME970 supports 3V plug in SIM, SIM interface scheme is shown in (Figure 10).

SIM_IO, SIM_CLK, SIM_RST ports are used to communicate with BBP(S-Gold2) and the SIM power supply enabled by BBP (_SIM_EN).

SIM Interface

SIM_CLK : SIM card reference clock

SIM_RST : SIM card Async /sync reset

SIM_IO : SIM card bidirectional reset

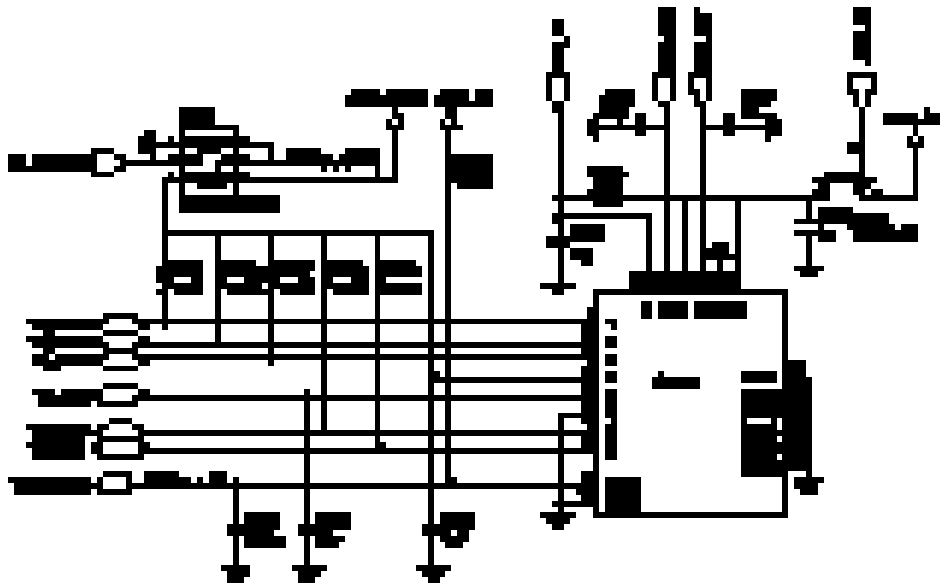


Figure 9 SIM CARD Interface

3. TECHNICAL BRIEF

3.6. Memory

512Mbit Flash & 256Mbit SDRAM employed on ME970 with 16 bit parallel data bus thru ADD(0) ~ ADD(24). The 512Mbit Sibley Wireless Flash memory with LPSPDRAM stacked device family offers multiple high-performance solutions. The Sibley flash die is manufactured on 90 nm process technology.

It delivers 108 MHz synchronous burst and page-mode read rates with supports multi-partitioning with Read-While-Write (RWW) or Read-While-Erase (RWE) dual operations. The LPSPDRAM is a high-performance volatile memory operating at speeds up to 104 MHz with configurable burst lengths.

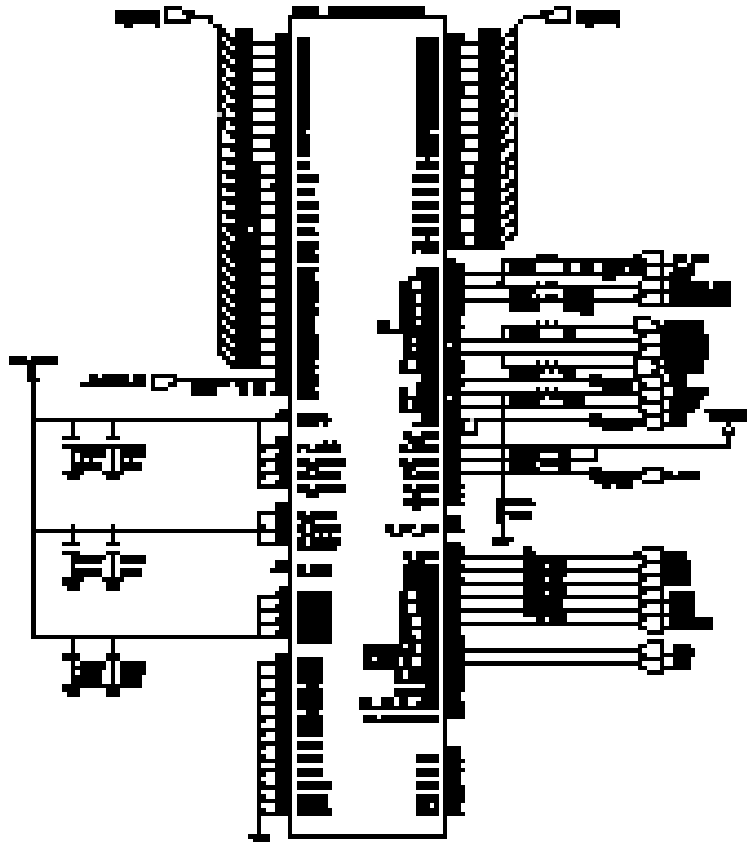


Figure 10 Flash memory & SDRAM MCP circuit diagram

3.7. LCD Display

LCD module include:

- LCD : 320*240 265K Color TFT LCD
- Backlight : 5 piece of white LED illumination

LCD module is connected to main board thru 35 pins connector.

LCD FPC Interface Spec:

Table 7 LCD FPC Interface Spec.

Pin No.	Pin Name	I/O	Description
1	VCC(2.8V)	Power	LCD power supply
2	VDDIO(2.8V)	Power	LCD power supply
3	GND	Power	LCD power supply
4	MAKER_ID	O	LCD maker Identification
5	D0	I/O	Data[0] for LCD
6	D1	I/O	Data[1] for LCD
7	D2	I/O	Data[2] for LCD
8	D3	I/O	Data[3] for LCD
9	D4	I/O	Data[4] for LCD
10	D5	I/O	Data[5] for LCD
11	D6	I/O	Data[6] for LCD
12	D7	I/O	Data[7] for LCD
13	D8	I/O	Data[8] for LCD
14	D9	I/O	Data[9] for LCD
15	D10	I/O	Data[10] for LCD
16	D11	I/O	Data[11] for LCD
17	D12	I/O	Data[12] for LCD
18	D13	I/O	Data[13] for LCD
19	D14	I/O	Data[14] for LCD
20	D15	I/O	Data[15] for LCD
21	GND	-	Ground
22	/RESET	I	Reset
23	/WR	I	Write strobe
24	/RD	I	Read strobe
25	/CS	I	LCD chip select
26	RS	I	Command / Data switch
27	IFMODE	I	8bit / 16bit switch
28	GND	-	Ground
29	VSYNC_OUT	I	NA
30	MLED_Cathod5	O	Back light LED Cathode
31	MLED_Cathod4	O	Back light LED Cathode
32	MLED_Cathod3	O	Back light LED Cathode
33	MLED_Cathod2	O	Back light LED Cathode
34	MLED_Cathod1	O	Back light LED Cathode
35	MLED_Anode	I	Back light LED Anode

3. TECHNICAL BRIEF

3.8. Keypad Switching & Scanning

The keypad interface is a peripheral which can be used for scanning keypads up to 6 rows (outputs from Port Control Logic) and 6 columns (inputs to PCL). The number of rows and columns depend on settings of the PCL.

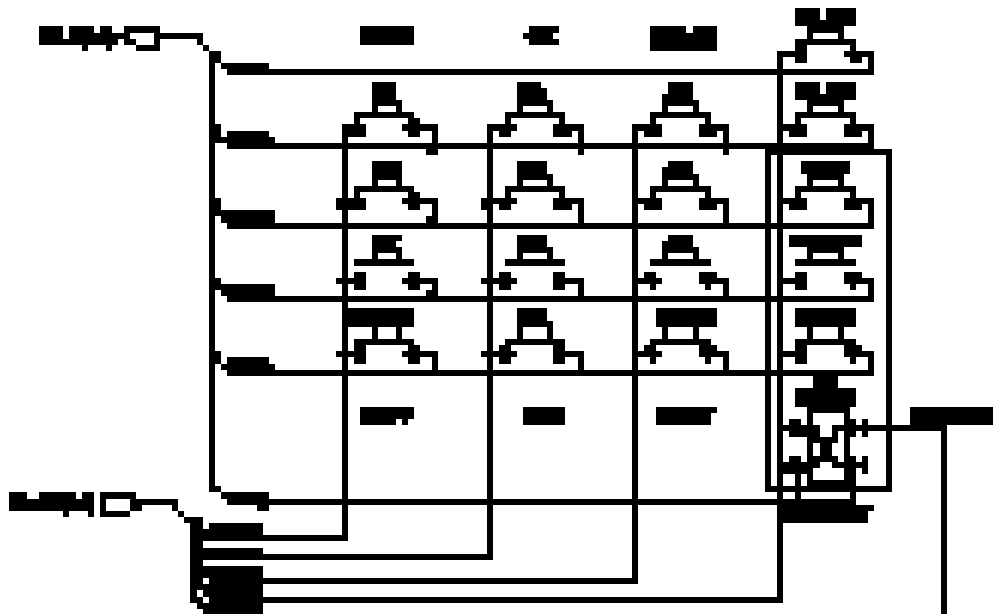
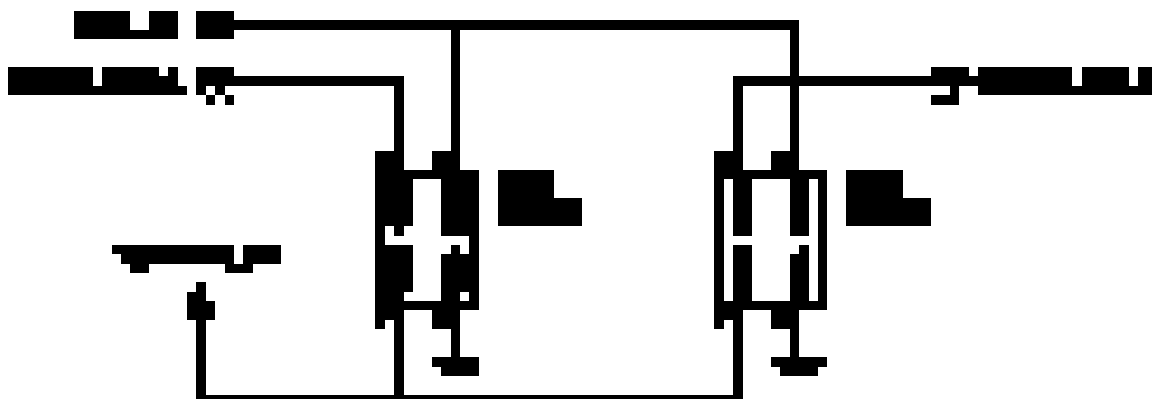


Figure 11 KEY FPCB part numeric key matrix



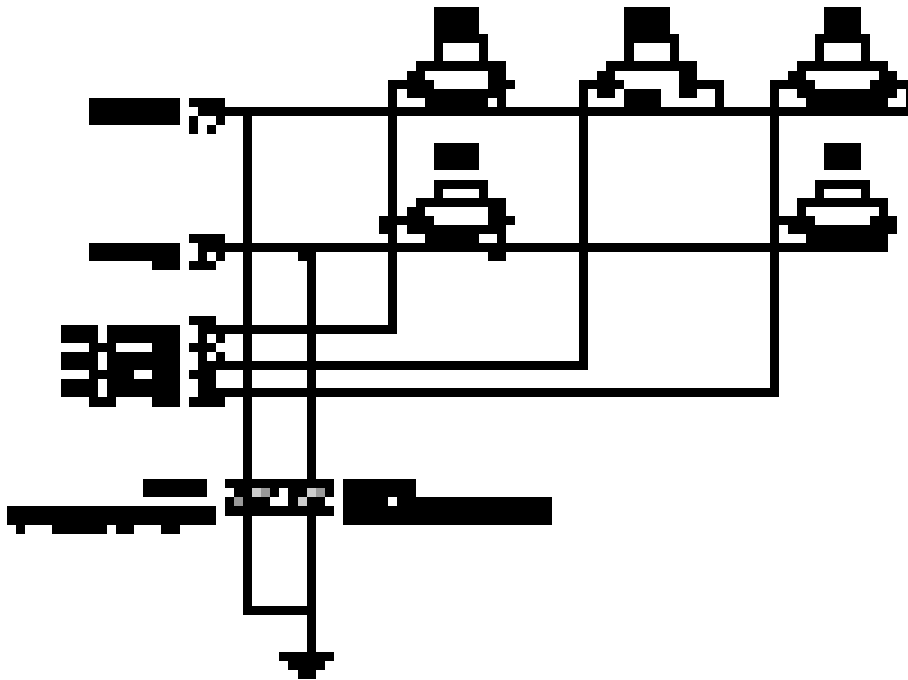


Figure 12 LCD PCB part Navi & Scroll key matrix

Most of numeric keys are located on the Keypad FPCB, Scroll key for menu navigation is on the LCD FPCB, and Power on (End key), BGM hot key, Camera shutter and volume up & down keys are connected via 70pin board to board connector between main PCB and Keypad FPCB.

3. TECHNICAL BRIEF

3.9. Keypad back-light illumination

There are 2 snow white color LEDs on the KEY FPCB for keypad illumination. Keypad Back-light is controlled by SM-Power LED port which has constant current control function.

The whole configuration of the SM-POWER LED drivers is shown in below Figure16.

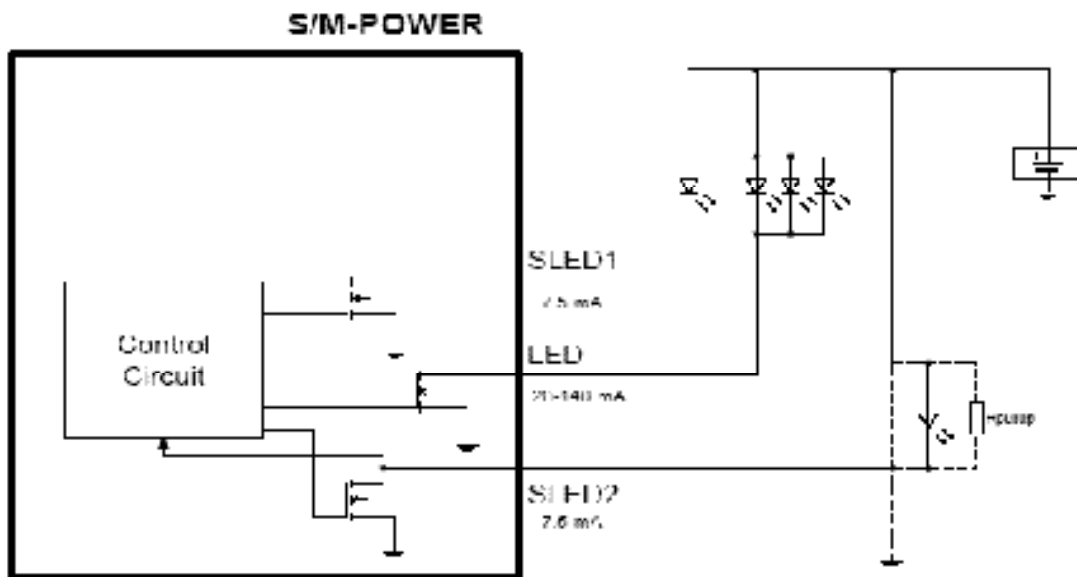


Figure 13 Keypad Back-light LEDs

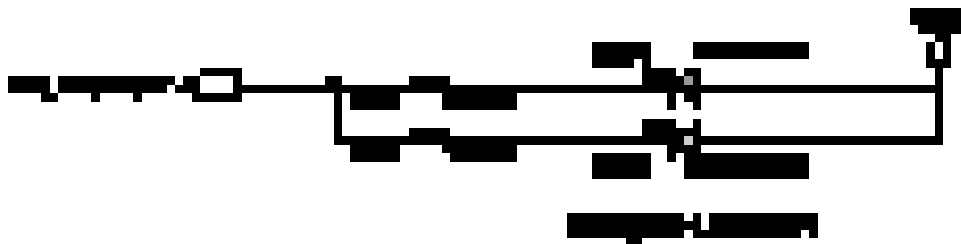


Figure 14 Keypad Back-light LEDs

3.10. LCD back light illumination

The MAX8645Y charge pump drives up to 6 white LEDs in the main display for backlighting and up to 2 white LEDs for flash, all with regulated constant current for uniform intensity. By utilizing adaptive 1x/1.5x/2x charge pump modes and very-low-dropout current regulators, it achieves high efficiency over the 1-cell lithium-battery input voltage range. 1MHz fixed-frequency switching allows for tiny external components and low input ripple. Two on-board 200mA programmable output voltage LDOs are provided to meet camera module requirements.

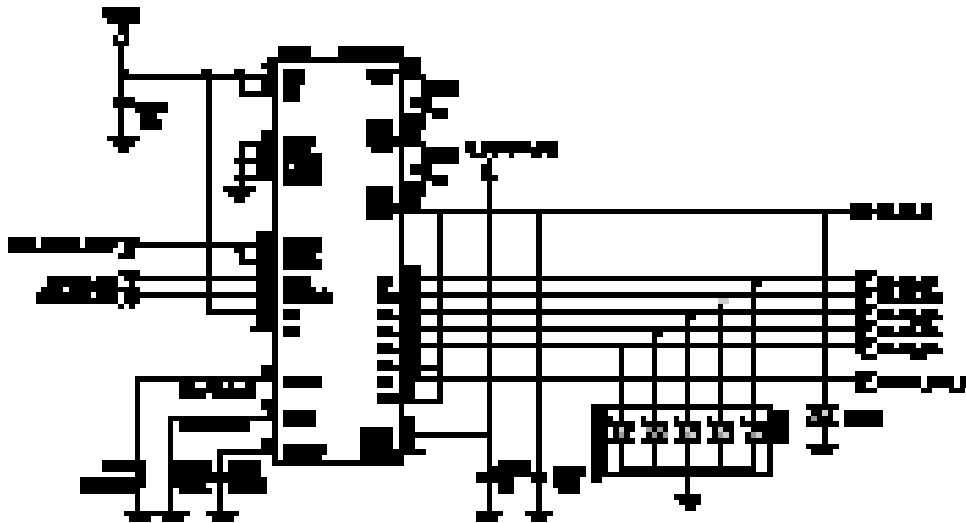


Figure 15 LCD Back light unit and Flash LED charge pump IC

For more dimming flexibility or to reduce the number of control traces, the MAX8645Y supports serial pulse dimming. Connect ENM1 and ENM2 together to enable single-wire pulse dimming of the main LEDs (or ENF only for single-wire pulse dimming of the Flash LEDs). When ENM1 and ENM2 (or ENF) go high simultaneously, the main (or flash) LEDs are enabled at full brightness. Each subsequent low-going pulse (500ns to 250 μ s pulse width) reduces the LED current by 3.125% (1/32), so after one pulse the LED current is 96.9% (or 31/32) \times I_{LED} . The 31st pulse reduces the current to 0.03125 \times I_{LED} . The 32nd pulse sets the LED current back to I_{LED} . Figure 1 shows a timing diagram for single-wire pulse dimming.

Because soft-start is longer than the initial t_{HI}, apply dimming pulses quickly upon startup (after initial t_{HI}) to avoid LED current transitioning through full brightness.

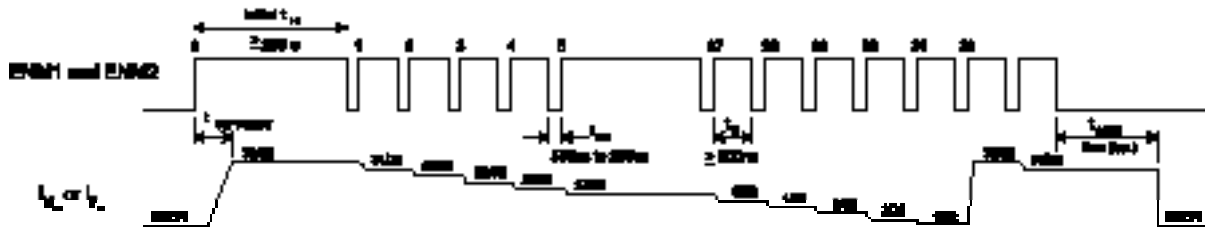


Figure 16 Seiral pulse dimming timing diagram

Setting the Main Output Current

SETM controls M1-M6 regulation current. Current flowing into M1, M2, M3, M4, M5, and M6 is a multiple of the current flowing out of SETM.

$$I_{M1}=I_{M2}=I_{M3}=I_{M4}=I_{M5}=I_{M6} = K * (0.6V / R_{SETM}) = 18.4mA$$

where $K = 230$, $R_{SETM} = 7500$

where $K = 23, 69$, or 230 (depending upon the state of ENM1 and ENM2, see Table 8), and R_{SETM} is the resistor connected between SETM and GND (see the Typical Operating Circuit).

Table 8. ENM1/ENM2 current setting table

ENM1/ENM2 STATES	BRIGHTNESS	M1 - M6 CURRENT
ENM1 = low, ENM2 = low	Shutdown	0
ENM1 = low, ENM2 = high	1/10 Brightness	23 x I _{SETM}
ENM1 = high, ENM2 = low	3/10 Brightness	69 x I _{SETM}
ENM1 = high, ENM2 = high	Full Brightness	230 x I _{SETM}

Setting the Flash Output Current

SETF controls the F1-F2 regulation current. Current flowing into F1 and F2 is a multiple of the current flowing out of SETF.

$$I_{F1}=I_{F2} = N * (0.6V / R_{SETF}) = 162mA$$

where $N = 1380$, $R_{\text{SETF}} = 5100$

3.11 Battery current consumption monitor

ME970 use a current monitoring function to calculate the battery capacity and the remaining time, as monitoring current flow from the battery thru 47mohm resistor.

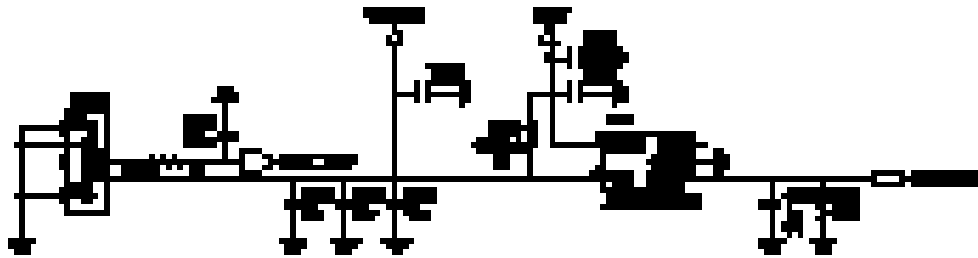


Figure 17 Current monitor circuit

3.12 JTAG & ETM interface connector

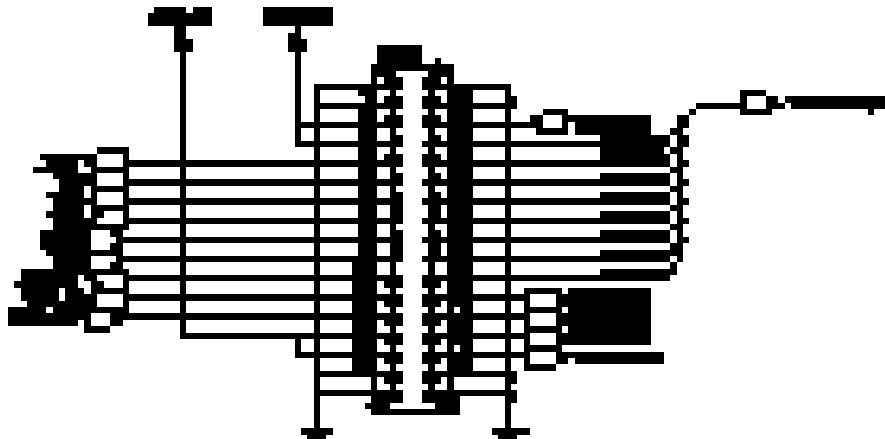


Figure 18 JTAG & ETM(Embedded Trace Module) interface connector

In case of ME970 mass production, the JTAG & ETM interface connector will not be mount on board. That is only for developing and software debugging purpose.

3. TECHNICAL BRIEF

3.13. Audio

ME970 Audio signal flow diagram as following diagram.

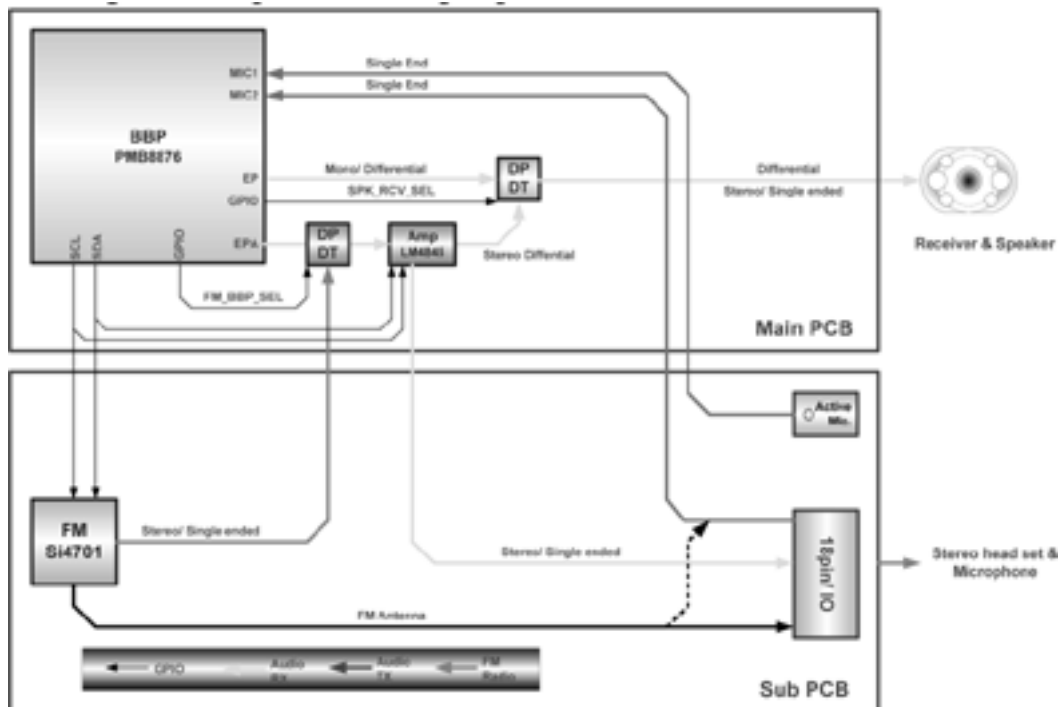


Figure 19 Audio signal flow diagram

3.13.1. Audio amplifier sub system IC with 3D effect

Audio amplifier sub system IC is an audio power amplifier capable of delivering 500mW of continuous average power into a mono 8 Ω load, 25mW per channel of continuous average power into stereo 32 Ω single-ended (SE) loads. The LM4845 features a 32-step digital volume control and eight distinct output modes. The digital volume control, 3D enhancement, and output modes (mono/SE/OCL) are programmed through a two-wire I2C interface that allows flexibility in routing and mixing audio channels.

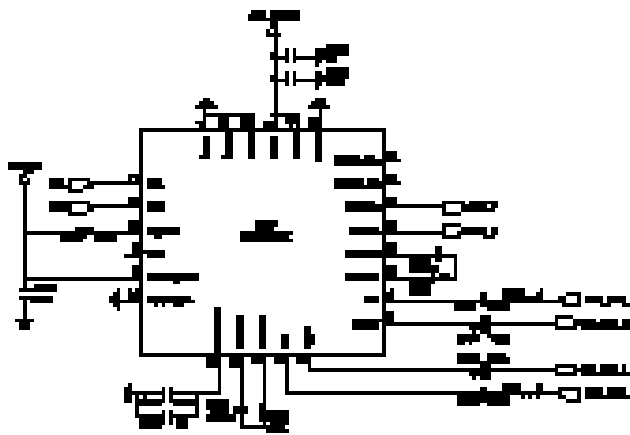


Figure 20 Audio amplifier Sub-system IC

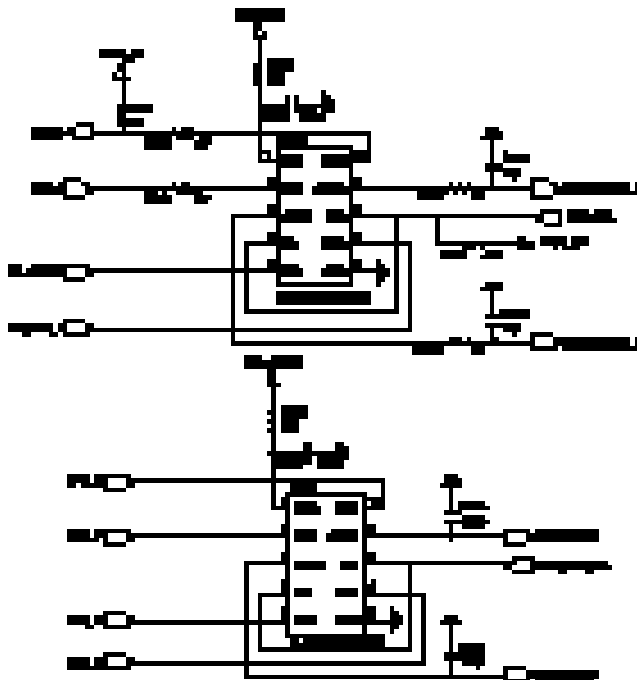


Figure 21 Audio signal distribute analog switch

3. TECHNICAL BRIEF

3.13.2. Microphone with gain switching circuit

When a call is established, MICBIAS signal goes up to '2.5V' in the MME970. PMB8876(S-Gold2) provides both 2.0V and 2.5V for MICBIAS to circuit designer. VA01, VA02 are employed to enhance ESD immunity.

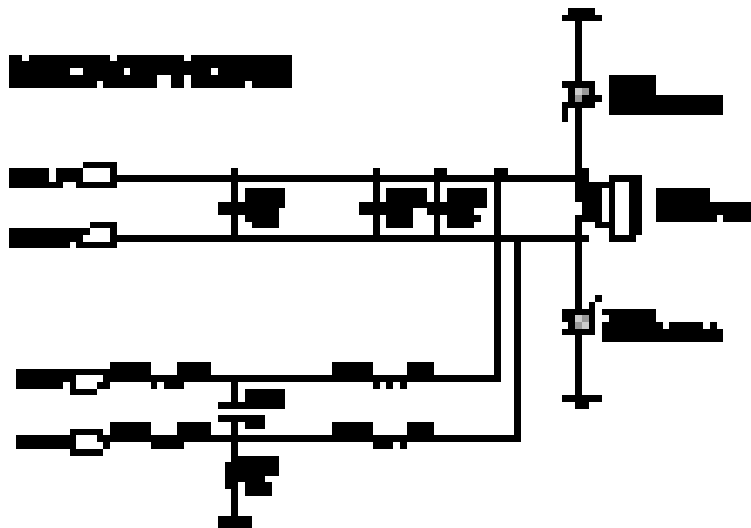


Figure 22 Microphone circuit

3.14. Multi port switch

Multi port switch has employed to decrease MMI(Multi Media Interface) connector's pin number. USB, USART, Remote controlled Headset is connected via this multi port switch. When USB VBUS voltage is detected Multi port 0 and 1 is connected to USB_DP and USB_DM each. If the remote controlled headset is plugged into MMI connector, then multi port 0 and 1 in go through REMOTE_INT and REMOTE_ADC.

Table 9 Multi port switch truth table

	VBUS_USB='L'	VBUS_USB='L'	VBUS_USB='H'
	JACK_DETECT='L'	JACK_DETECT='H'	
Pin6	REMOTE_INT	TXD	USB_DP
Pin7	REMOTE_ADC	RXD	USB_DM

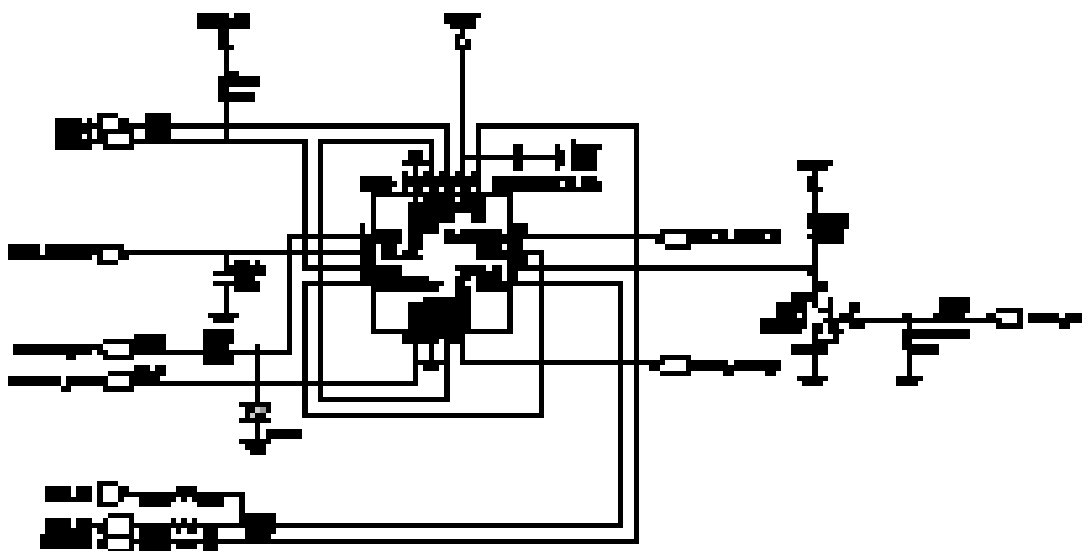


Figure 23 Multi port switch 1

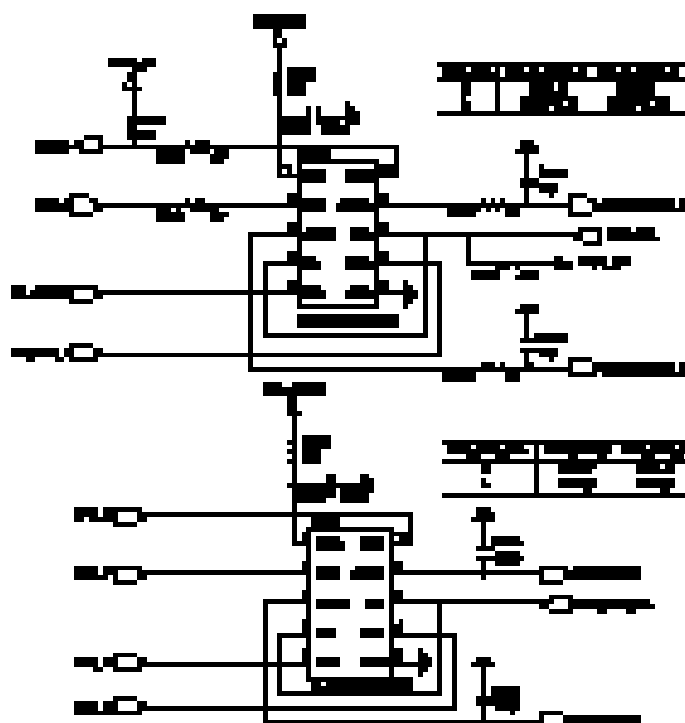


Figure 24 Multi port switch 2

3. TECHNICAL BRIEF

3.15. USB charging circuit

The USB charging circuit is a fully integrated USB VBUS voltage single-cell Li-ion battery charger circuit.

The charger uses a CC/CV charge profile required by Li-ion batteries. CC charging current and End of charging current is programmable I_{REF} & I_{MIN} resistors. I_{REF} resistor between this pin and the GND pin to set the charge current limit determined by the following equation:

$$I_{CC} = 12089/33K = 366mA$$

The End Of Charging current is set by I_{MIN} That can be programmed by the as following equation:

$$I_{EOC} = 11000/220K = 50mA$$

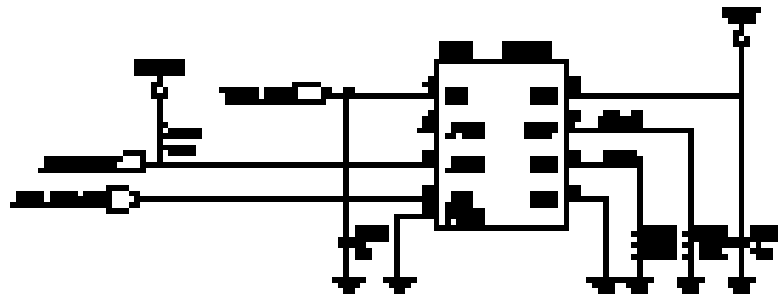


Figure 25 USB charging circuit

Charging indicator LED controlled by two ICs, one is a SM-Power, the other is a ISL6294. When TA(Travel Charger) is plugged in to MMI connector, the LD100 controlled by SM-Power both power off case and power on case. When USB cable is connected via MMI connector, indicator LED is controlled by ISL6294 in power off case and by SM-Power in power on case.

3. TECHNICAL BRIEF

3.16.1. General Features

- Single Chip Bluetooth device for cellular applications integrating radio, baseband and memory
- Fabricated in advanced low power 0.13µm CMOS technology
- Very low component count (6 external components)
- Ultra low power design
 - Peak current 40mA for basic data rate
 - Peak current 45mA for enhanced data rate
 - Bluetooth low power mode typ. 25µA
- Multiple input clock signals supported (10-40MHz)
- Supply from external voltage regulator 1.8V..3.6V 1)
- Autonomous power down scenarios of Bluetooth and cellular system supported
- Packages:
 - P-VQFN-48 package
 - P-WFLGA-56 package
- Temperature range from -40°C up to 85°C
- Boundary scan for interface lines via JTAG

3.16.2 Micro-Controller-Section

- ARM7TDMI-STMicro ARM® Processor for protocol and application software
- Timers + Watchdog + Interrupt Module

3.16.3 Micro-Controller Memory

- 32 KByte RAM
- 256 KByte read only Program Memory
- 8 KByte Patch RAM

3.16.4 Interfaces

- UART (Bluetooth - Interface, support for HCI UART and Three-Wire UART transport layers with/without hardware handshaking) up to 3.25Mbaud
- Two channel PCM Audio interface with I2S mode
- I2C Interface
- Three channel full duplex CVSD trans coder
- General Purpose I/Os
 - External interrupt
 - Port output levels available during low-power mode (VDD supplied)
- Separate voltage domains for GPIO, UART and PCM interfaces
- Control signal for requesting external (cellular) system clock
- Multi frequency (e.g. 32.768 kHz) low power clock input

3.16.5. RF-Section

- Integrated antenna switch to minimize external components count
- Programmable RF transmit power between -55dBm...+6dBm
 - Fine tuning in 2dB programmable steps also supported
- 20dBm power class 1 supported with external power amplifier
 - Separate TX output interface to PA (bypass of internal T/R switch)
 - Digital power step control
- Receiver sensitivity typ. -90dBm
- High performance integrated LNA with excellent blocking and inter modulation performance
- Low-IF receiver topology eliminates external IF filters
- Digital demodulation for optimum sensitivity and co- / adjacent channel performance
 - Digital offset compensation, symbol and frame synchronization
- RSSI information for power control

3. TECHNICAL BRIEF

3.16.6 System Integration

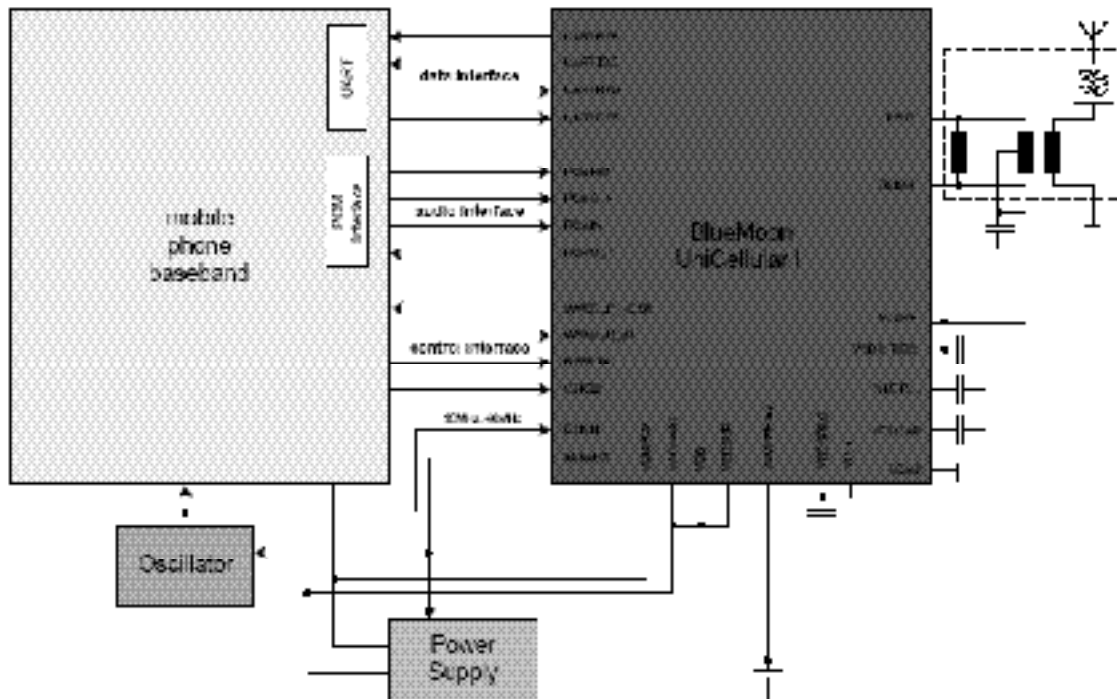


Figure 27 Mobile system integraton

The UART (serial interface) is used for the software interface between S-Gold2 baseband and the Bluetooth chip. For the HCI UART transport layer four interface lines are needed, two for data (UARTTXD and UARTRXD) and two for hardware flow control (UARTRTS and UARTCTS). For the HCI Three-Wire UART transport layer two interface lines (UARTTXD and UARTRXD) are needed. The hardware flow control lines (UARTRTS and UARTCTS) are supported but the use is optional. In ME970 used three-wire UART communication.

The UART interface has its own supply voltage (VDDUART) to ensure compatibility with the I/O voltages used by the S-Gold2.

The PCM/I2S interface is used as audio interface and can handle up to two voice channels. The PCM interface also has its own supply voltage (VDDPCM) to ensure compatibility with the I/O voltages used by the S-Gold2 baseband processor.

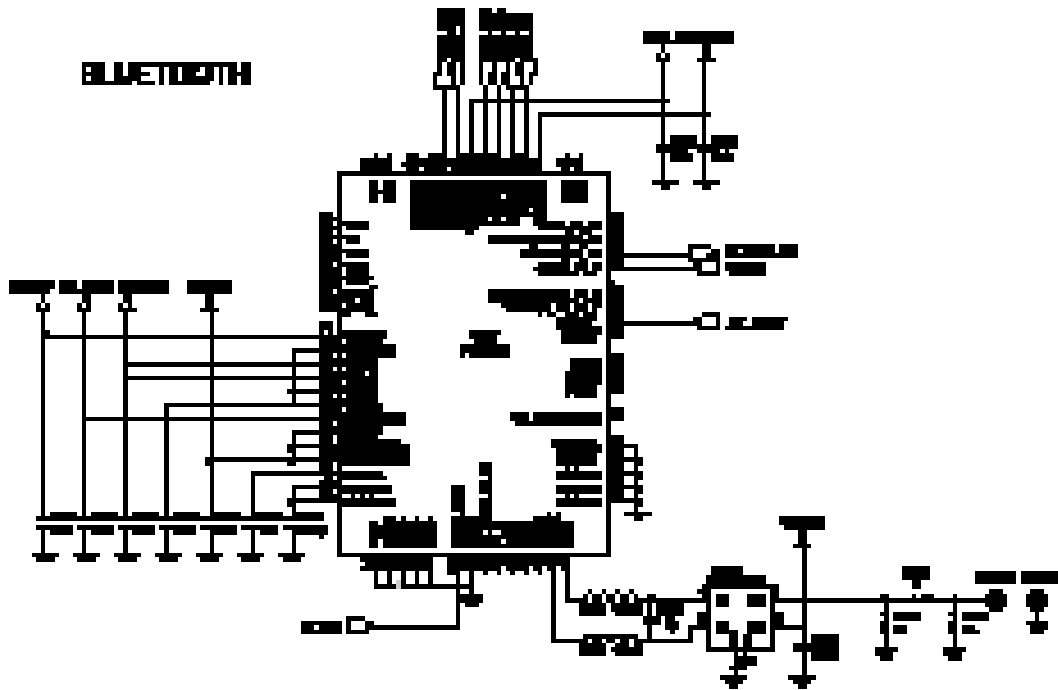


Figure 28 Bluetooth circuit

3.17. Micro SD external memory card slot

The TransFlash Memory Module has eight exposed contacts on one side. The S-Gold2 is connected to the module using a dedicated eight-pin connector

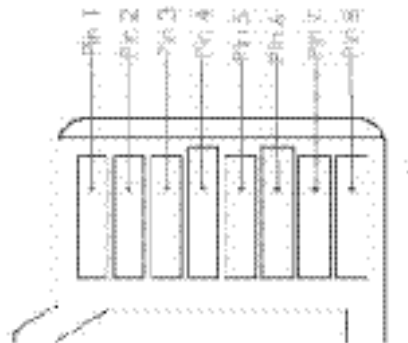


Figure 29 Micro SD pin assignment

3. TECHNICAL BRIEF

Table 10 Micro SD memory pad assign.

SD mode			
Pin No.	Name	Type	Description
1	DAT2	I/O	Data bit [2]
2	CD/DAT3	I/O	Data bit [3]
3	CMD	I/O	Command response
4	VDD	Power	Power supply
5	CLK	I	Clock
6	VSS	Ground	Power ground
7	DAT0	I/O	Data bit [0]
8	DAT1	I/O	Data bit [1]

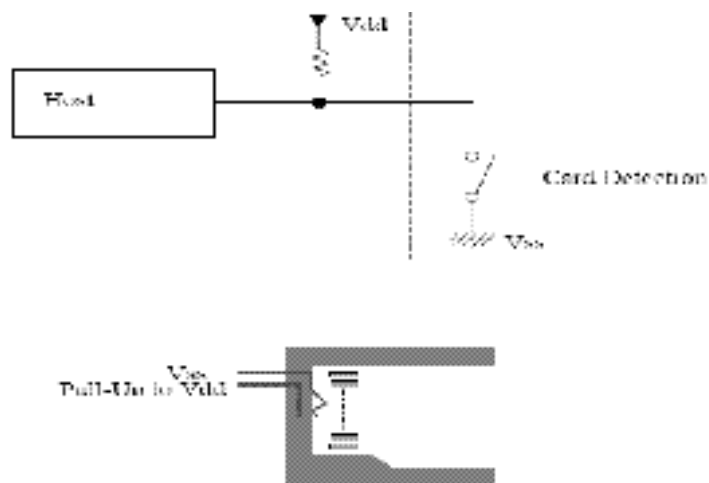


Figure 30 Micro SD memory card detection scheme

Table 11 Micro SD memory card detect truth table.

	Micro SD card status	
	it is removed	it is inserted
TF_DETECT	High	Low

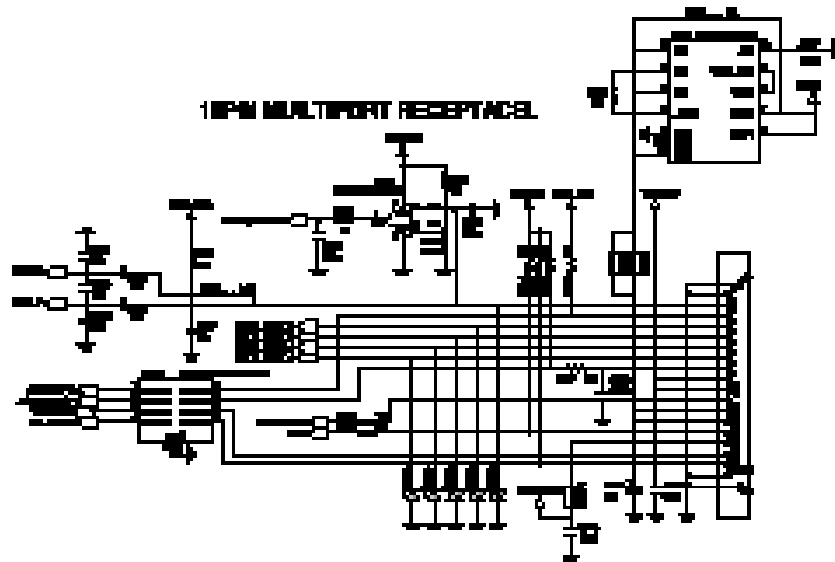


Figure 31 Micro SD socket circuit with power control

3.18. 18pin Multi Media Interface connector

Table 12 Multi media interface pin assign

ME970 MMI		
	Pin Function	Description
1	FM_ANT	FM radio antenna / Audio ground
2	HS_MIC	Headset microphone signal
3	JACK_TYPE	Accessory type detect
4	HS_OUT_L / CTS	Headset left sound / CTS
5	HS_OUT_R / RTS	Headset Right sound / RTS
6	TXD / USB_DP / REMOTE_INT	USART / USB/ Remote control interrupt
7	RXD / USB_DM / REMOTE_ADC	USART / USB/ Remote control Key ADC
8	JACK_DETECT	Headset detect (active low)
9	VBAT	Battery voltage
10	VBAT	Battery voltage
11	RPWRON	Remote power on (active high. 2.8V)
12	VCHG	Charger voltage
13	VCHG	Charger voltage
14	DSR	N.C.
15	VBUS_USB	USB VBUS
16	TX_DEBUG	Trace TX data(Debug)
17	RX_DEBUG	Trace RX data(Debug)
18	GND	Power GND

3. TECHNICAL BRIEF

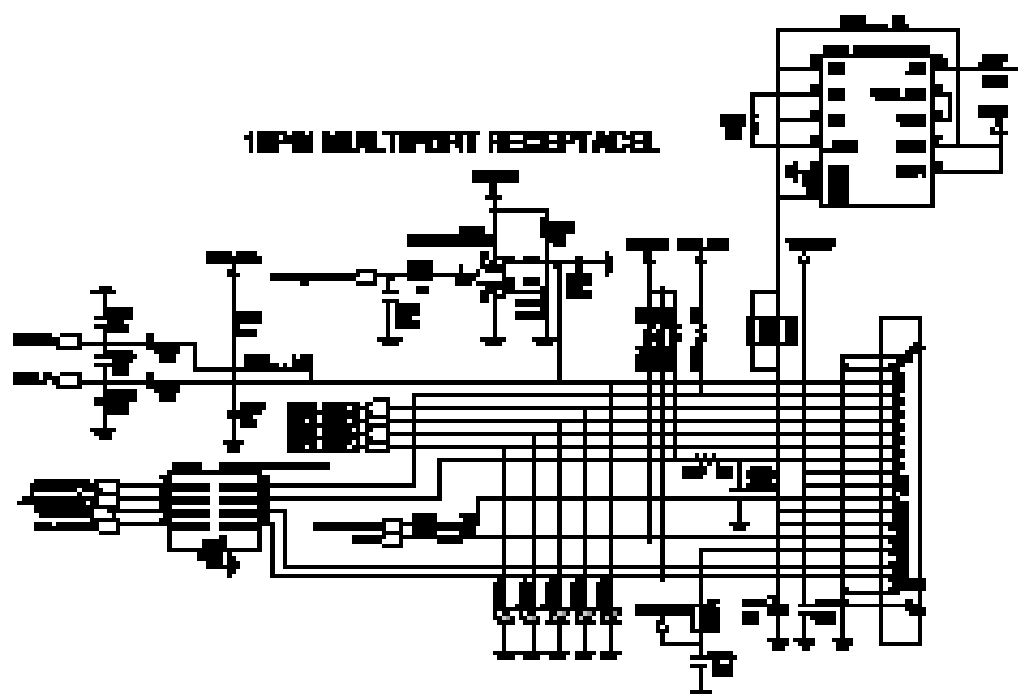


Figure 32 MMI 18pin connector circuit

RF circuit technical brief

3.19. General Description

The RF transceiver (PMB 6272 SMARTi-PM) is an integrated single chip, quad-band transceiver for GSM850/GSM900/GSM1800/GSM1900 designed for voice and data transfer applications. The transceiver provides an analog I/Q baseband interface and consists of a direct conversion receiver and a quad-band polar transmitter for GSM and EDGE with integrated PGA functionality. Further on a completely integrated SD-synthesizer with HSCSD and GPRS/EDGE capability, a digitally controlled reference oscillator with three outputs, a fully integrated quad-band RF oscillator and a three wire bus interface with all necessary control circuits complete the transceiver.

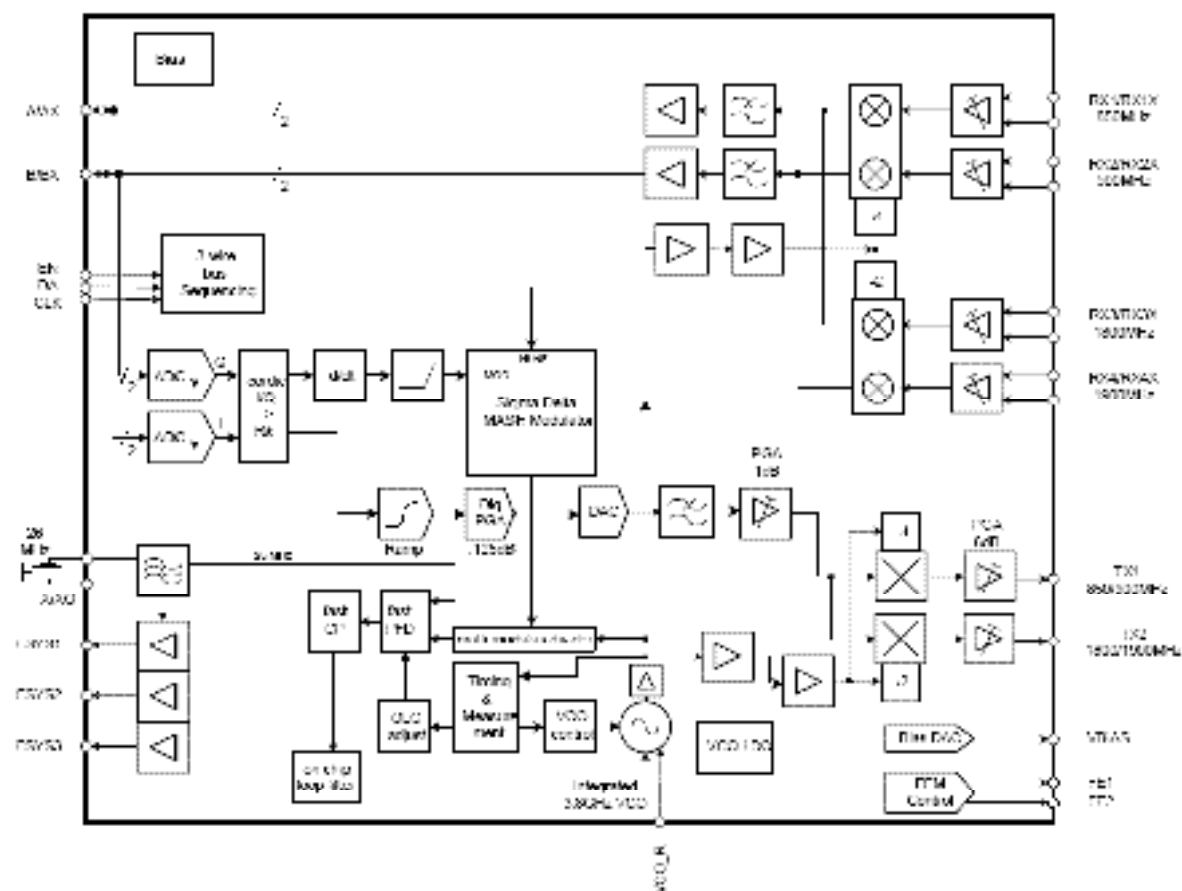


Figure 33 RF transceiver PMB7262 SMARTi-PM functional block diagram

3. TECHNICAL BRIEF

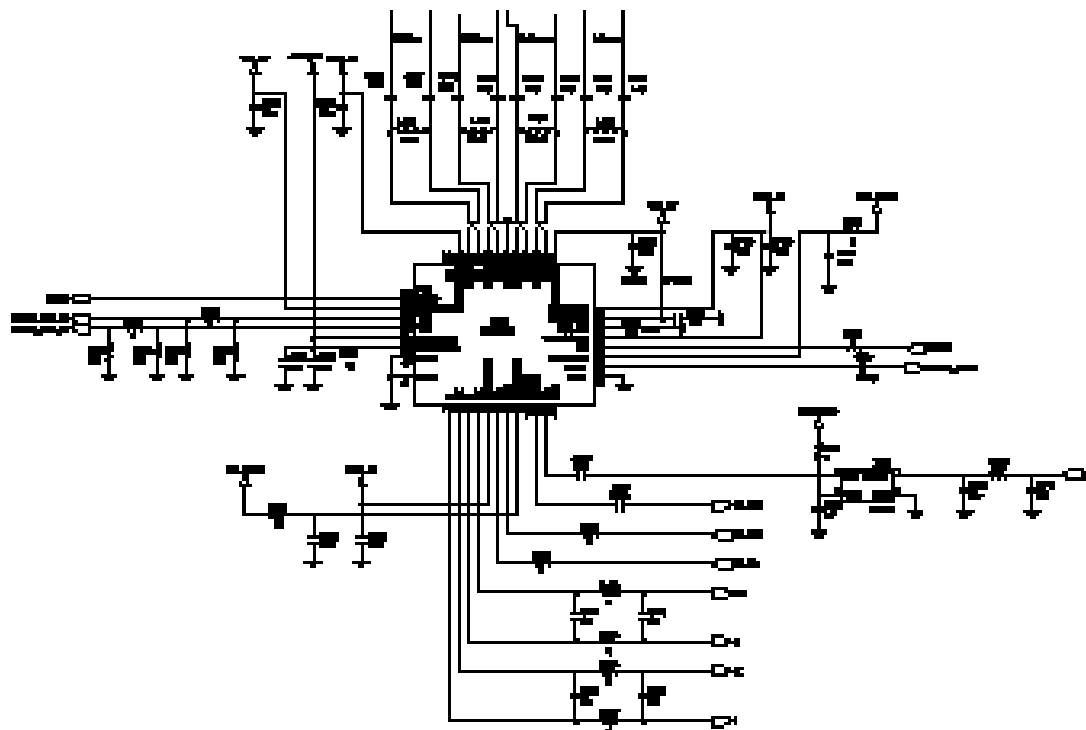


Figure 34 RF transceiver PMB7262 SMARTi-PM schematic

3.20. Receiver part

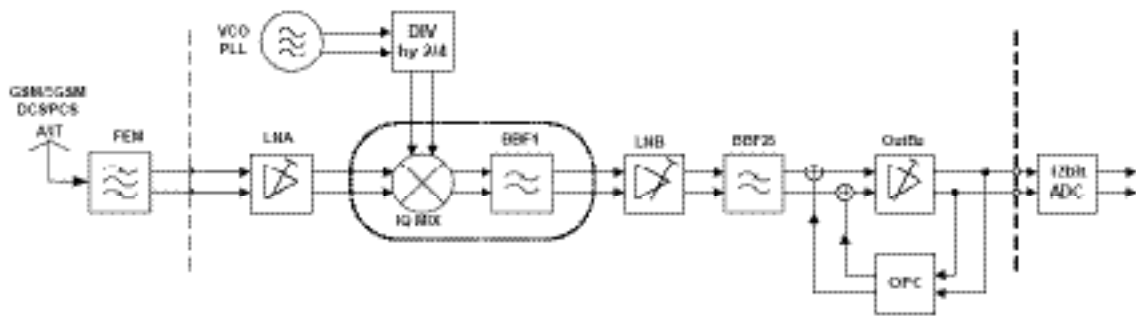


Figure 35 Receiver part block diagram

The constant gain direct conversion receiver contains all active circuits for a complete receiver chain for GSM/GPRS/EDGE (see Figure 39). The GSM850/900/DCS1800/ PCS1900 LNAs with balanced inputs are fully integrated. No inter-stage filtering is needed. The orthogonal LO signals are generated by a divider-by-four for GSM850/900 band and a divider-by-two for the DCS1800/PCS1900 band. Down conversion to baseband domain is performed by low/high band quadrature direct down conversion mixers. The baseband chain contains a LNB (low noise buffer), channel filter, output buffer and DC-offset compensation. The 3rd order low pass filter is fully integrated and provides sufficient suppression of blocking signals as well as adjacent channel interferers and avoids anti-aliasing through the baseband ADC. The receive path is fully differential to suppress on-chip interferences. Several gain steps are implemented to cope with the dynamic range of the input signals. Depending on the baseband ADC dynamic range, single- or multiple gain step switching schemes are applicable. Furthermore an automatic DC-offset compensation can be used (depending on the gain setting) to reduce the DC-offset at baseband-output. A programmable gain correction can be applied to correct for front end- and receiver gain tolerances.

3. TECHNICAL BRIEF

3.21. Transmitter part

The GMSK transmitter supports power class 4 for GSM850 and GSM900 as well as power class 1 for DCS1800 and PCS1900. The digital transmitter architecture is based on a very low power fractional-N Sigma-Delta synthesizer without any external components (see Figure39). The analog I/Q modulation data from the baseband is converted to digital, filtered and transformed to polar coordinates. The phase/frequency signal is further on processed by the Sigma-Delta modulation loop. The output of its associated VCO is divided by four or two, respectively, and connected via an output buffer to the appropriate single ended output pin. This configuration ensures minimum noise level. The 8PSK transmitter supports power class E2 for GSM850 and GSM900 as well as for DCS1800 and PCS1900. The digital transmitter architecture is based on a polar modulation architecture, where the analog modulation data (rectangular I/Q coordinates) is converted to digital data stream and is subsequently transformed to polar coordinates by means of a CORDIC algorithm. The resulting amplitude information is fed into a digital multiplier for power ramping and level control. The ready processed amplitude signal is applied to a DAC followed by a low pass filter which reconstructs the analog amplitude information. The phase signal from the CORDIC is applied to the Sigma-Delta fractional-N modulation loop. The divided output of its associated VCO is fed to a highly linear amplitude modulator, recombining amplitude and phase information. The output of the amplitude modulator is connected to a single ended output RF PGA for digitally setting the wanted transmit power. The PA interface of SMARTi-PM supports direct control of standard dual mode power amplifiers (PA's) which usually have a power control input VAPC and an optional bias

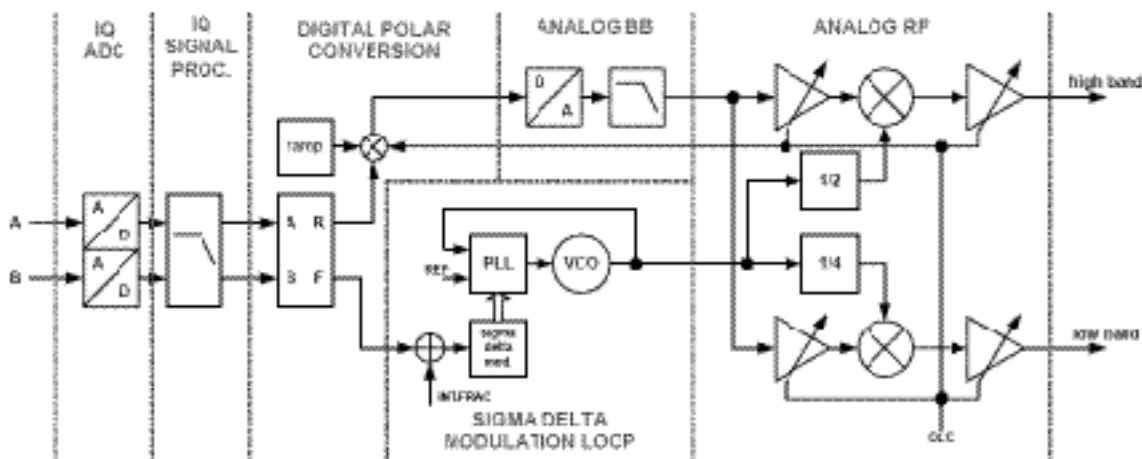


Figure 36 Transmitter part block diagram

control pin VBIAS for efficiency enhancement. In GMSK mode, the PA is in saturated high efficiency mode and is controlled via its VAPC pin directly by the baseband ramping DAC. In this way both up- / down-ramping and output power level are set. In 8PSK mode, the ramping functionality is assured by an on-chip ramping generator, whereas output power is controlled by the PGA's as described above.

3.22. RF synthesizer

The transceiver contains a fractional-N sigma-delta synthesizer for the frequency synthesis in the RX operation mode. For TX operation mode the fractional-N sigma-delta synthesizer is used as Sigma-Delta modulation loop to process the phase/frequency signal. The 26MHz reference signal is provided by the internal crystal oscillator. This frequency serves as comparison frequency of the phase detector and as clock frequency for all digital circuitry. The divider in the feedback path of the synthesizer is carried out as a multi-modulus divider (MMD). The loop filter is fully integrated and the loop bandwidth is about 100 kHz to allow the transfer of the phase modulation. The loop bandwidth is automatically adjusted prior to each slot (OLGA[®]). To overcome the statistical spread of the loop filter element values an automatic loop filter adjustment (ALFA) is performed before each synthesizer startup. The fully integrated quad-band VCO is designed for the four GSM bands (850, 900, 1800, 1900 MHz) and operates at double or four times transmit or receive frequency. To cover the wide frequency range the VCO is automatically aligned by a binary automatic band selection (BABS) before each synthesizer startup.

3.23. TCXO

The transceiver contains a fully integrated 26MHz temperature compensated controlled crystal oscillator (DCXO) with three outputs for the system clock, one output for the GSM baseband and two additional for other subsystems (GPS, Bluetooth, etc.). The only external part of the oscillator is the crystal itself. The overall pulling range of the TCXO consists of eight subranges. The subrange closest to the '0ppm' at the middle AFC-value is selected during the calibration process in the mobile phone's production and is used for the rest of the lifetime. The frequency tuning is performed along the selected subrange by programming the frequency control word (XO_TUNE) via the three wire bus ("3Wbus").

3. TECHNICAL BRIEF

3.24. Front End Module control

Implemented in the Transceiver are two outputs for direct control of front end modules with two logic input pins to select RX- and TX-mode as well as low- and high band operation.

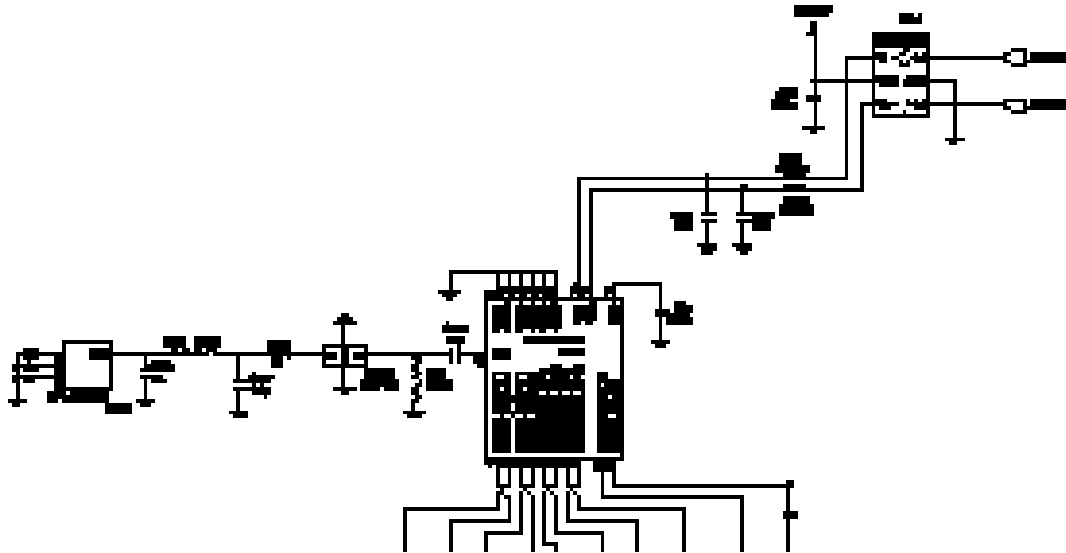


Figure 37 FEM schematic

3.25. Power Amplifier Module

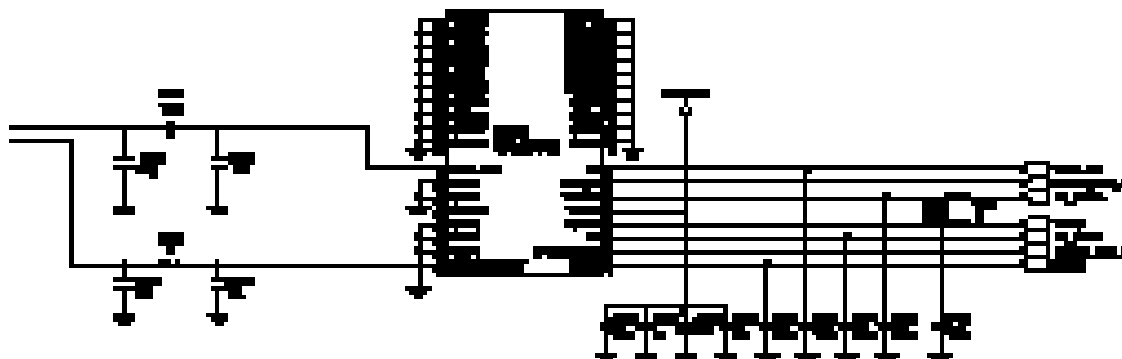


Figure 38 PAM schematic

4. PCB layout

4.1 Main & Sub PCB component placement

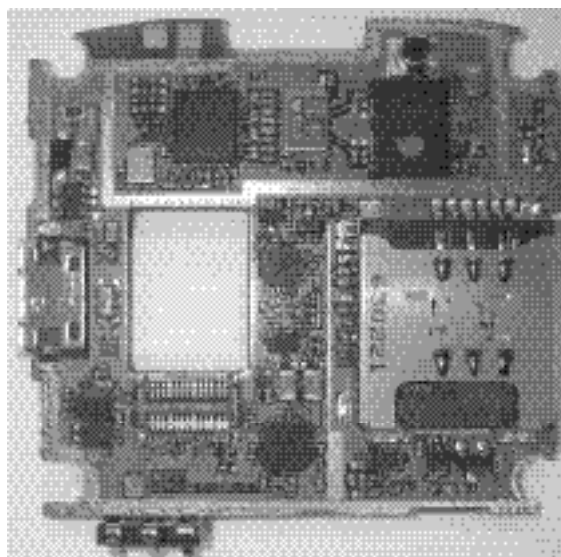
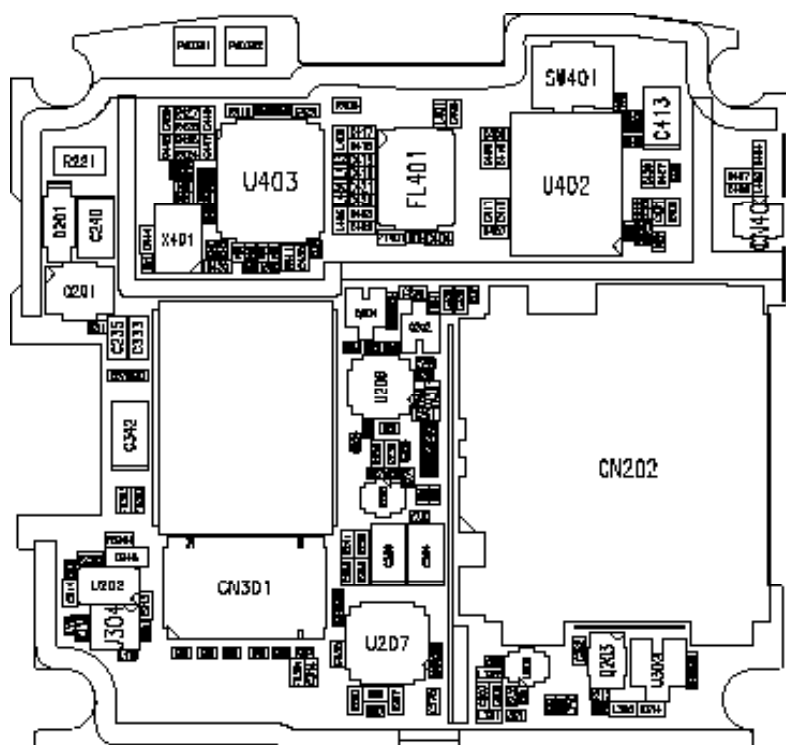


Figure 39 Main PCB top





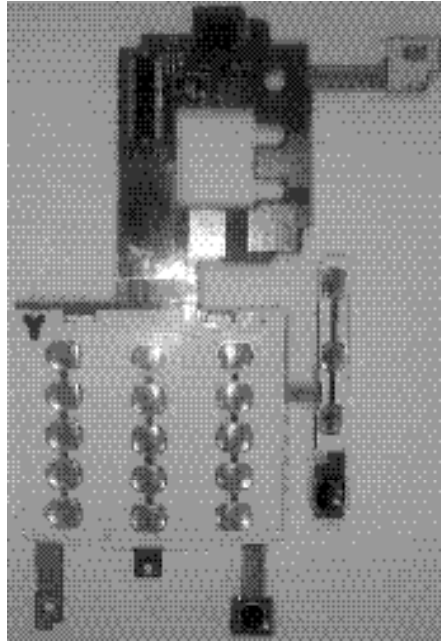


Figure 43 KEY FPCB top

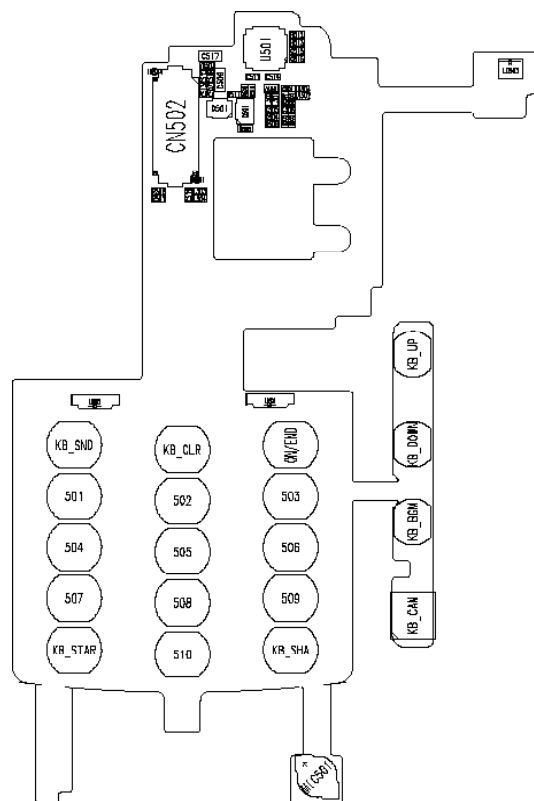


Figure 44 KEY FPCB placement

4. PCB layout

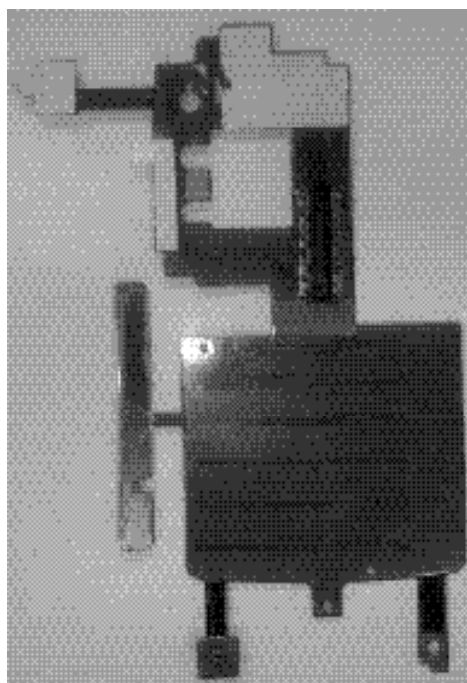


Figure 45 KEY FPCB bottom

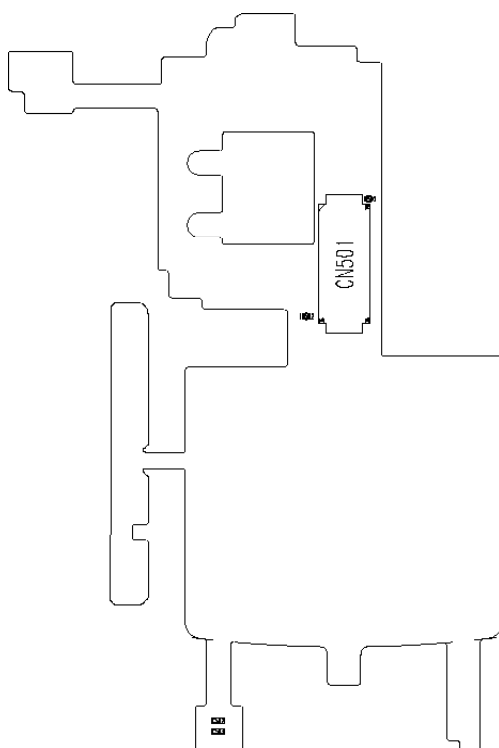
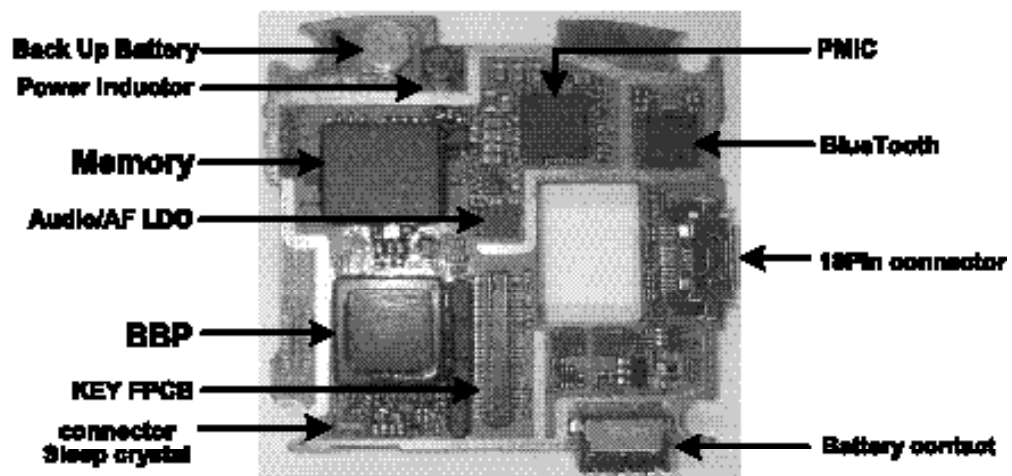
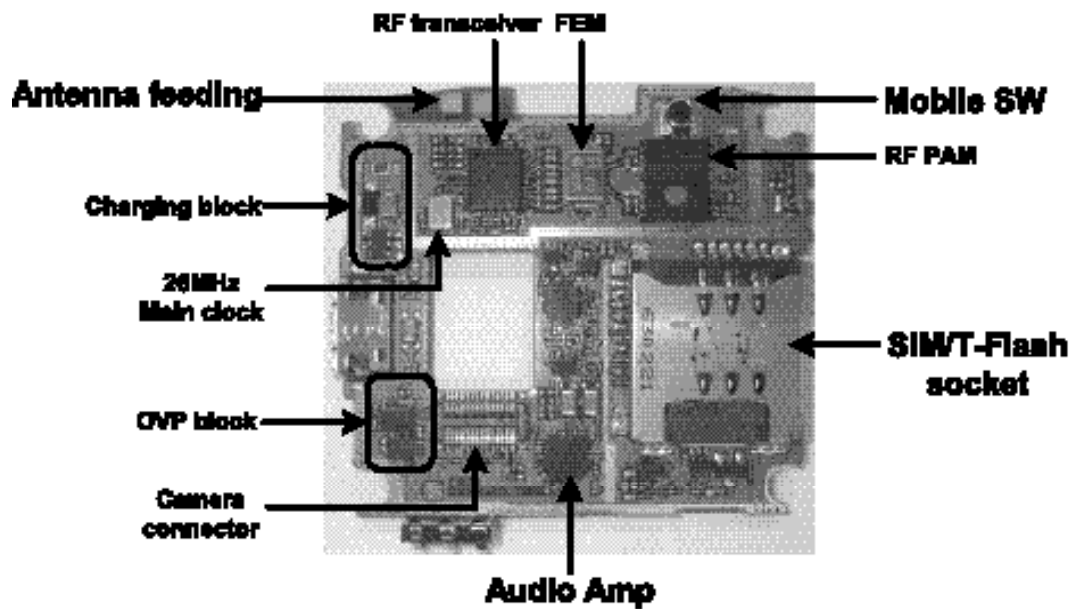
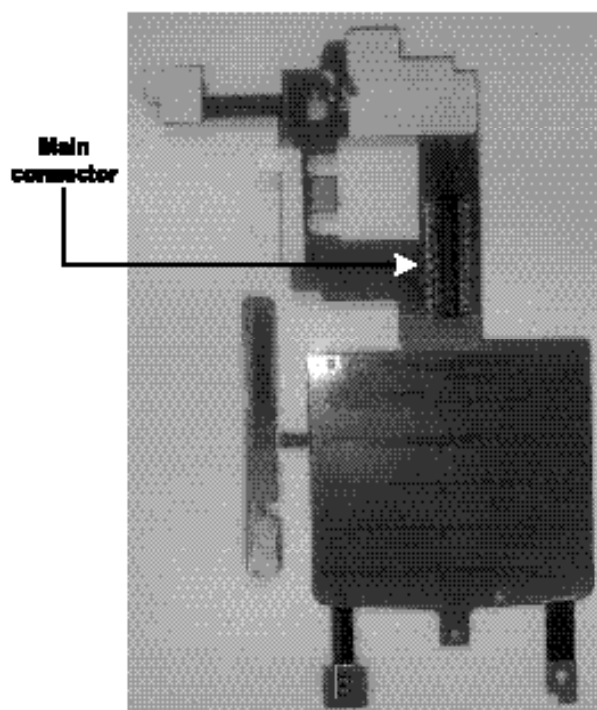
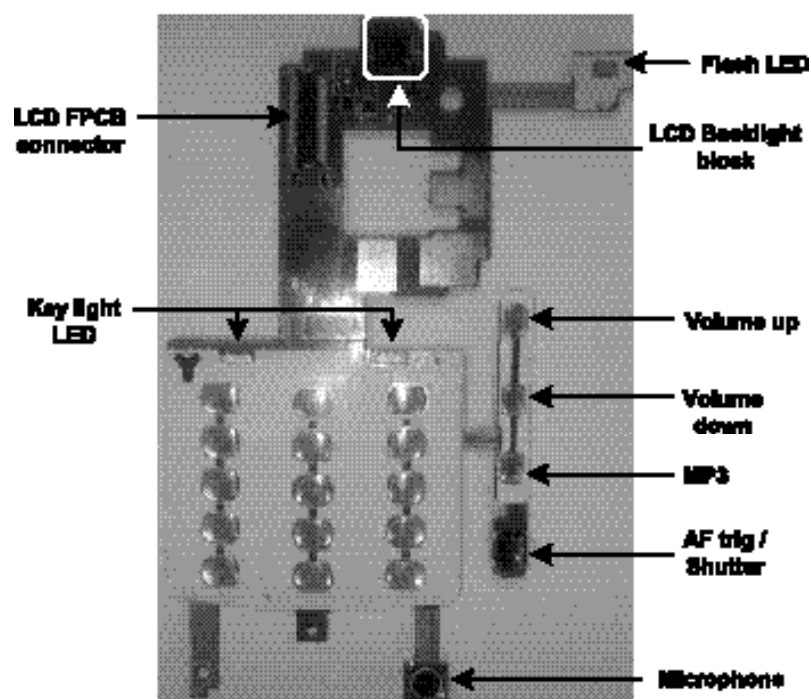
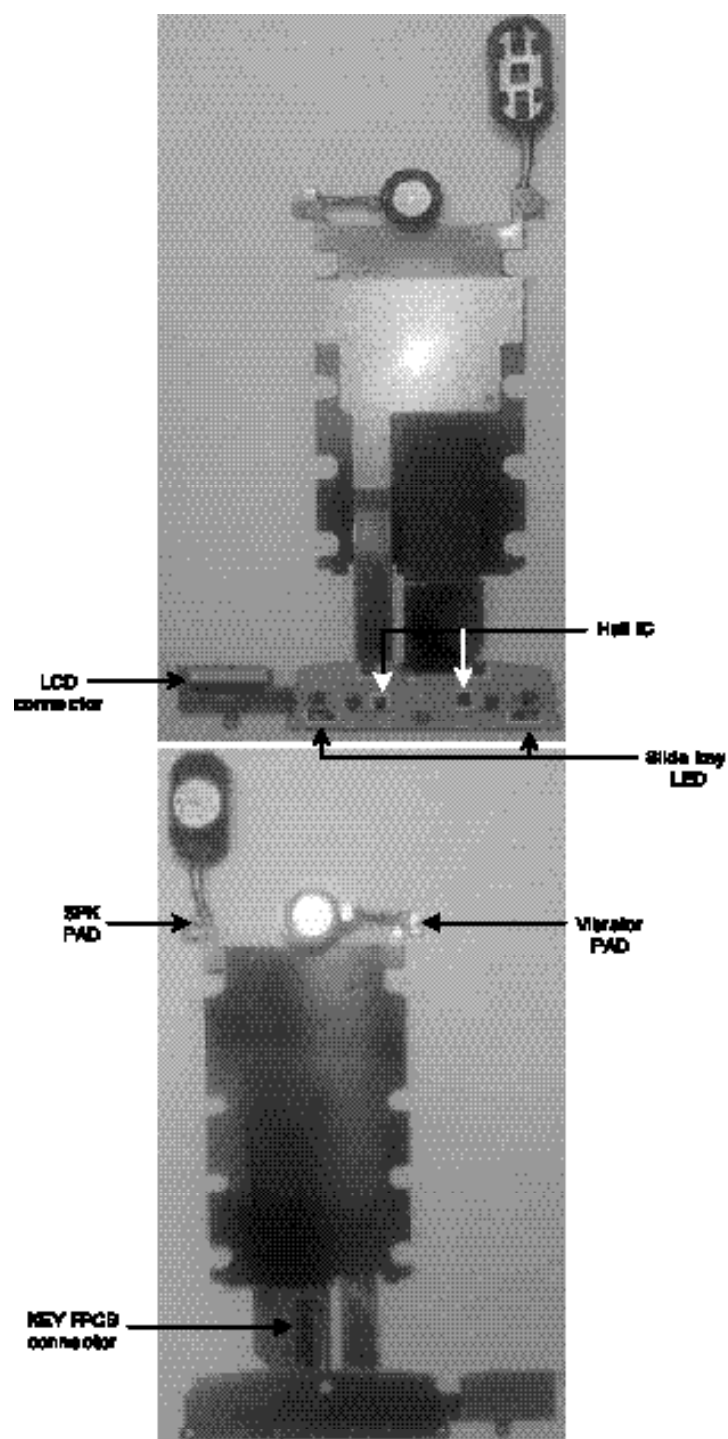


Figure 46 KEY FPCB bottom placement



4. PCB layout





5. Trouble shooting

5. Trouble shooting

5.1 Trouble shooting test setup

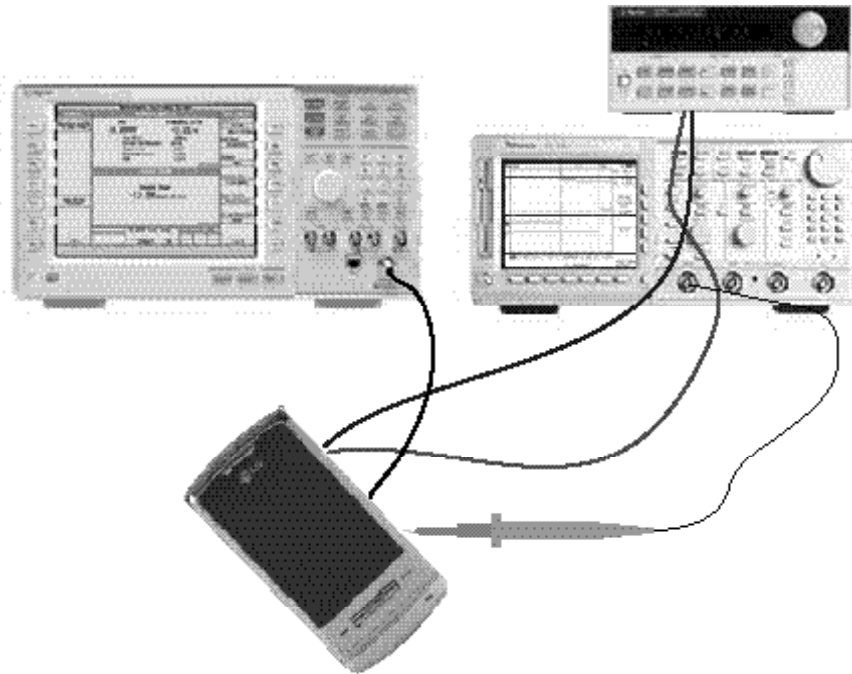


Figure 51 Equipment setup

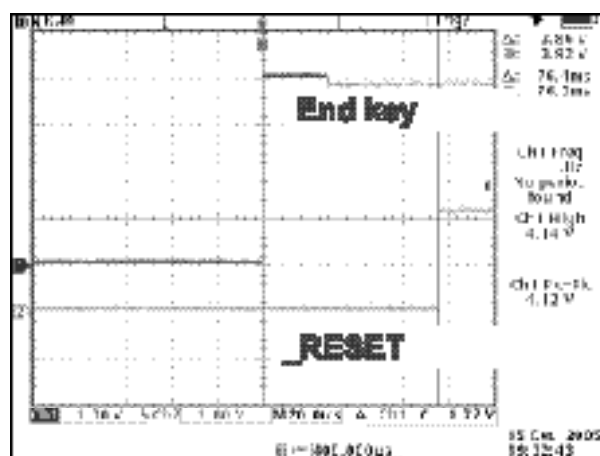
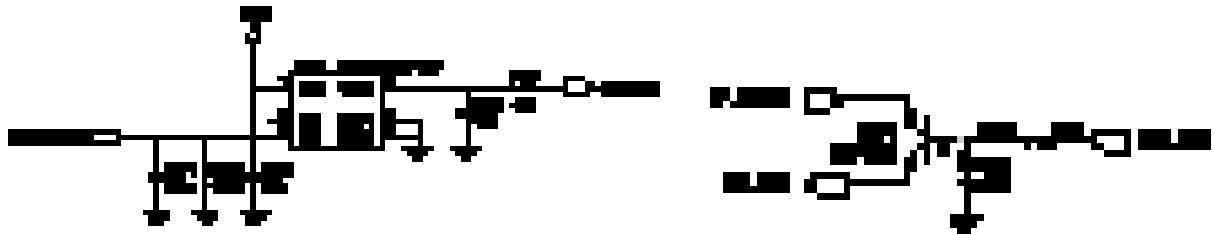
Power on all of test equipment

- Connect PIF-UNION JIG or dummy battery to the DUT for power up.
- Connect mobile switch cable between Communication test set and DUT when you need to make a phone call.
- Follow trouble shooting procedure

5.2 Power on Trouble

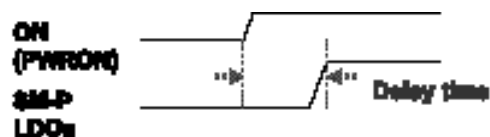
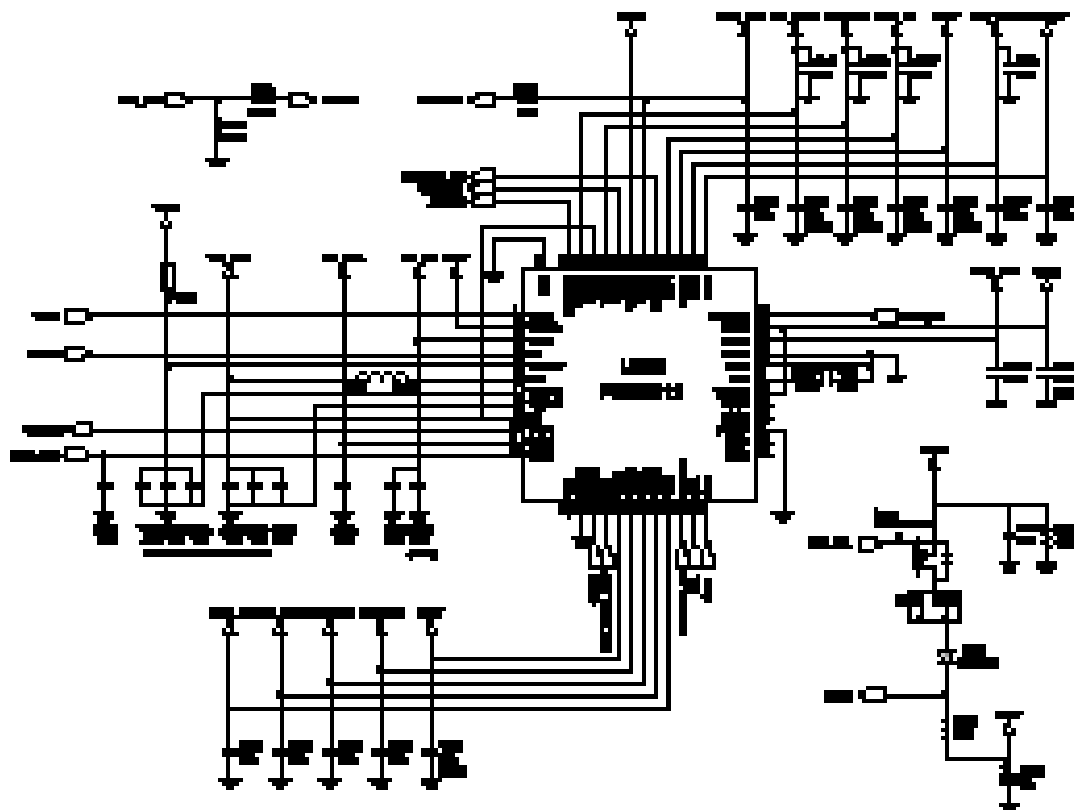
Check Points

- Battery Voltage(Need to over 3.35V)
- Power-On Key detection (PWRON signal)
- Outputs of LDOs from PMIC

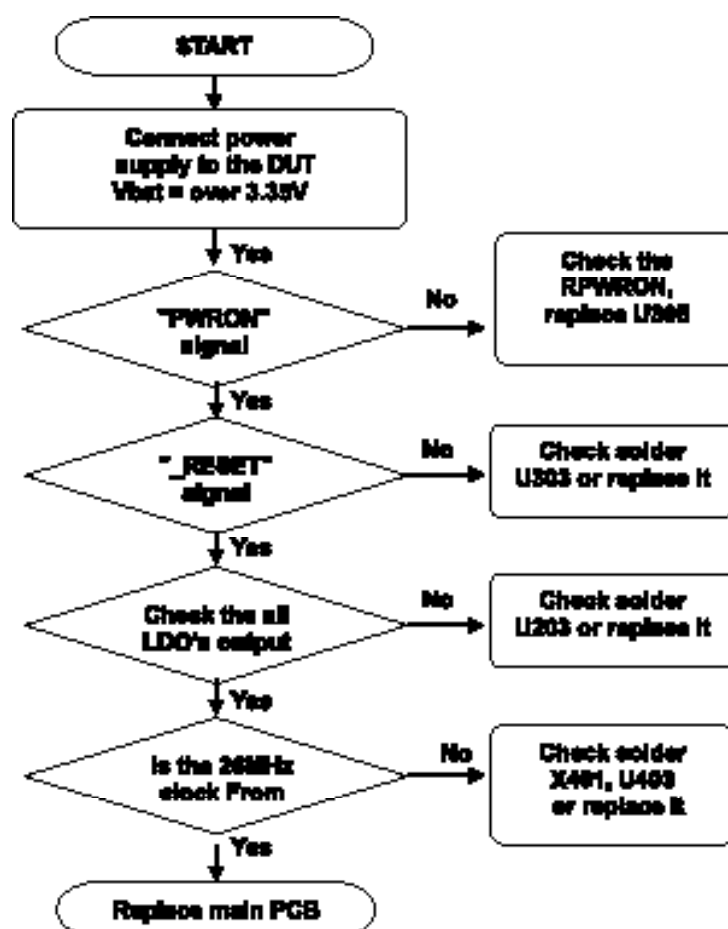


5. Trouble shooting

PMOD & LDOs CHARTER



SSAP-ONE R.LDOs	2V7_V C32	V8 B2	1V8_C C33	1V8 B1	2V72 _J0	2V80 ARA	2V80 _Bm	2V80 _J01	2V80 CARD	2V8 UMB	1V8 _B1	2V80 _J01
LDO NAME	VREF2	VL8 B2	VINT(B DB3)	VL8 B1	VINT	VANA	V8M 1	V8M 2	V8M3	VL8 B	VREF C	VREF 1
Delay Time(sec)	20	24.2	23.7	25.2	26.2	28.1	30.2	32.2	34.3	36.5	628	628

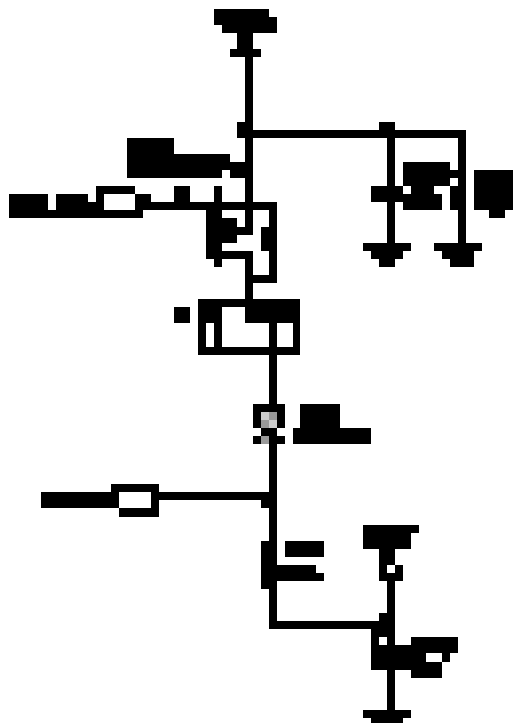
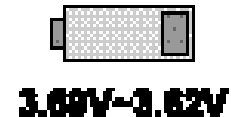
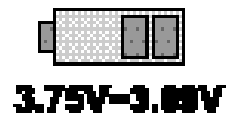
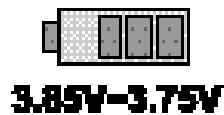
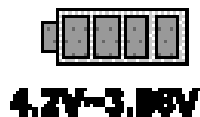


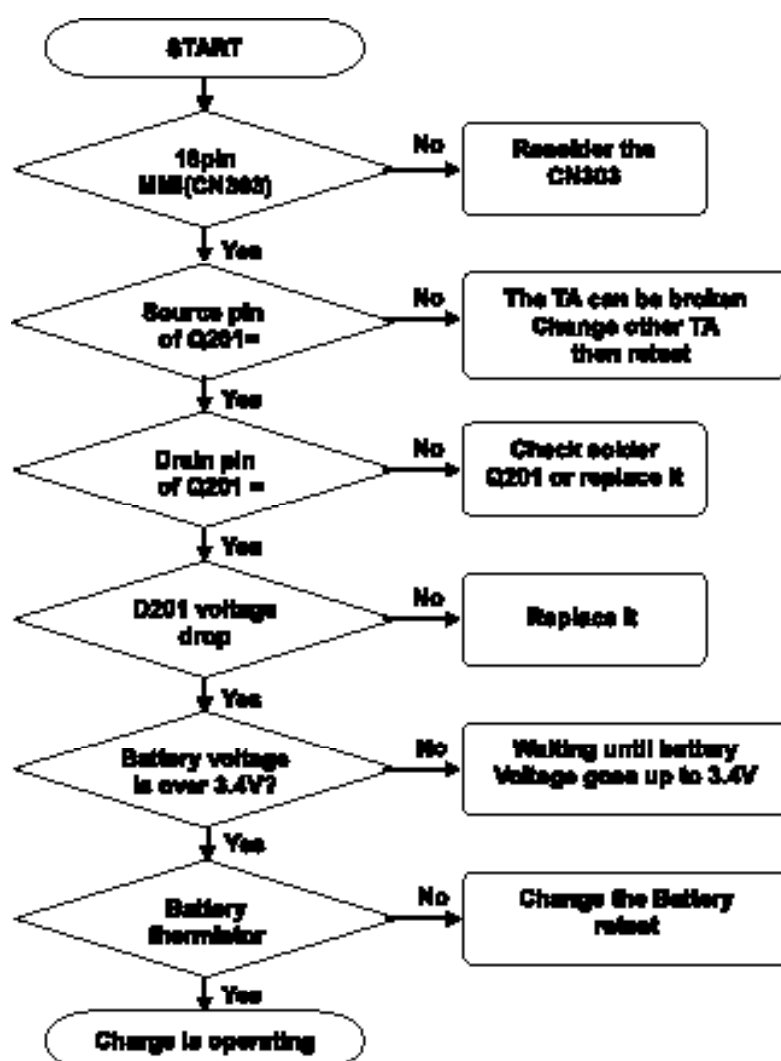
5. Trouble shooting

5.3 Charging trouble

Check Points

- Connection of TA (check TA voltage 4.8V)
- Charging Current Path component voltage drop
- Battery voltage
 - Charging method : CC-CV
 - Charger detect voltage : about 4.0V
 - Charging time : 3h under
 - Charging current : 500mA
 - Cutoff current : 100mA
 - Low battery alarm
 - . Idle : 3.62V
 - . Dedicated : 3.50V
- Switch-off voltage : 3.35V
- Charging temperature ADC range
 - ~ -20°C : small charging operation.
 - -20°C ~ 60°C : charging.
 - 60°C ~ : not charging operation small charging operation.



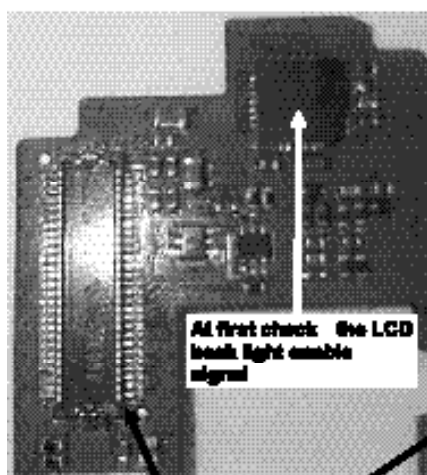
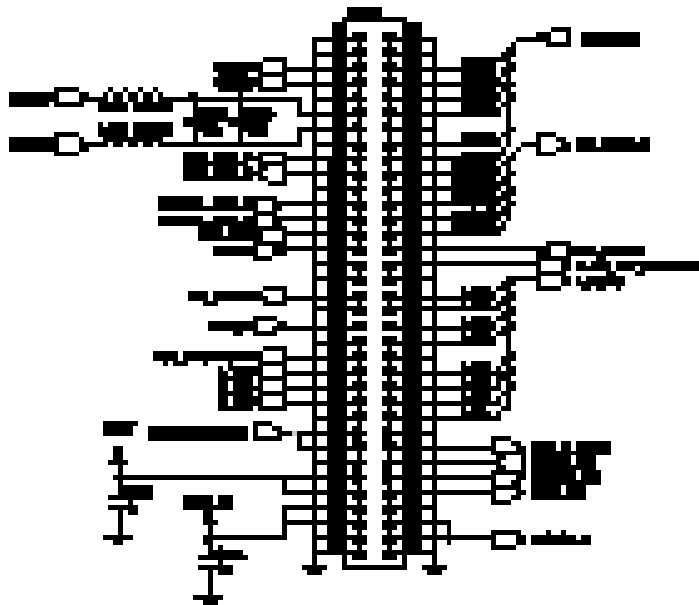


5. Trouble shooting

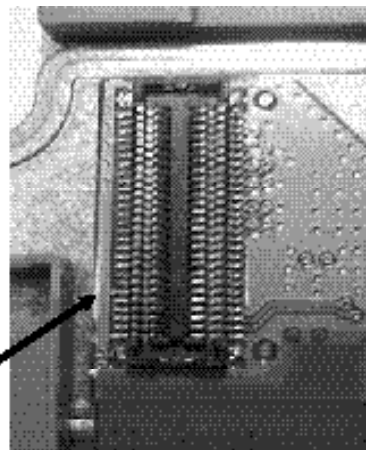
5.4 LCD display trouble

Check Points

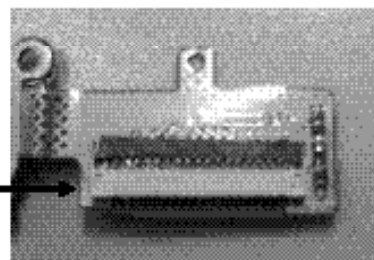
- LCD assembly status (FPCB)
- Connector combination

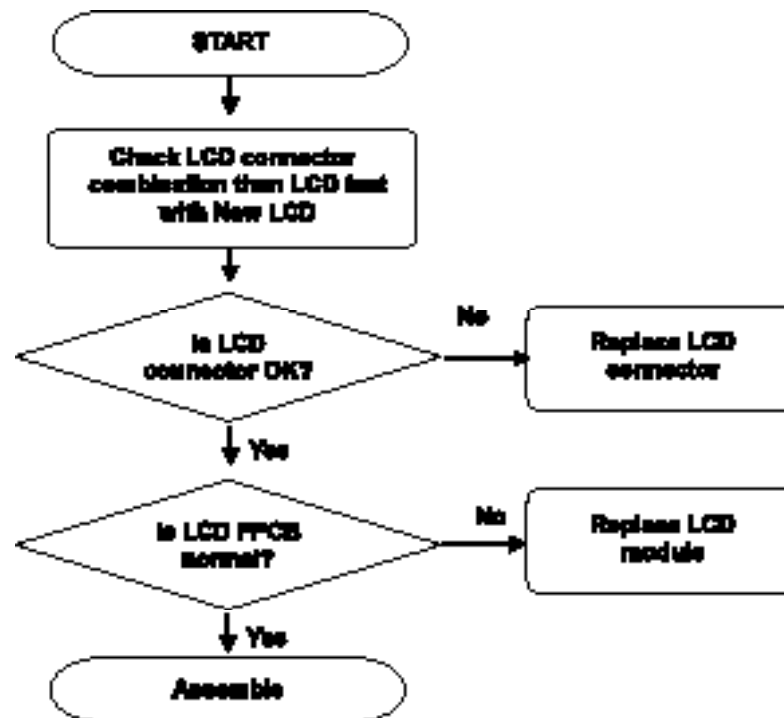


70pin FPCB connector



LCD connector



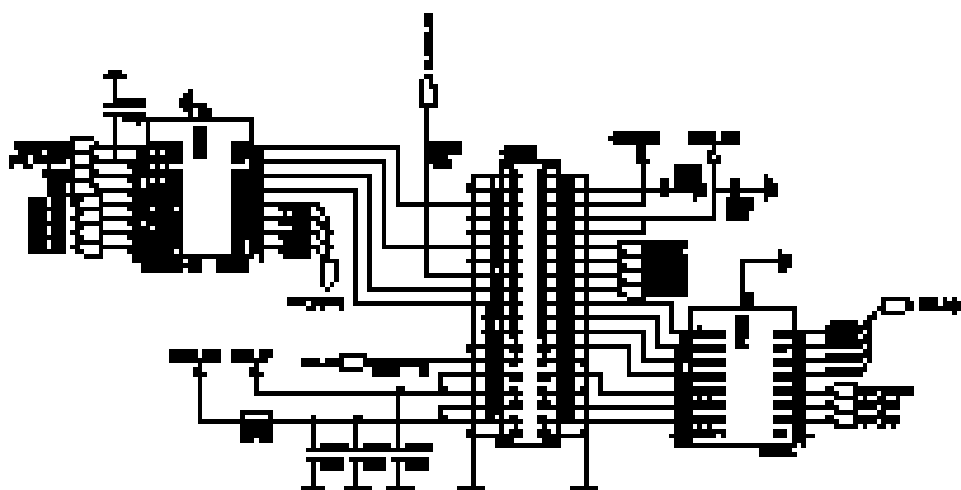


5. Trouble shooting

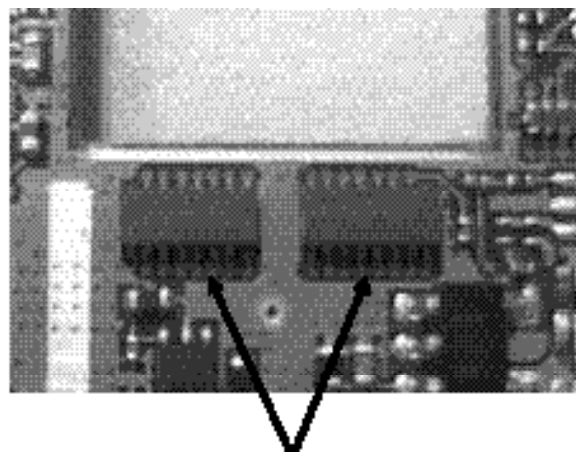
5.5 Camera Trouble

Check Points

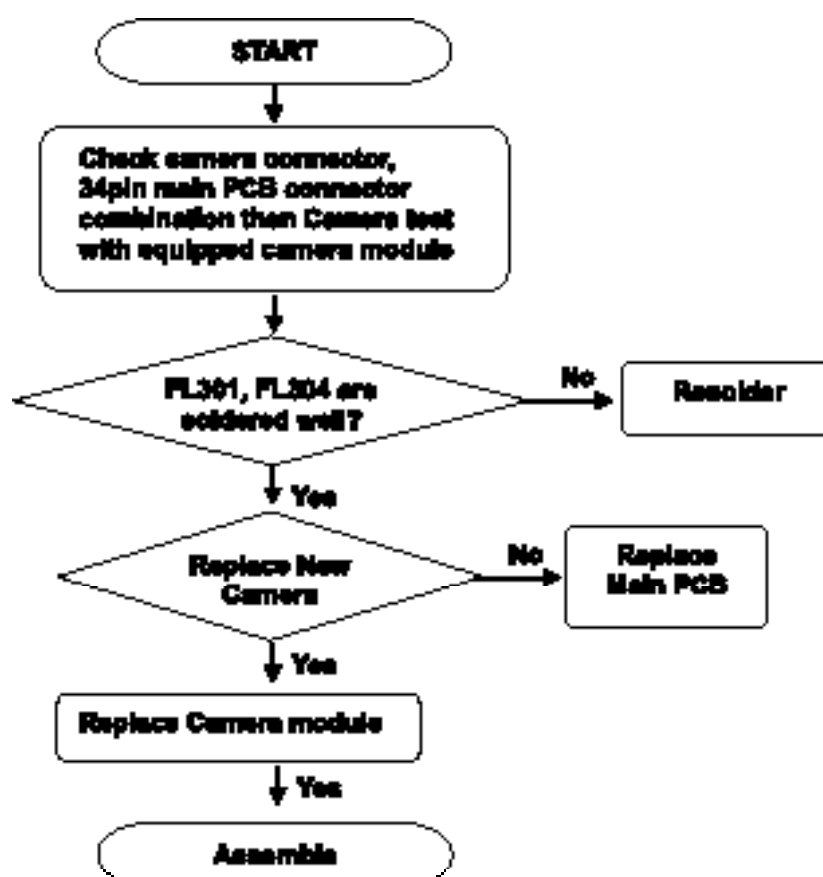
- Connectors combination
- EMI filter soldering



Check the connector combination



Check signal flow via EMI filter

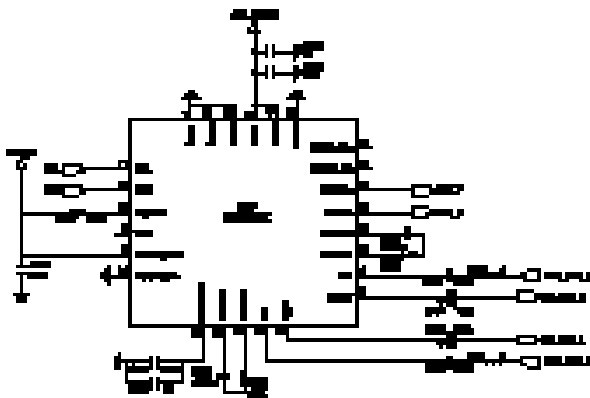


5. Trouble shooting

5.6 Receiver & Speaker trouble

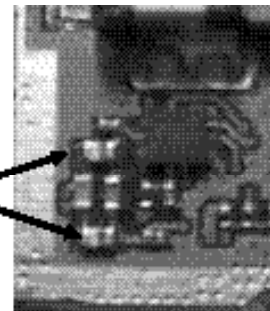
Check Points

- Speaker wire
- Audio amp soldering

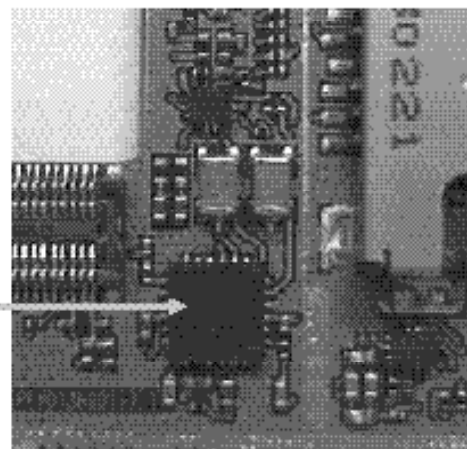


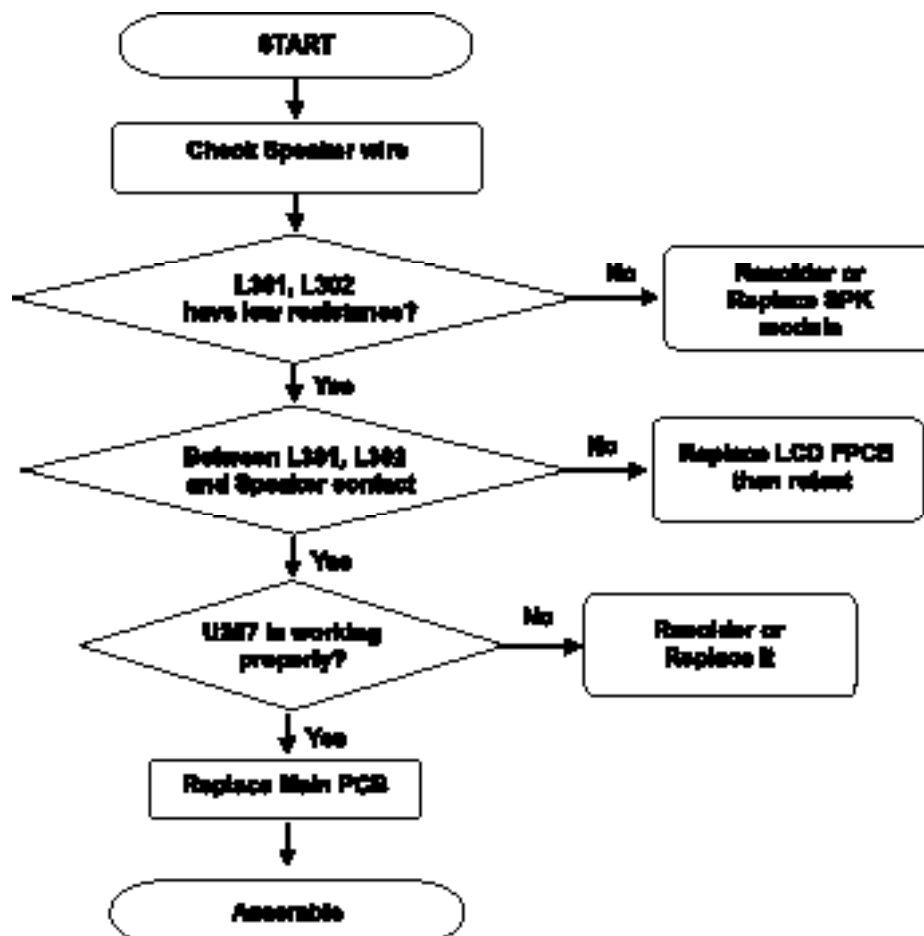
Check the SPK wire and soldering

**Check signal
flow via inductor
L301, L302**



**Check Audio
amp, in output
signal**



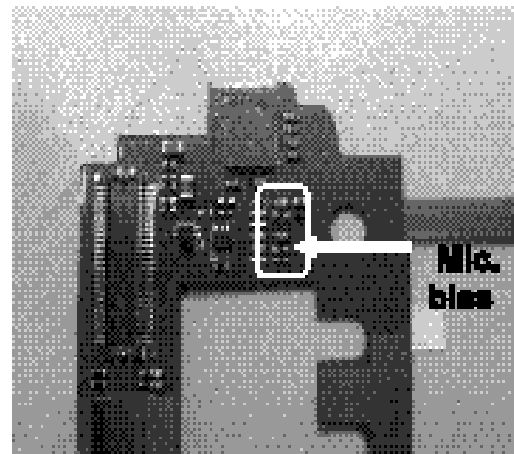
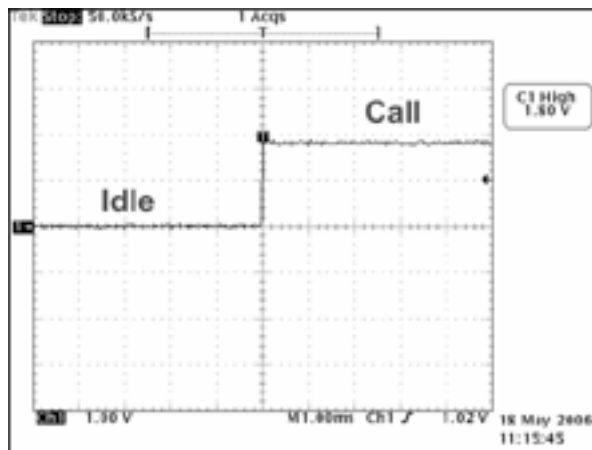
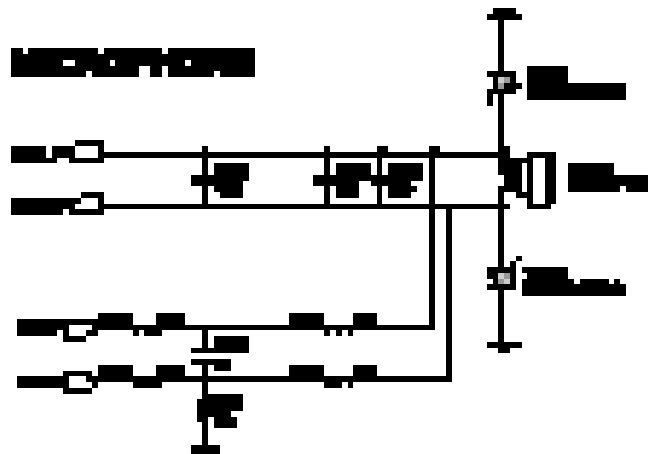


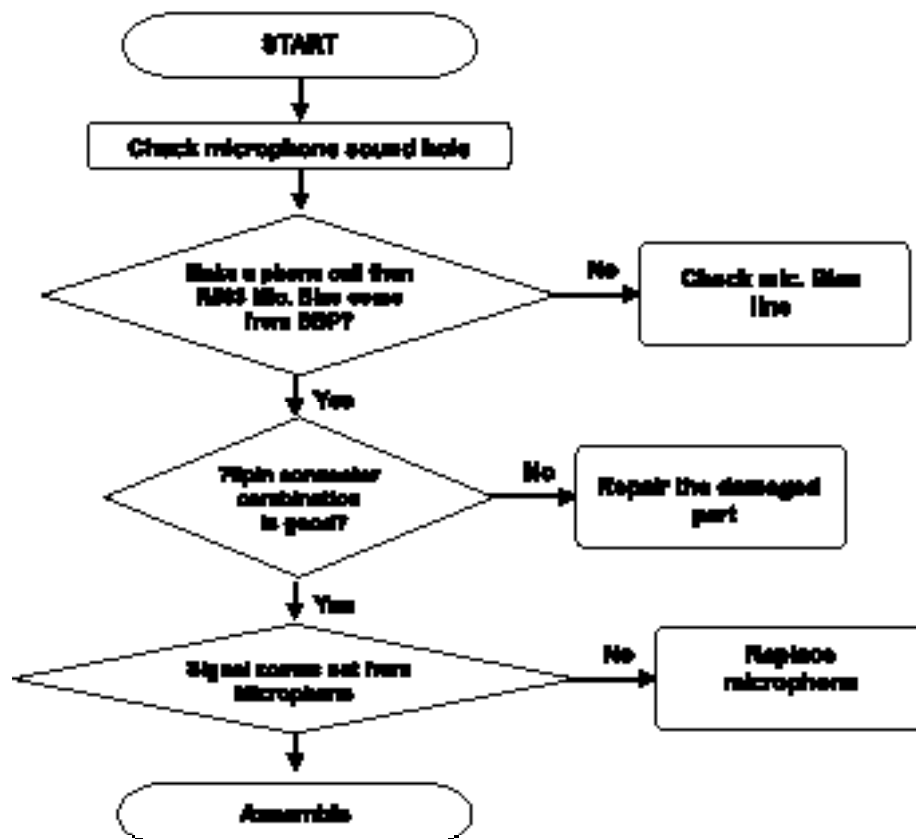
5. Trouble shooting

5.7 Microphone trouble

Check Points

- Microphone hole
- Mic. Bias & signal come from



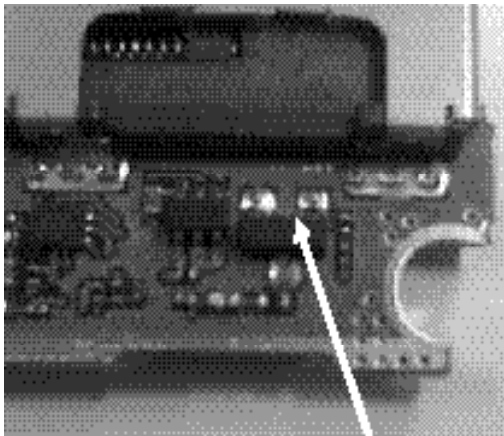
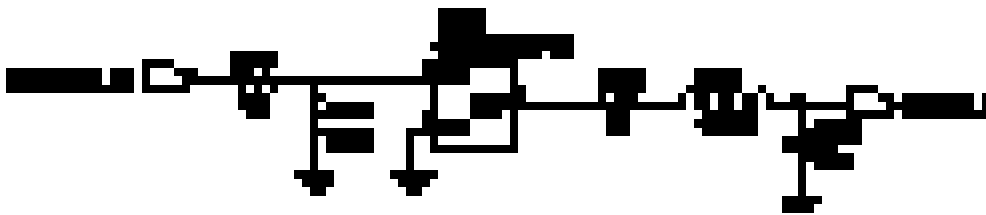


5. Trouble shooting

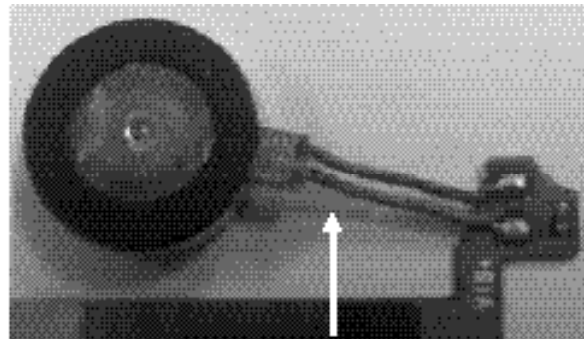
5.8 Vibrator trouble

Check Points

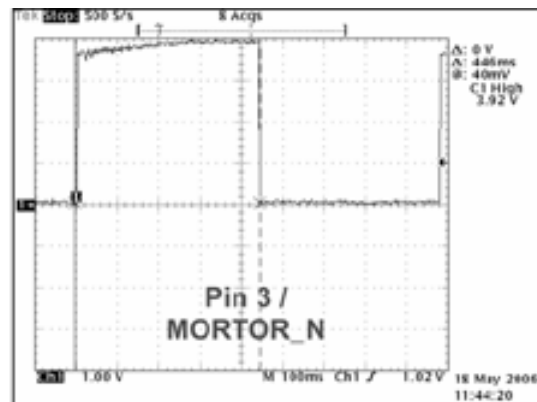
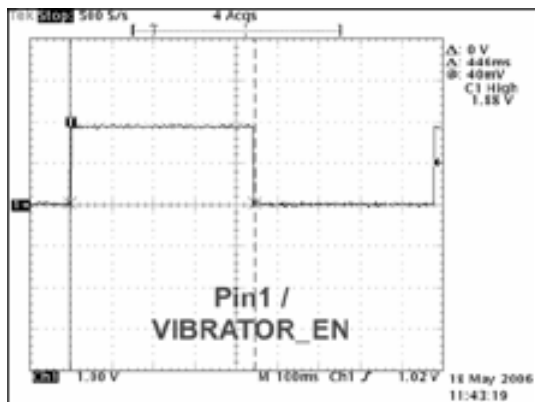
- Vibrator contact
- IC is working correct

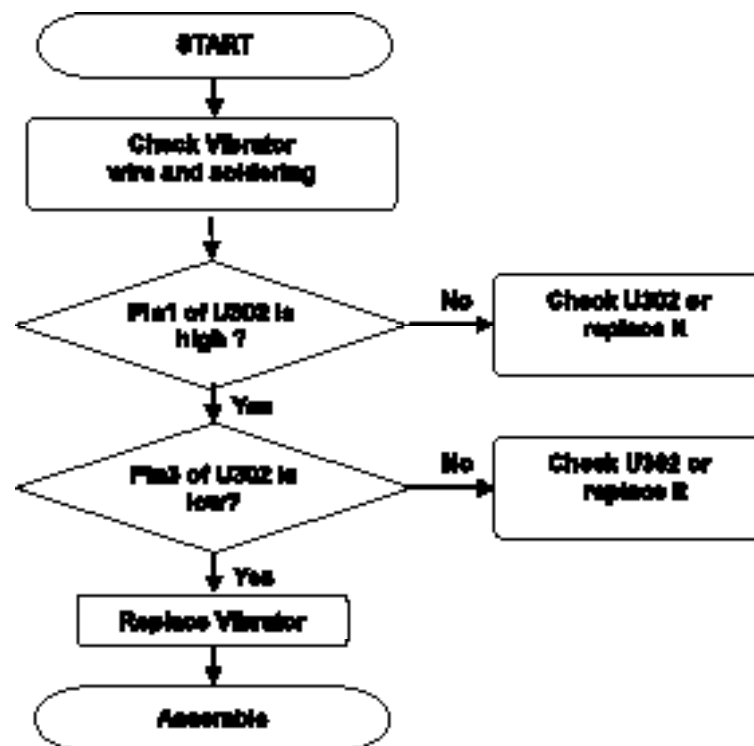


**Check the driver IC
Enable signal goes to
high then vibration**



Check the wire and soldering



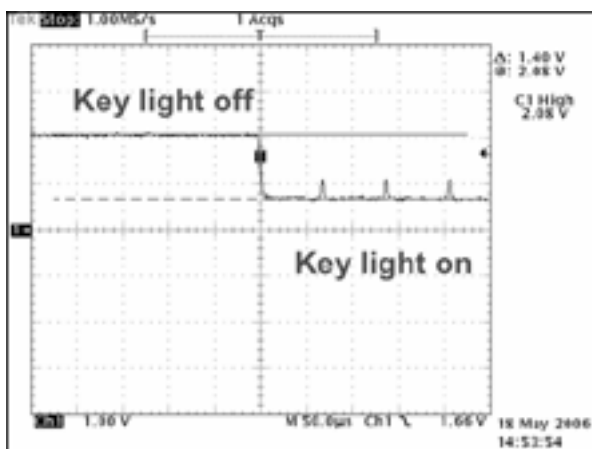
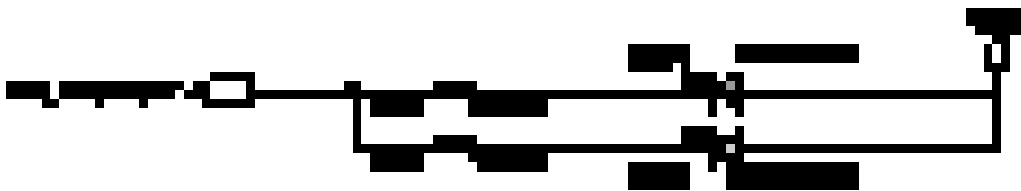


5. Trouble shooting

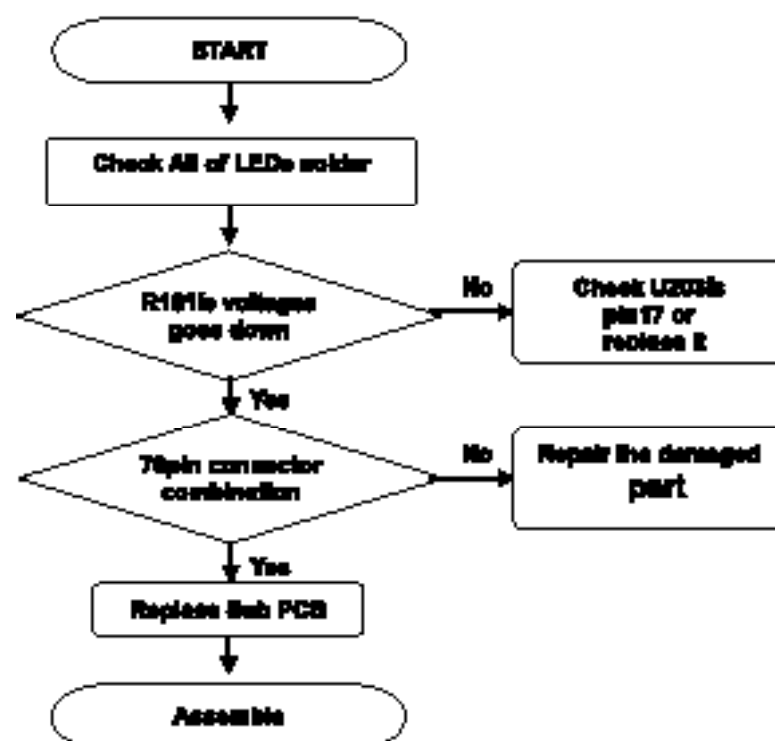
5.9 Keypad back light trouble

Check Points

- Signal path is connected well
- Control IC is working properly



Check R501,502 resistor

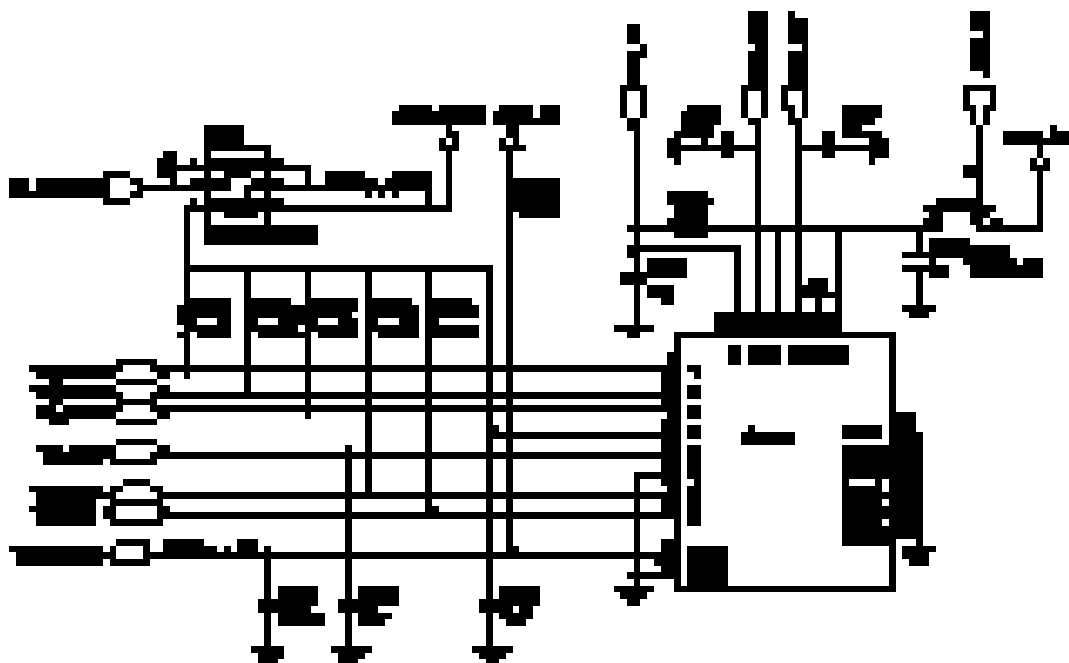


5. Trouble shooting

5.10 Micro SD and SIM card trouble

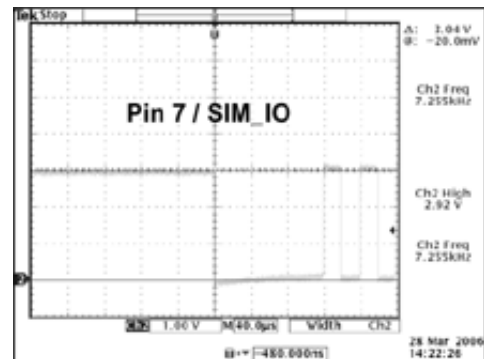
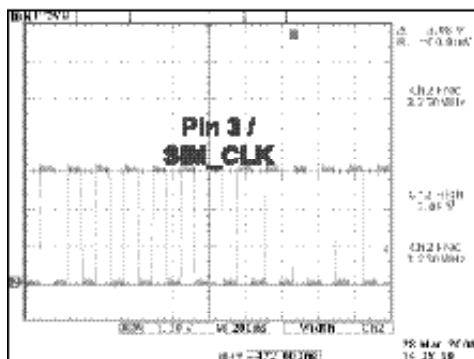
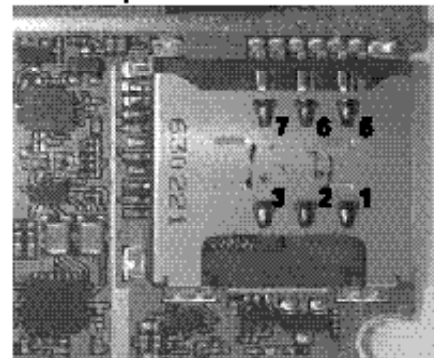
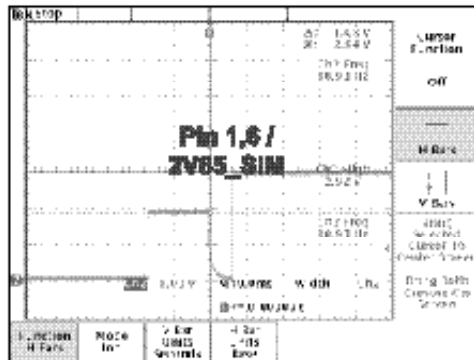
Check Points

- Power control FET is working
- Socket soldering
- Proper SIM is used
- Card detect is working

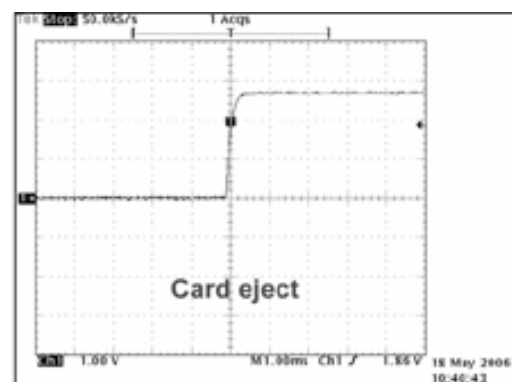
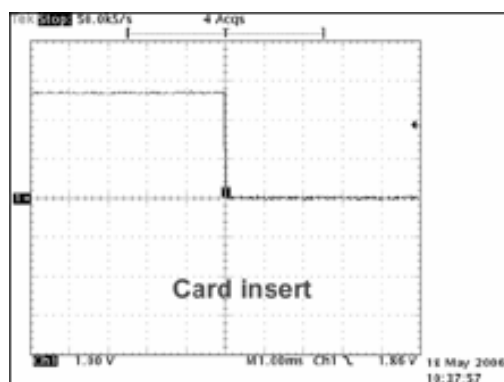
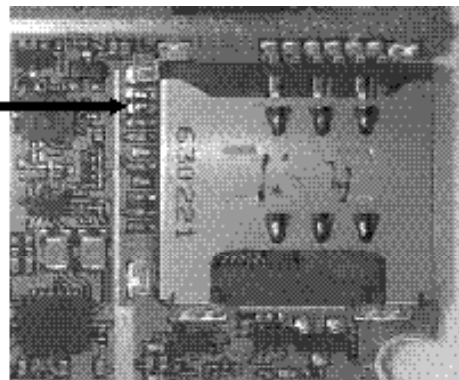


5. Trouble shooting

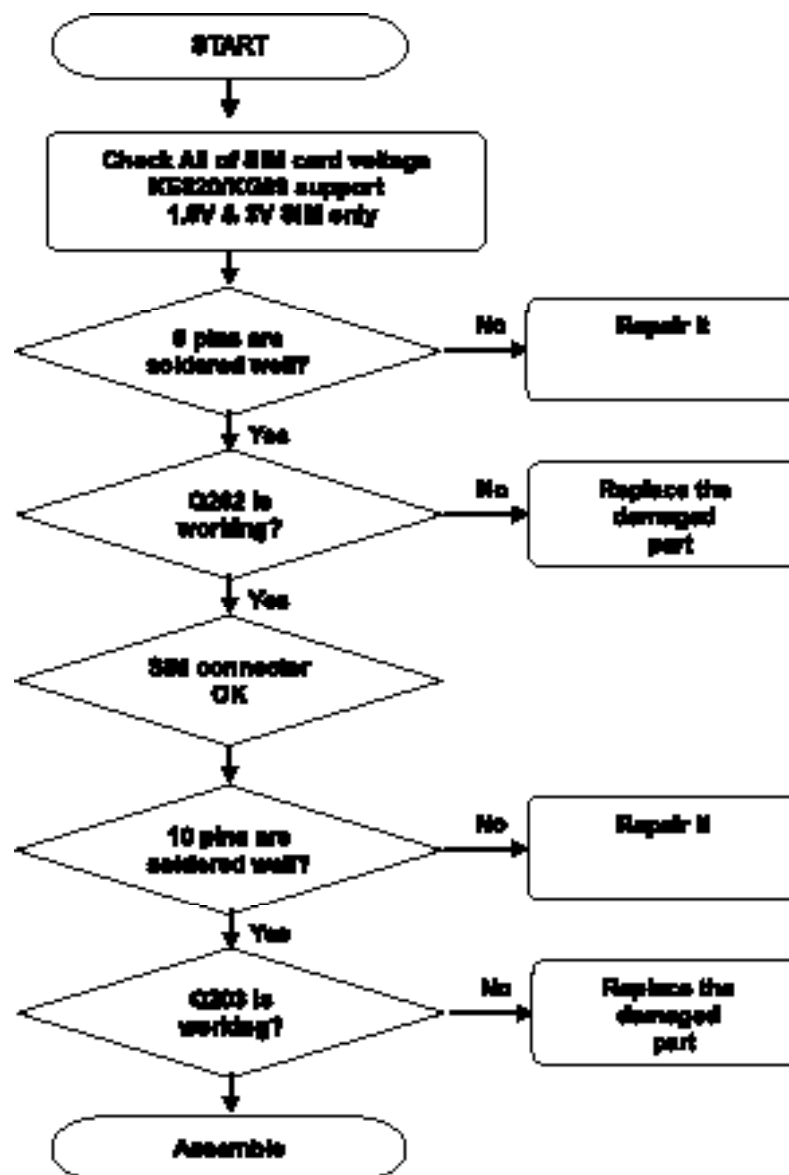
**Check
soldering all
pin of socket**



TF_DETECT

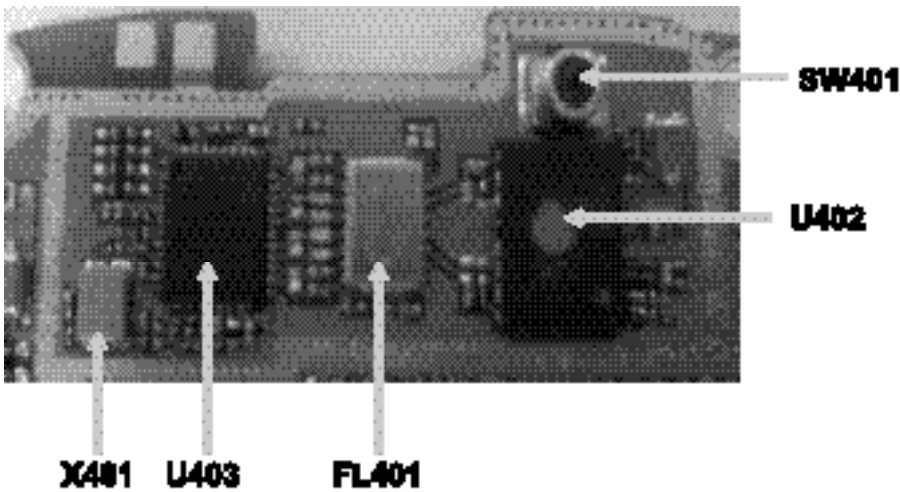


5. Trouble shooting



5.11 RF PART TROUBLESHOOTING

5.11.1 RF Components

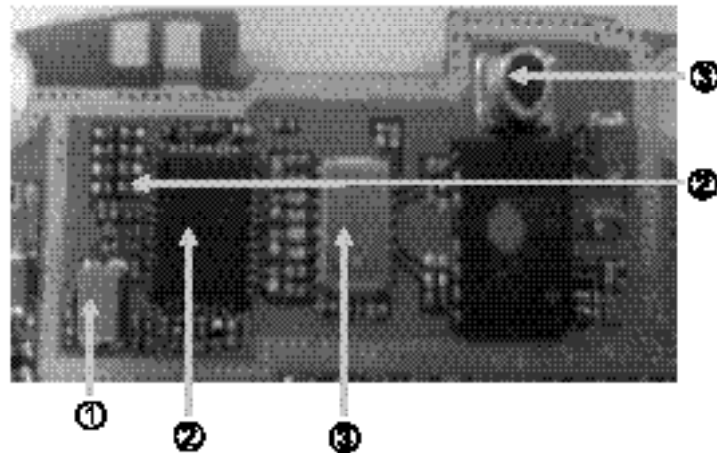
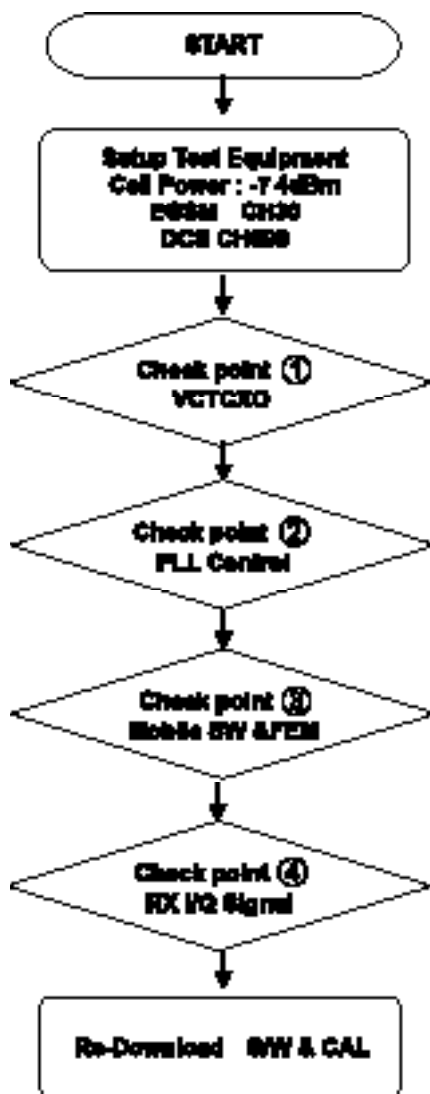


REFERENCE	PART Description
U402	PAM (Power Amplifier Module)
X401	VCTCXO (26MHz)
FL401	FEM (Front End Module)
U403	Transceiver
SW401	Mobile Switch

5. Trouble shooting

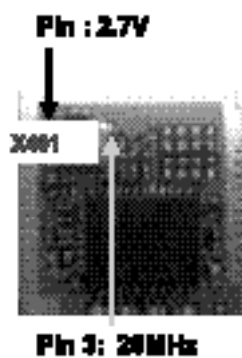
5.11.2 Trouble Shooting of Receiver Part

Checking Flow

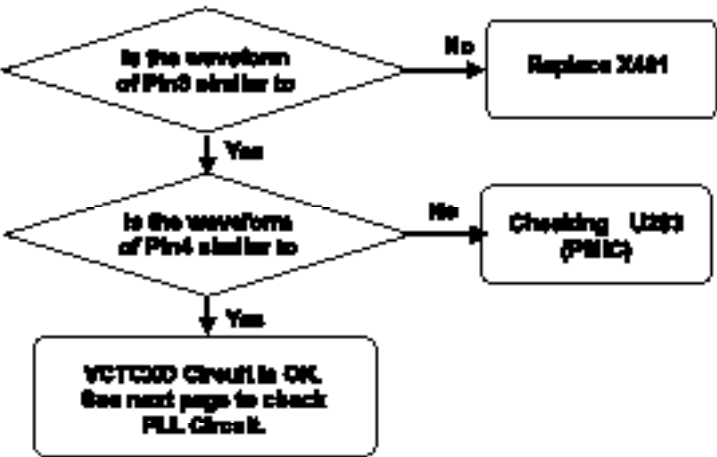


5.11.3 Checking VCTCXO Circuit

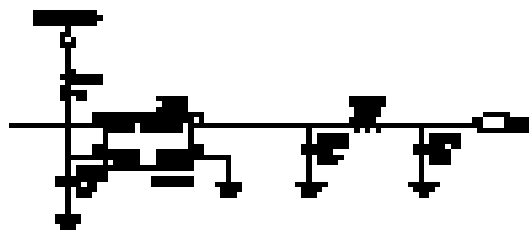
Checking Points



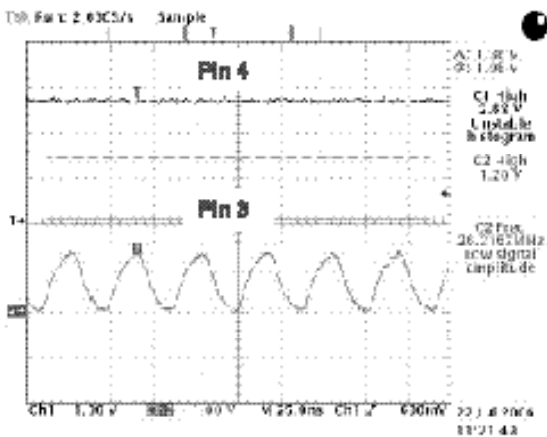
Checking Flow



VCTCXO Circuit Diagram



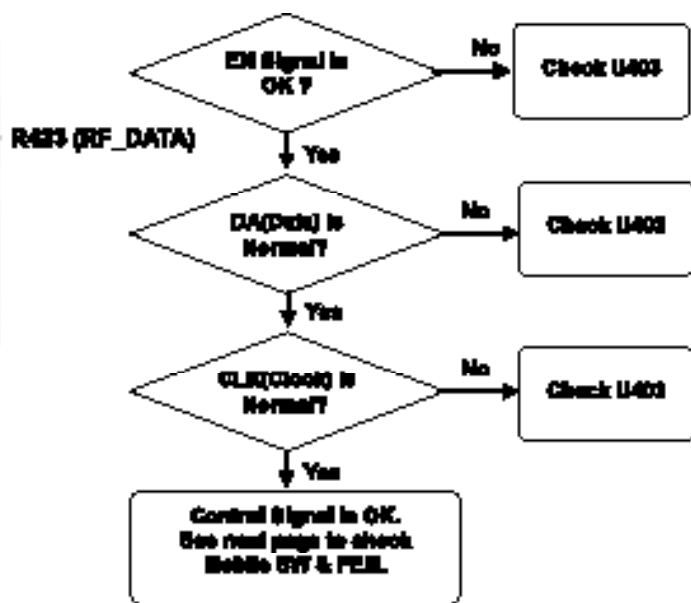
Waveform



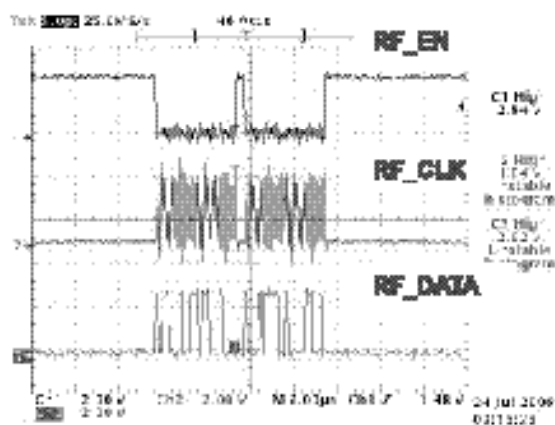
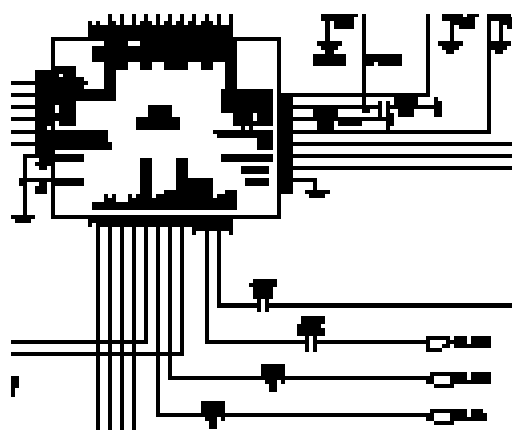
Checking Points

Checking Flow

Checking Flow

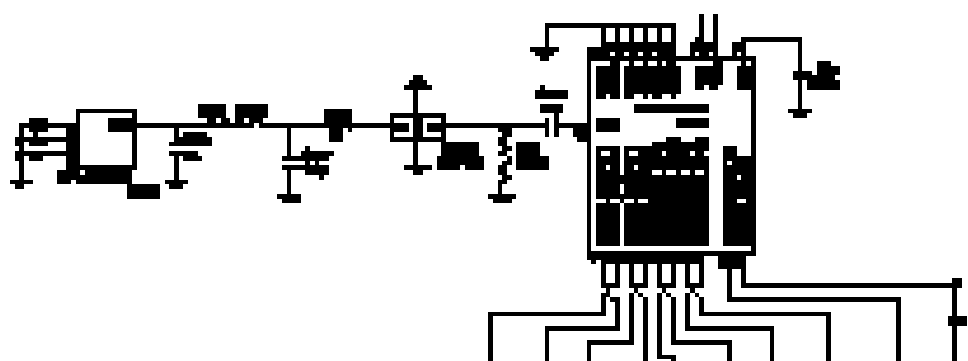


Waveform

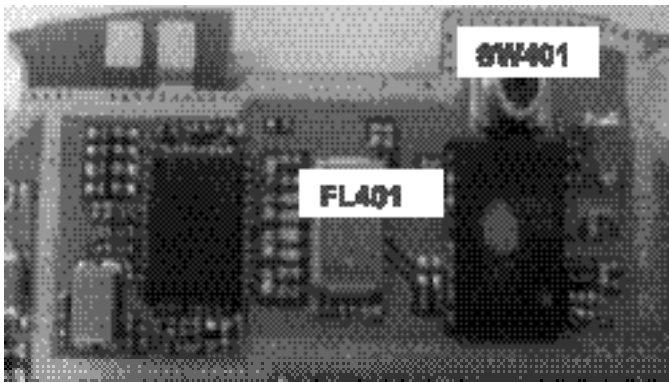


5.11.5 Checking Mobile SW & FEM

Mobile SW & FEM Circuit Diagram



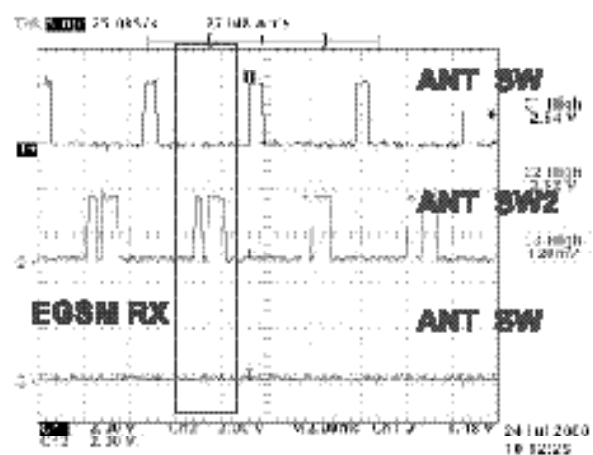
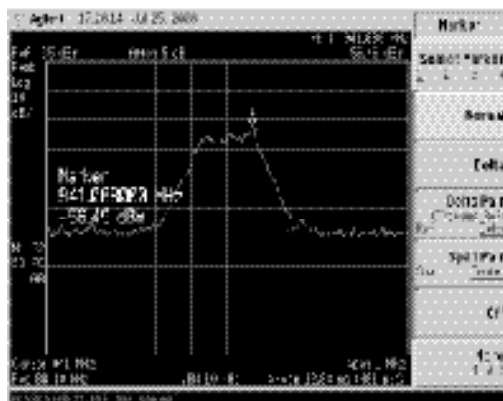
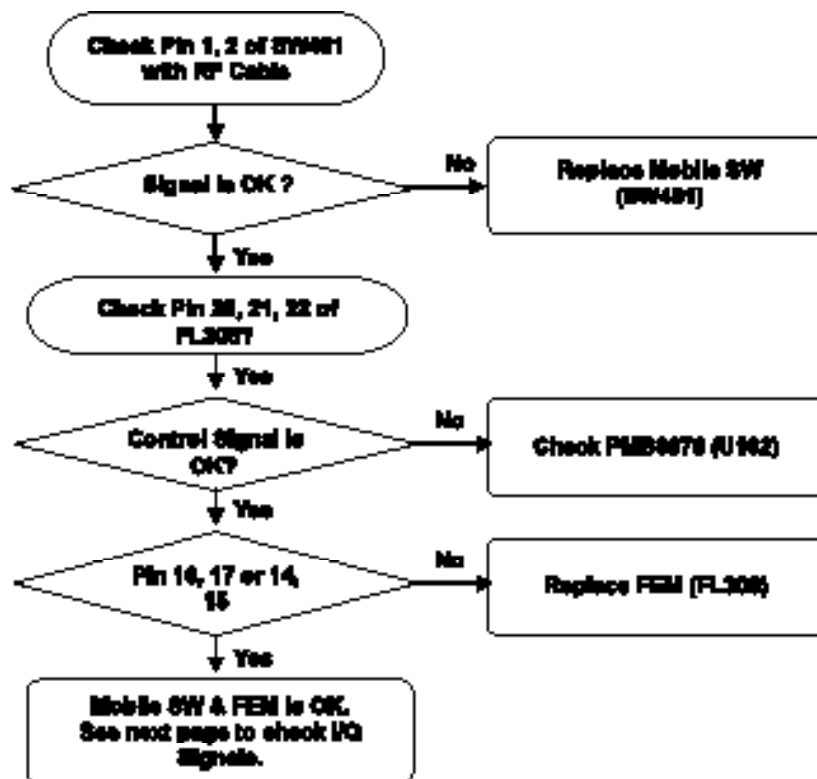
Checking Points



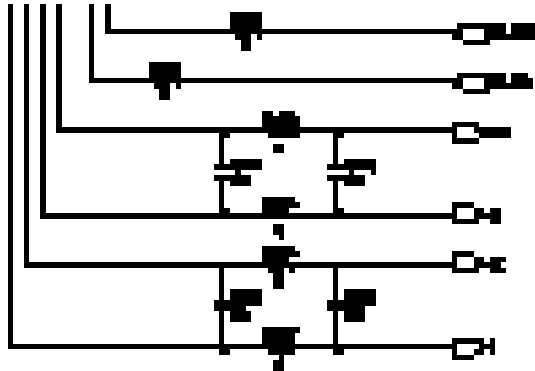
RX Mode	EGSM	DCS	PCS
ANT_SW1	Off	Off	Off
ANT_SW2	On	Off	Off

5. Trouble shooting

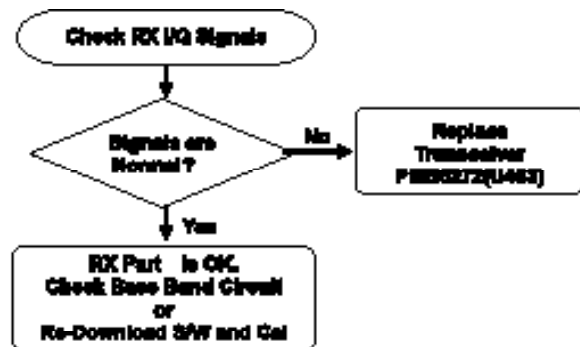
Checking Flow



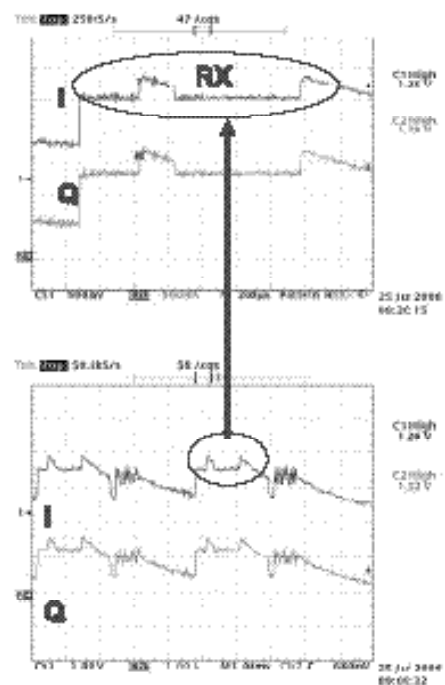
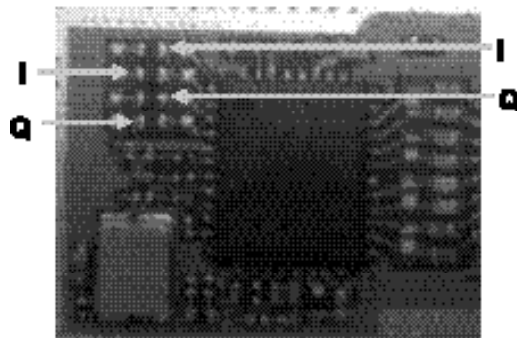
5.11.6 Checking RX I/Q Signals



Checking Flow

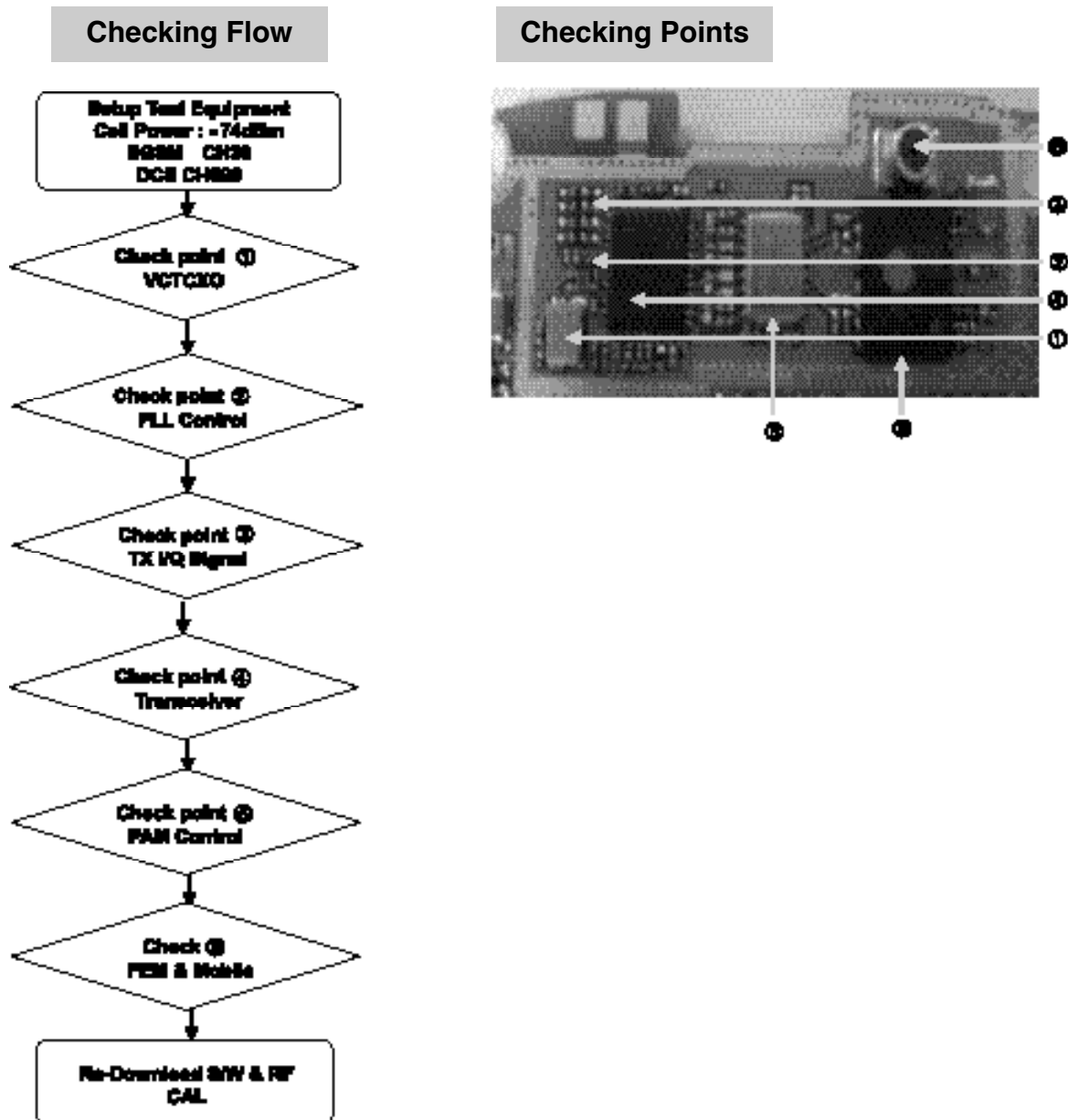


Checking Points



5. Trouble shooting

5.11.7 Shooting of Transmitter Part



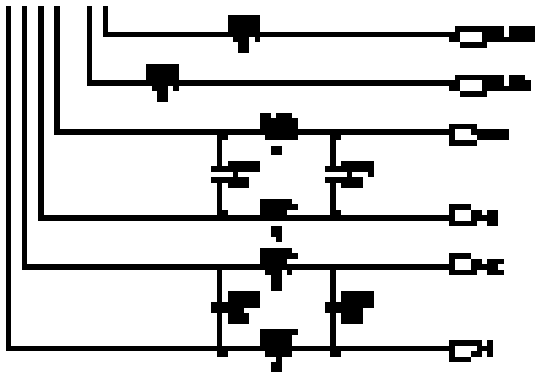
5.11.8 VCTCXO Circuit

See RX Part “1. Checking VCTCXO Circuit”

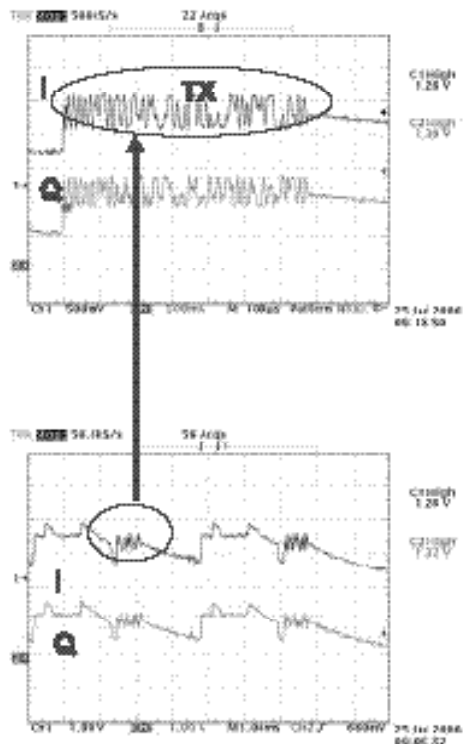
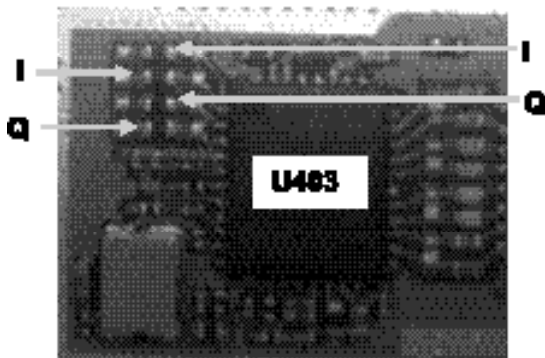
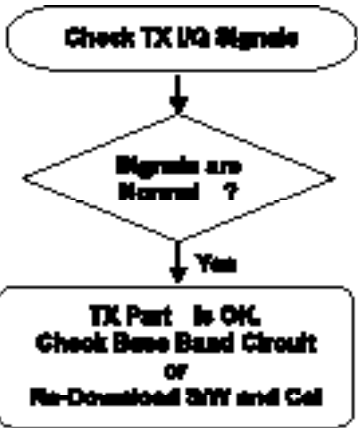
5.11.9 Checking PLL Control Signal

See RX Part “2. Checking PLL Control Signal”

5.11.10 Checking TX I/Q Signals

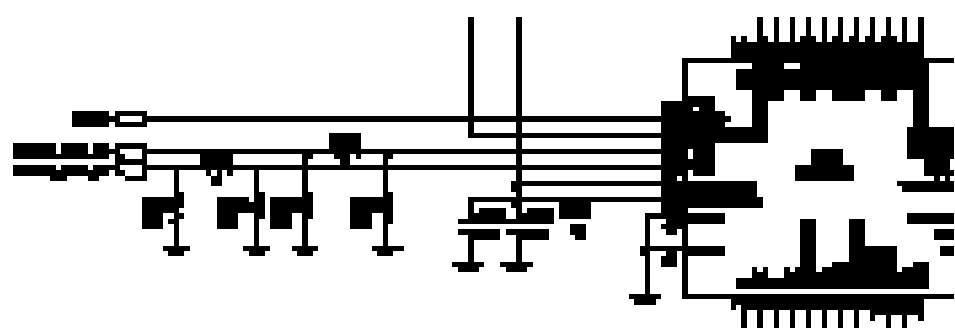


Checking Flow

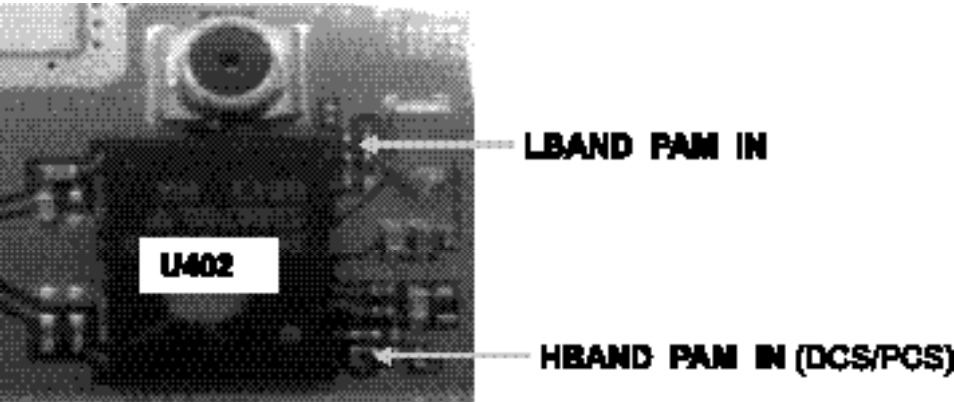


5. Trouble shooting

5.11.11 Checking Transceiver Output Signals

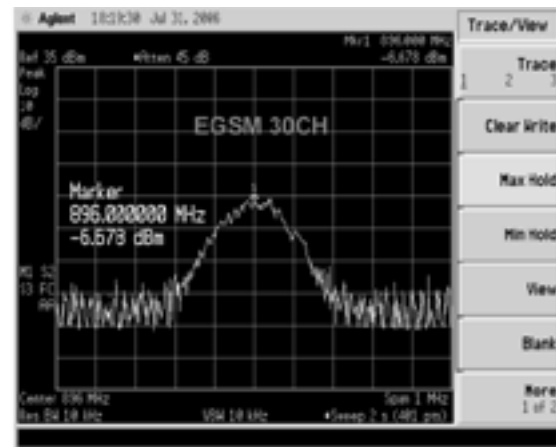
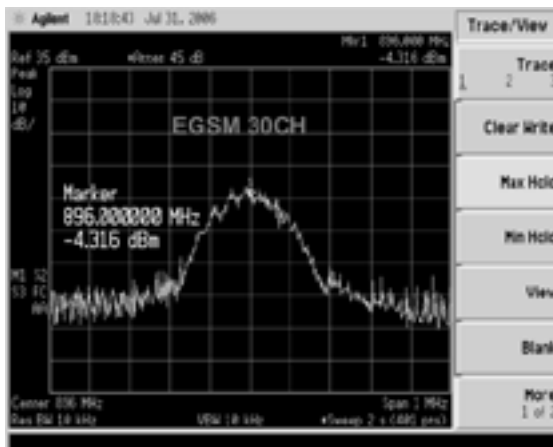
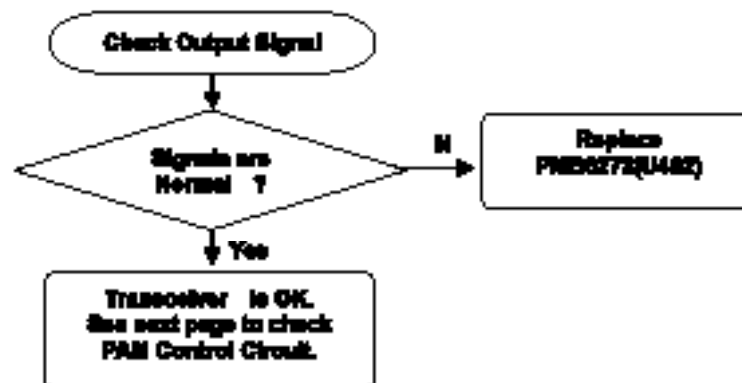


Checking Points



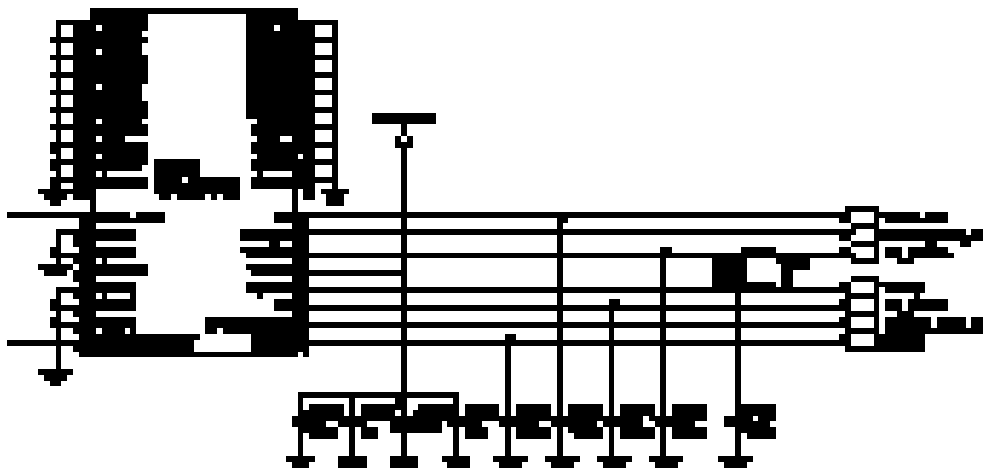
MODE	Transceiver Output
GMSK	Fixed
8PSK	Ramp Burst Control

Checking Flow

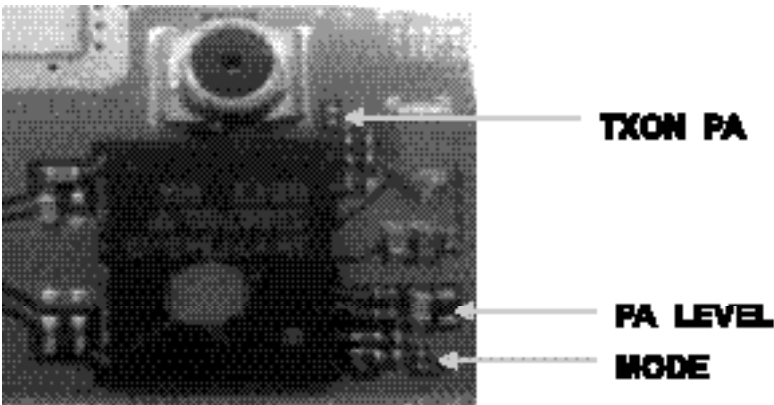


5. Trouble shooting

5.11.12 Checking PAM Control Signals

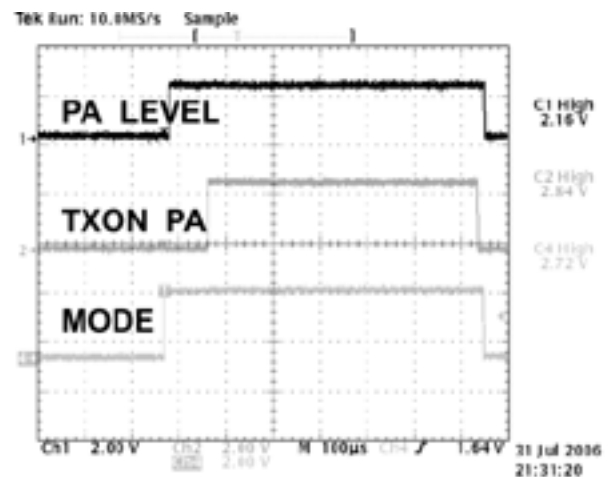
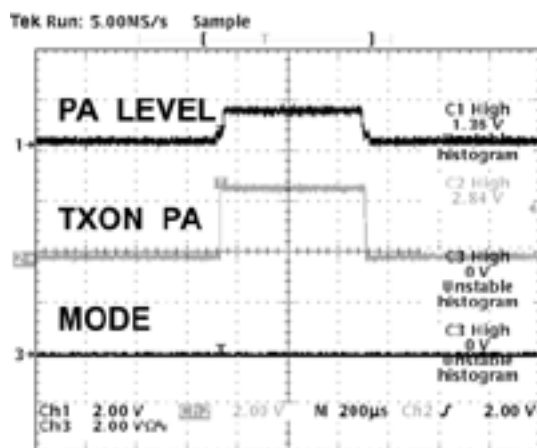
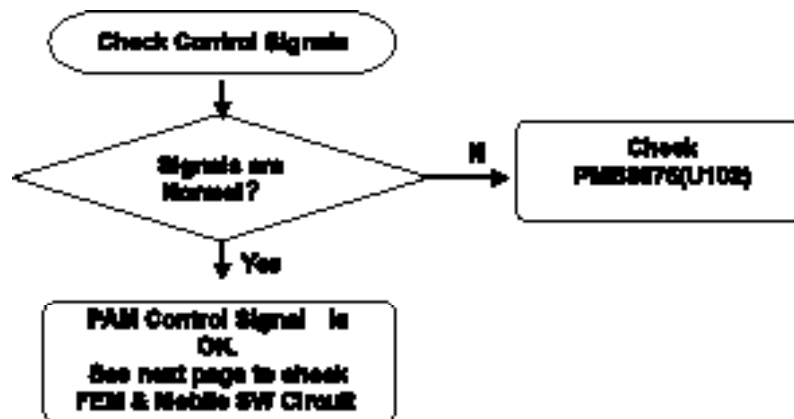


Checking Points



MODE	MODE	PA_LEVEL	TXON_PA
GMSK	LOW	Ramp Burst Control	HIGH
8PSK	HIGH	Control Amp bias	HIGH

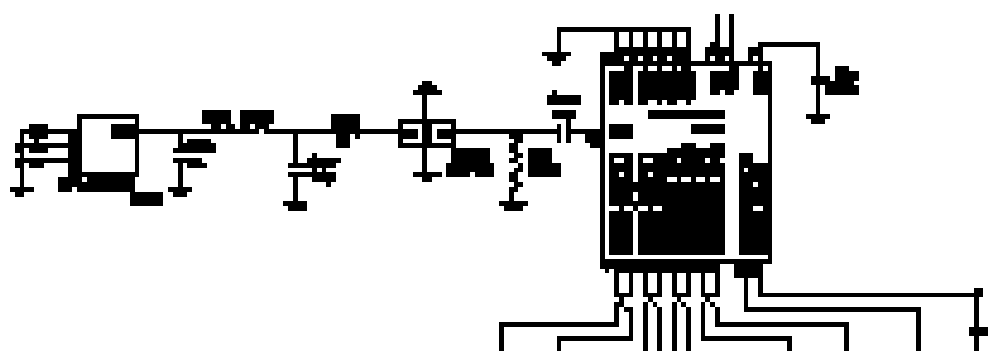
Checking Flow



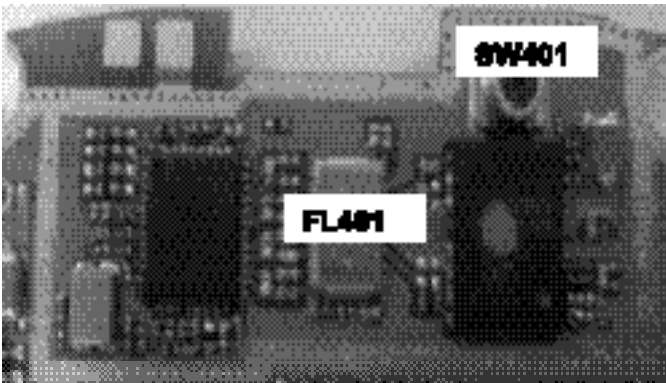
5. Trouble shooting

5.11.13 Checking FEM & Mobile SW

Mobile SW & FEM Circuit Diagram

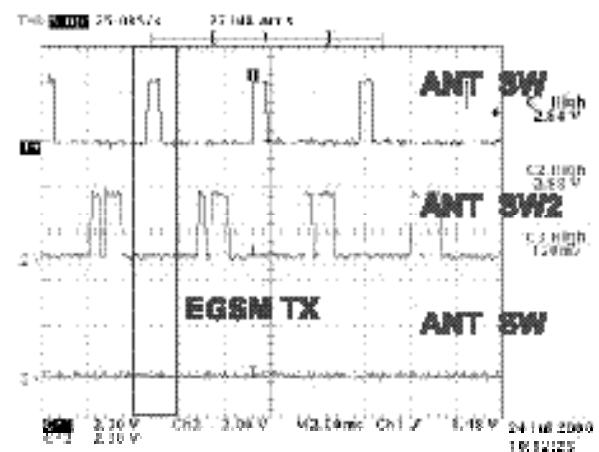
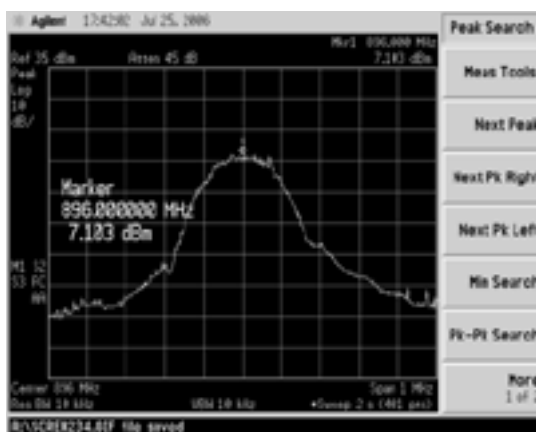
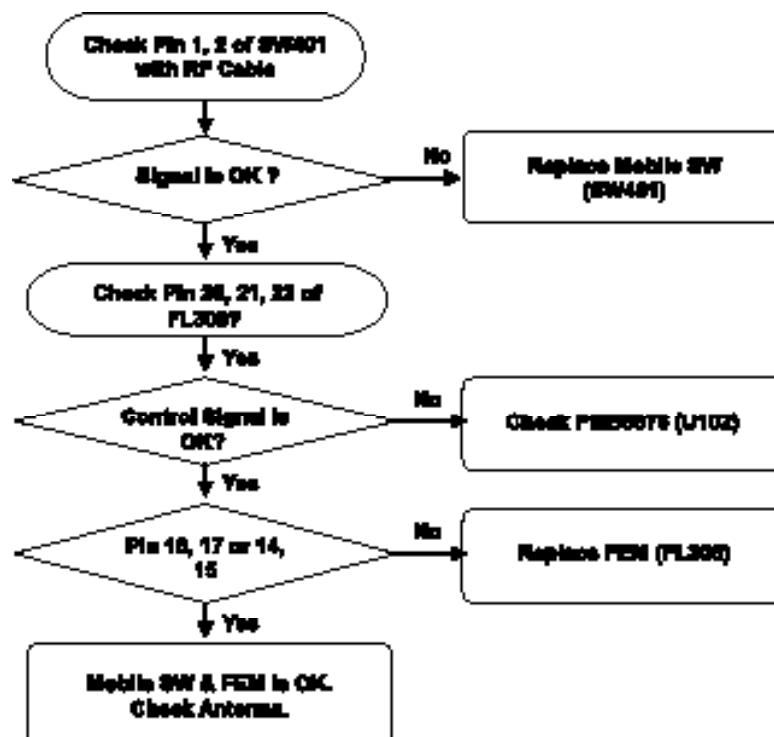


Checking Points



TX Mode	EGSM	DCS	PCS
ANT_SW1	On	Off	Off
ANT_SW2	Off	On	On

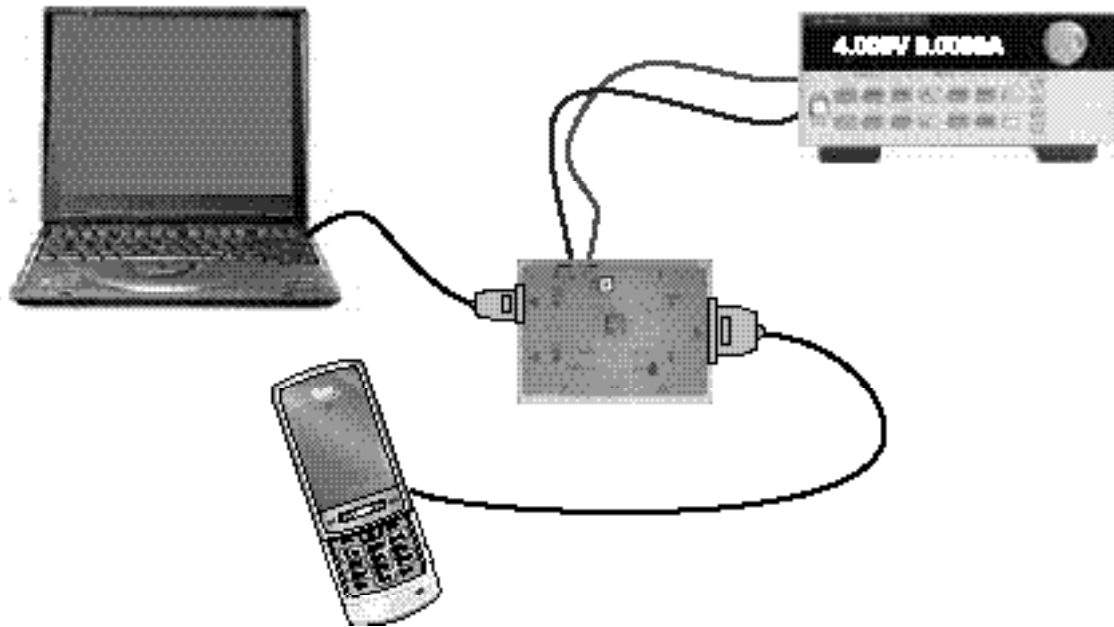
Checking Flow



6. Download & S/W upgrade

6. Download & S/W upgrade

6.1 S/W download setup



Preparation

- Target terminal
- PIF-Union
- RS-232 Cable and PIF-UNION to Phone interface Cable
- Power Supply or Battery
- IBM compatible PC supporting RS-232 with Windows 98 or newer.

If you are going to use battery, the voltage of the battery should be over 3.7V for stable power supplying during S/W download.

Execute Flashtool program, then below window will be appeared.
Click the OK button



6. Download & S/W upgrade

When the application is started first time the following screen appears.
Each section is described in the text below.

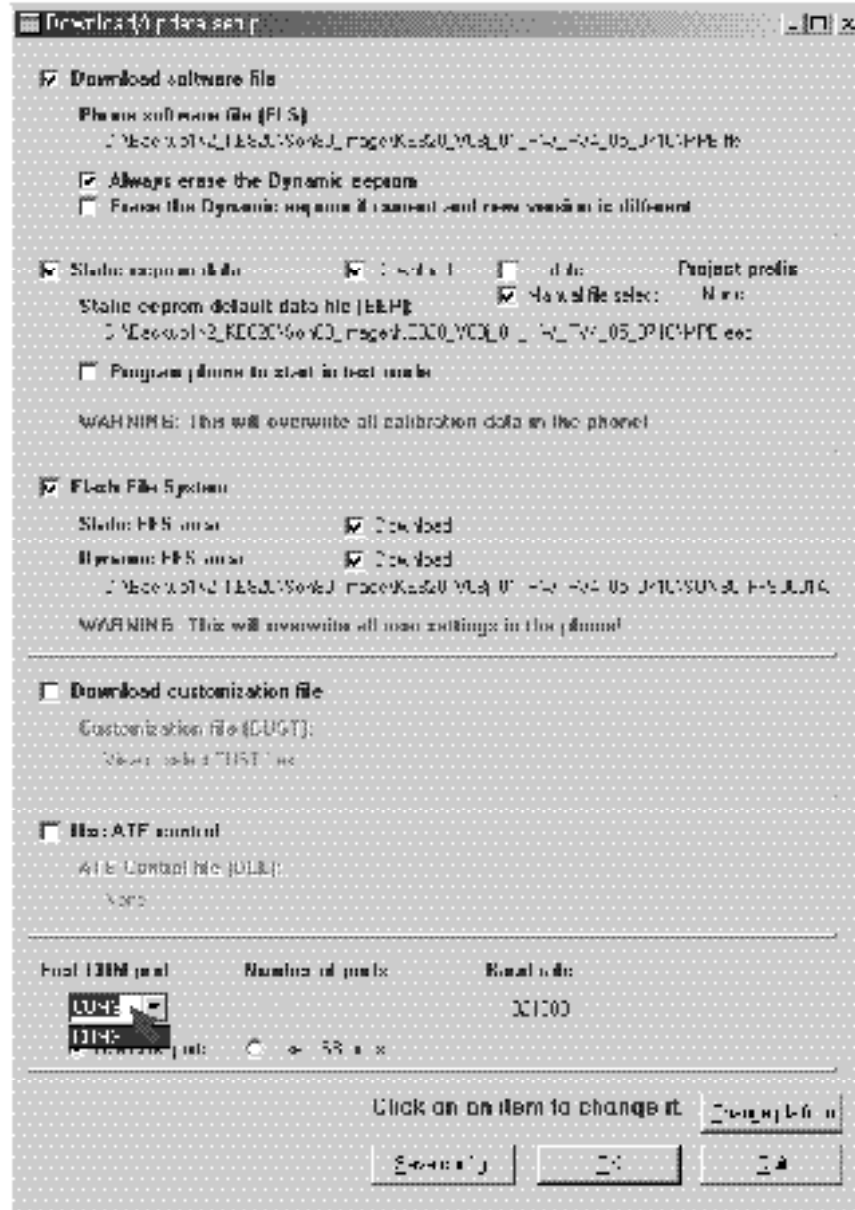
Click the check box to enable or disable file download.
Click on the blue text to select the file to download.
This will open a normal file select box. Select the wanted file.

The screenshot shows a window titled "Download/Upgrade screen" with the following sections:

- Download software file:** Includes a checked checkbox, a text field for "Phone software file (PLB):" with a file path, and two checkboxes: "Always erase the Dynamic eeprom" (checked) and "Erase the Dynamic eeprom if current and new version is different" (unchecked).
- Static eeprom data:** Includes a checked checkbox, radio buttons for "Static" (selected) and "Dynamic", a "Project prefix" field, a checked "Manual file select" checkbox, a text field for "Static eeprom default data file (EEP):" with a file path, and an unchecked checkbox for "Program phone: In start or last mode:". Below this is a warning: "WARNING: This will overwrite all calibration data in the phone!".
- Flash File System:** Includes a checked checkbox, radio buttons for "Static" (selected) and "Dynamic", a text field for "Static FFS area:" with a file path, and a text field for "Dynamic FFS area:" with a file path. Below this is a warning: "WARNING: This will overwrite all user settings in the phone!".
- Download customization file:** Includes an unchecked checkbox, a text field for "Customization file (CUST):", and a link "View or select CUST file".
- Use ATF manual:** Includes an unchecked checkbox, a text field for "ATF Control file (ULL):", and a link "View".
- Hardware settings:** A table with columns "First LUM part:", "Number of ports:", and "Baud rate:". The first row shows "111P2", "1", and "41111". The second row shows "R111111111" and "U111111111".
- Buttons:** "Click on an item to change it.", "Change pattern", "Save config", "OK", and "Cancel".

6. Download & S/W upgrade

Click on the blue text to select the COM port.



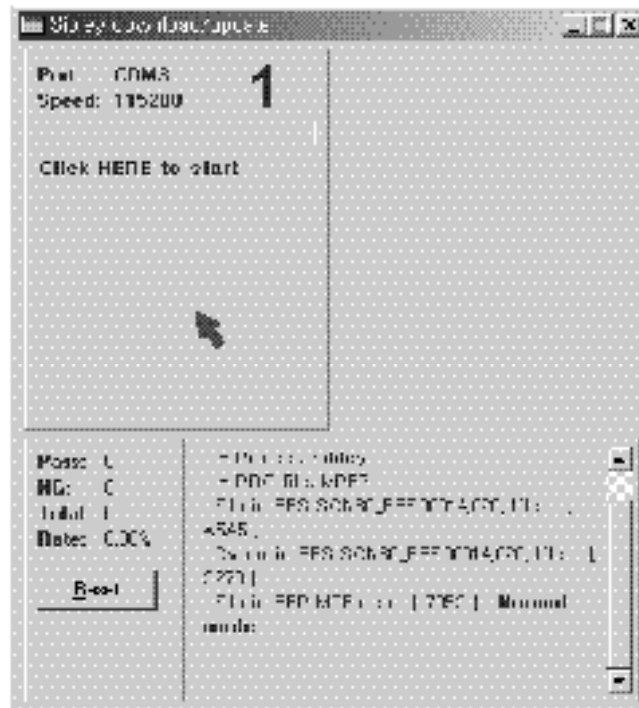
6. Download & S/W upgrade

Click on the blue text to select the Baud rate.

Click OK button to next step.



Will change the window as below



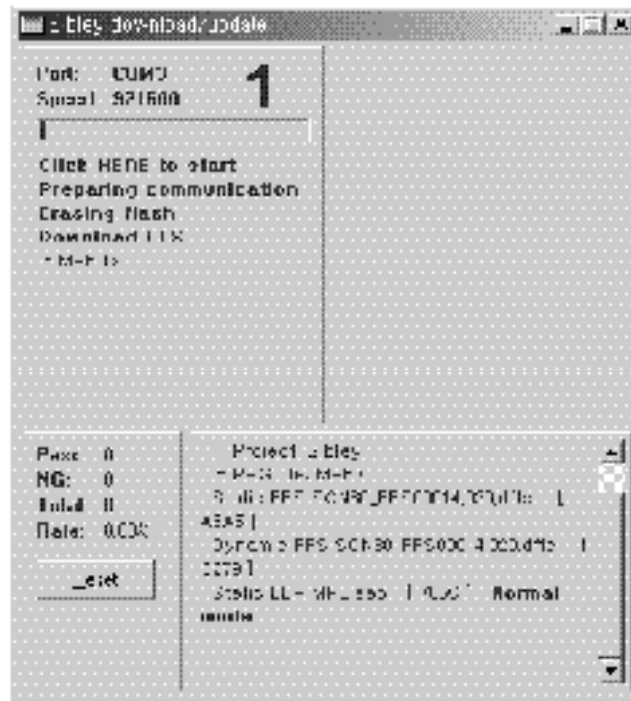
Click to anywhere on the control panel to start download.

Can see the “Reset the phone/module” then remote power on the target phone

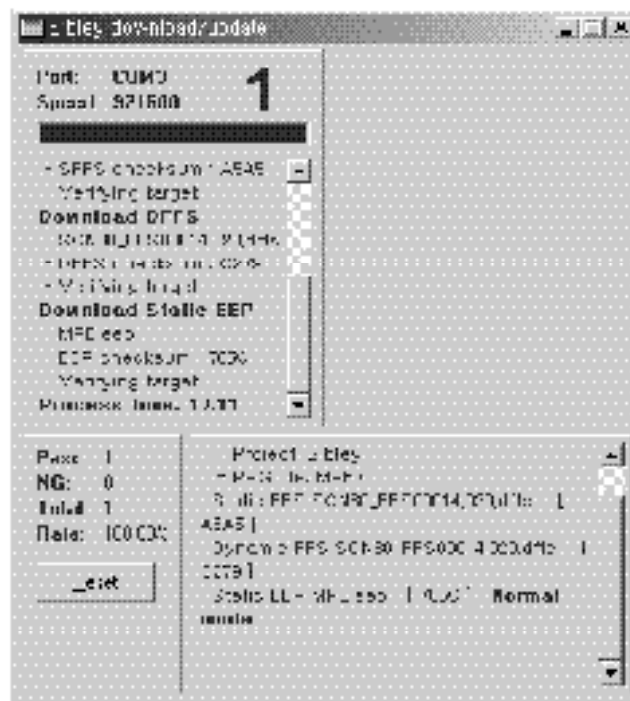


6. Download & S/W upgrade

During download, the screen will look something like this:



The blue bar shows the download progress. The FLS filename and the expected checksum are shown. The download statistics are shown. Click "Reset" to reset the counters.



After download, the status is shown.

6. Download & S/W upgrade

If there is a need to stop the download process, click on the panel for the channel to be stopped.

To stop the download the panel must be clicked twice.

This is to avoid that the download is stopped accidentally.

Furthermore, to avoid that the download is stopped on a mouse double-click, there must be at least 0.5 second between the two clicks.

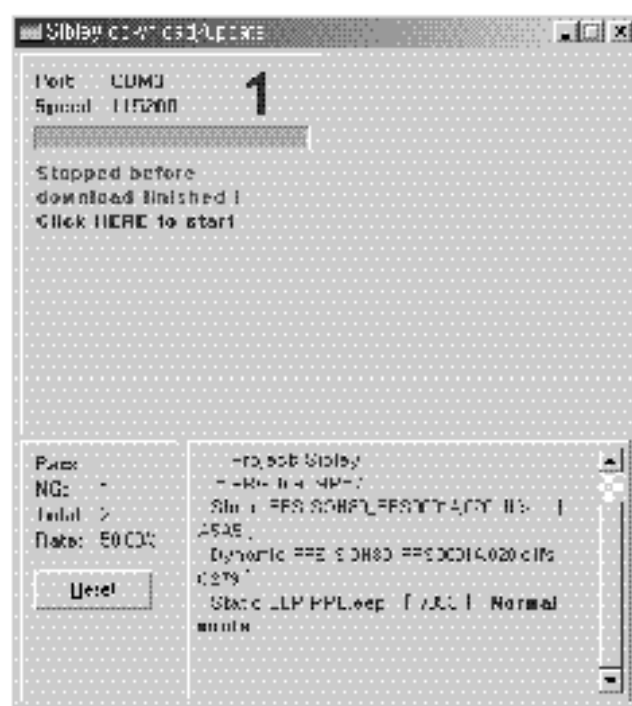
If the panel is clicked only once, the text "Click again to stop" will disappear and the download will continue.



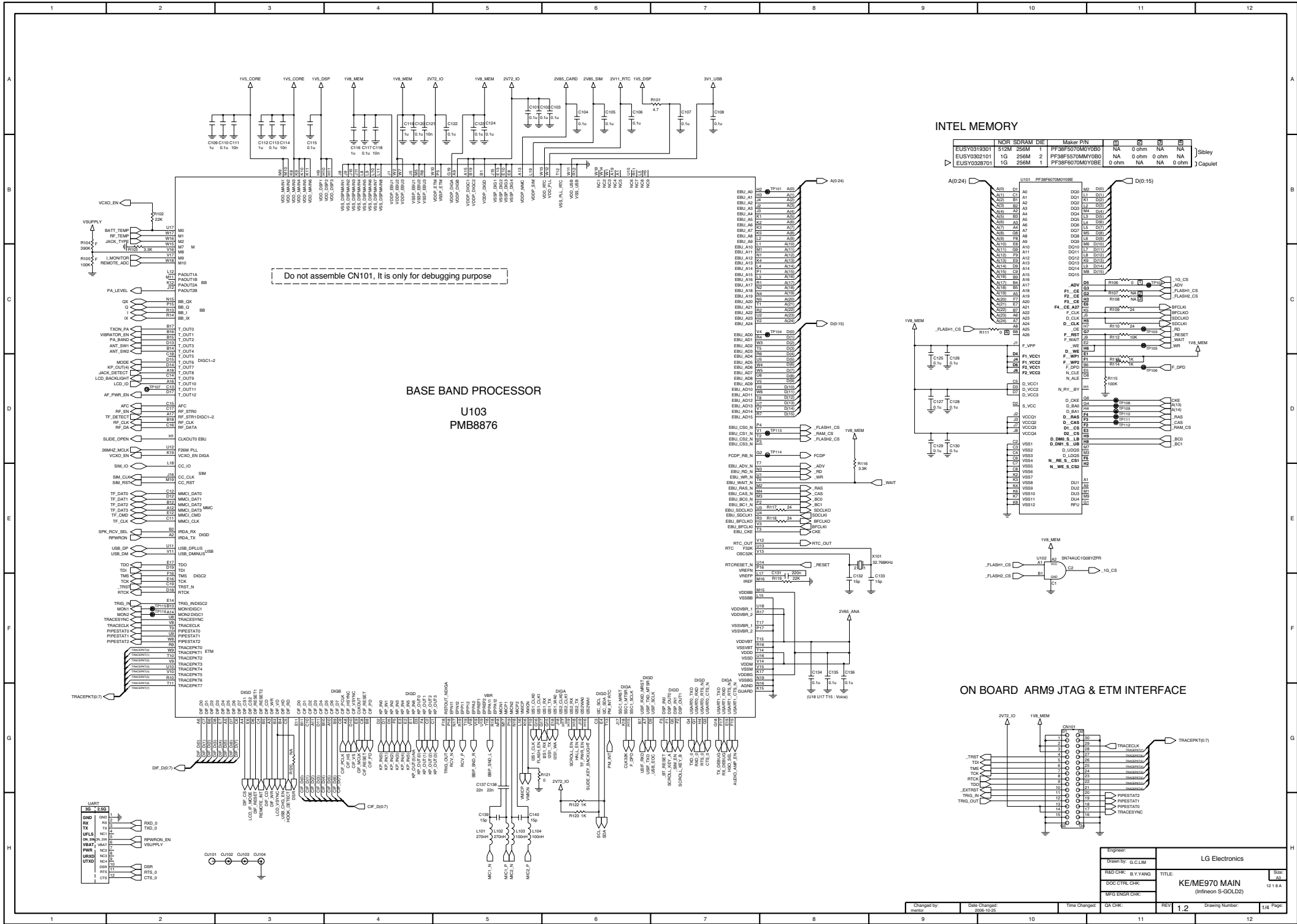
If a valid second click is detected, the download process is stopped and the progress bar turns yellow. At this point the download can be started from the beginning as usually.

NOTE: That the download statistic is unaffected when the download is stopped manually.

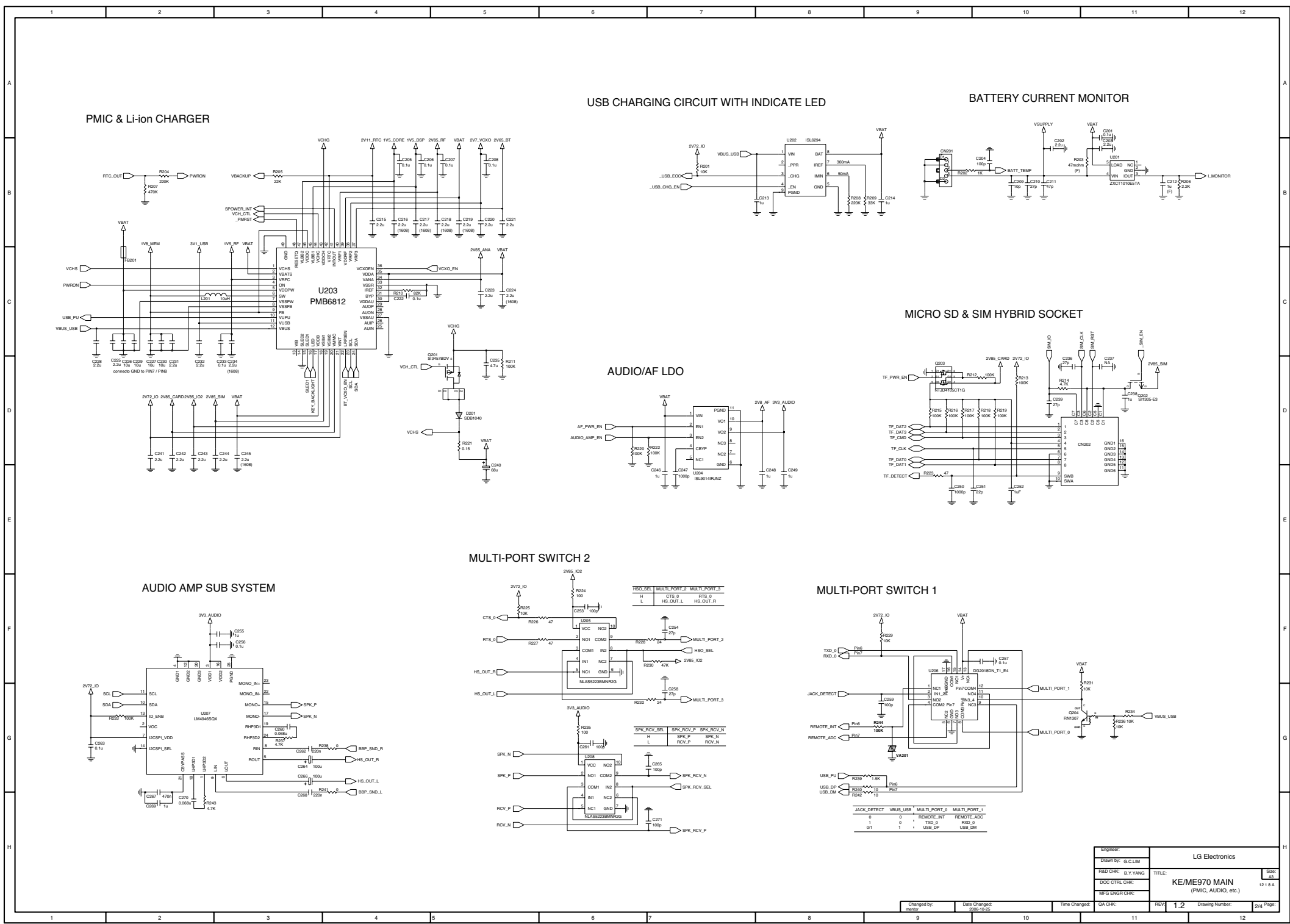
6. Download & S/W upgrade



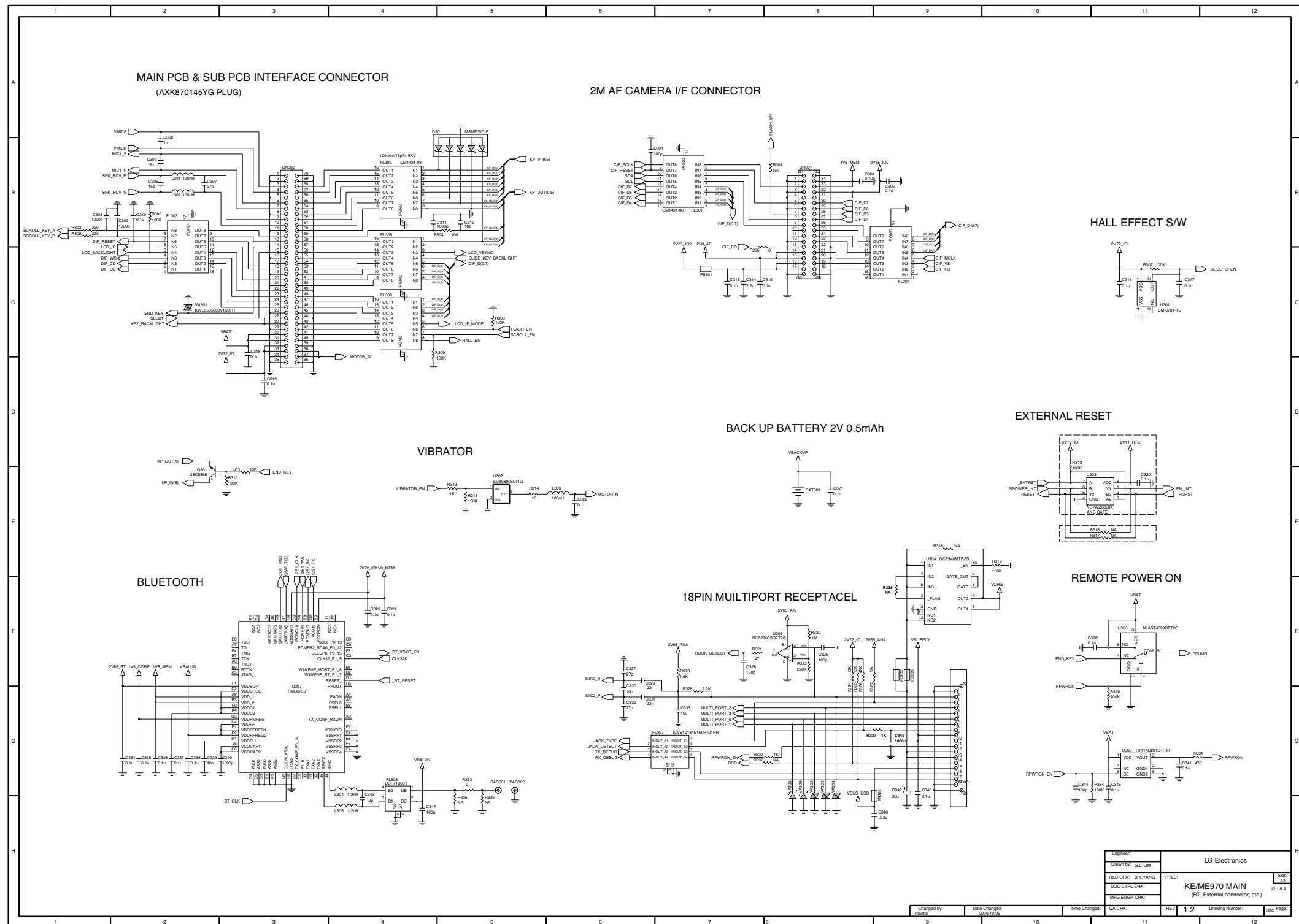
7. CIRCUIT DIAGRAM



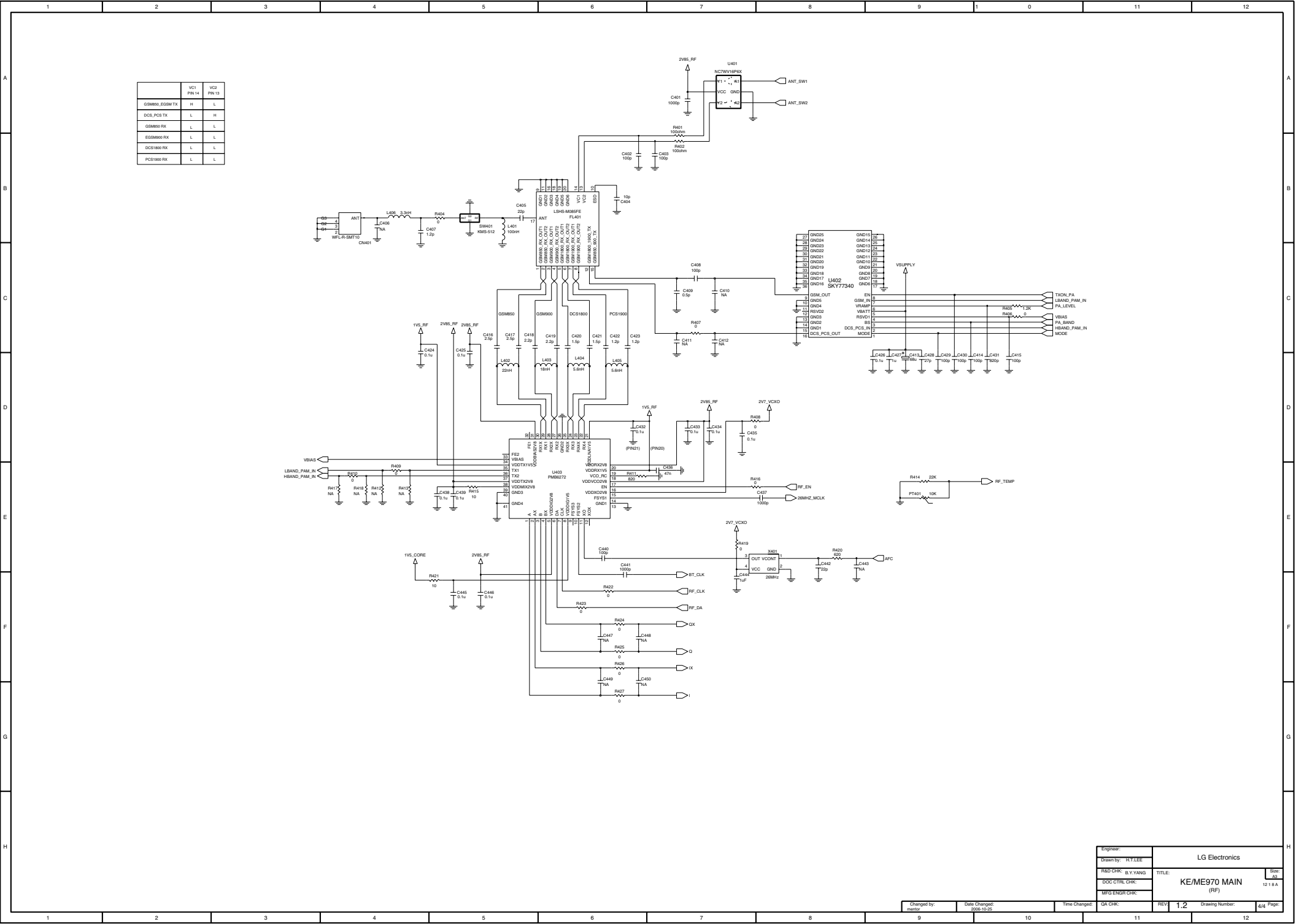
7. CIRCUIT DIAGRAM



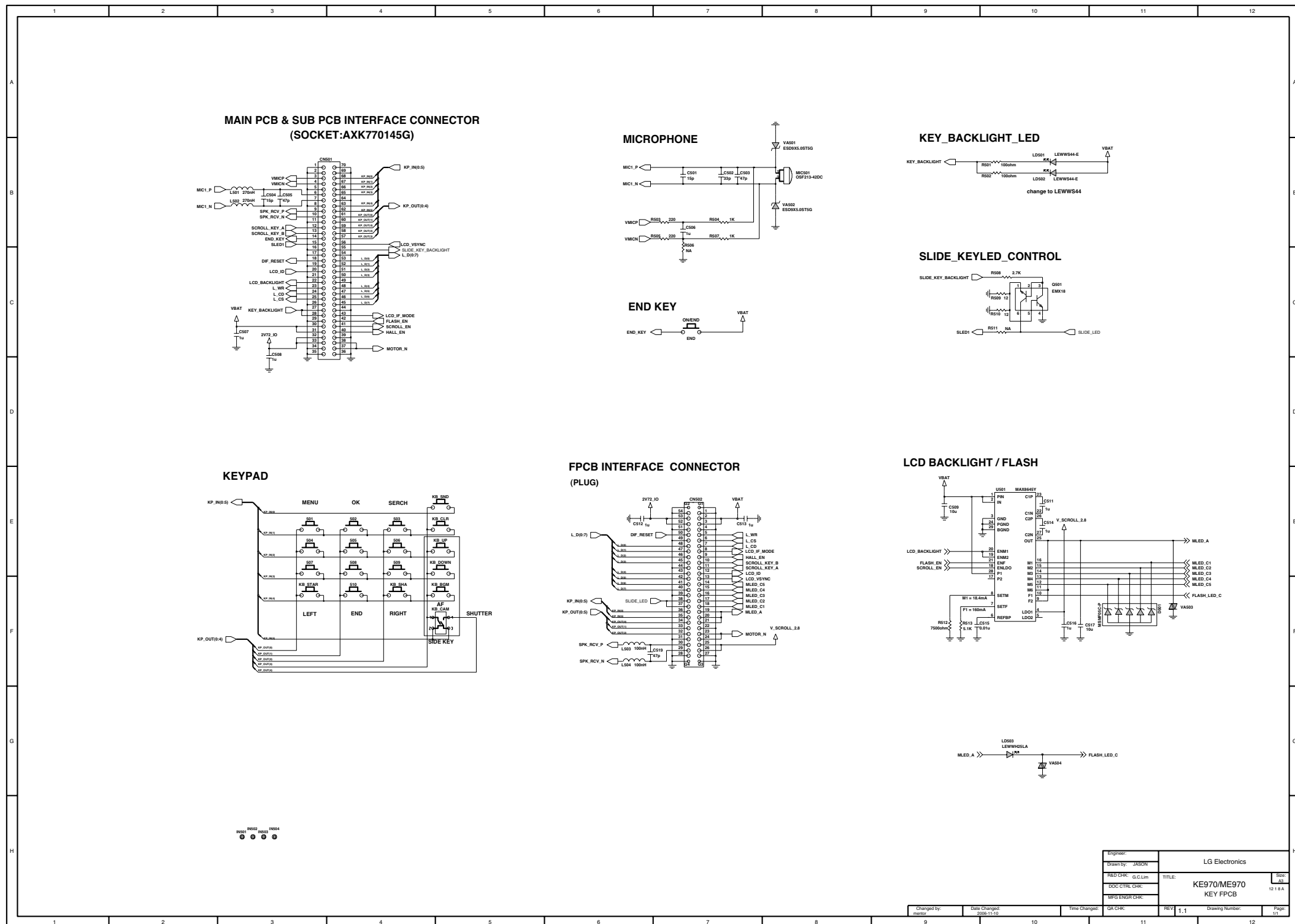
7. CIRCUIT DIAGRAM



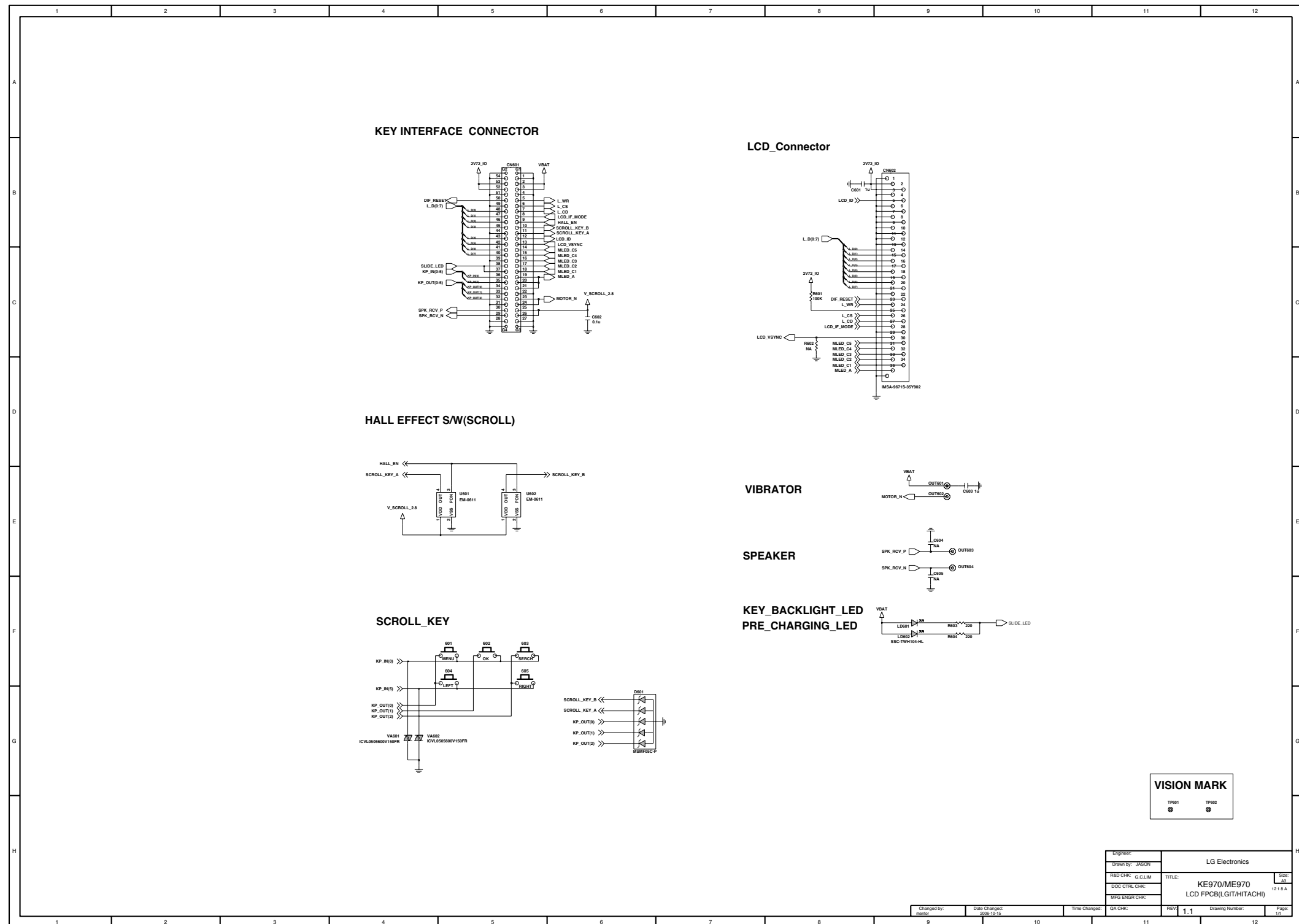
7. CIRCUIT DIAGRAM



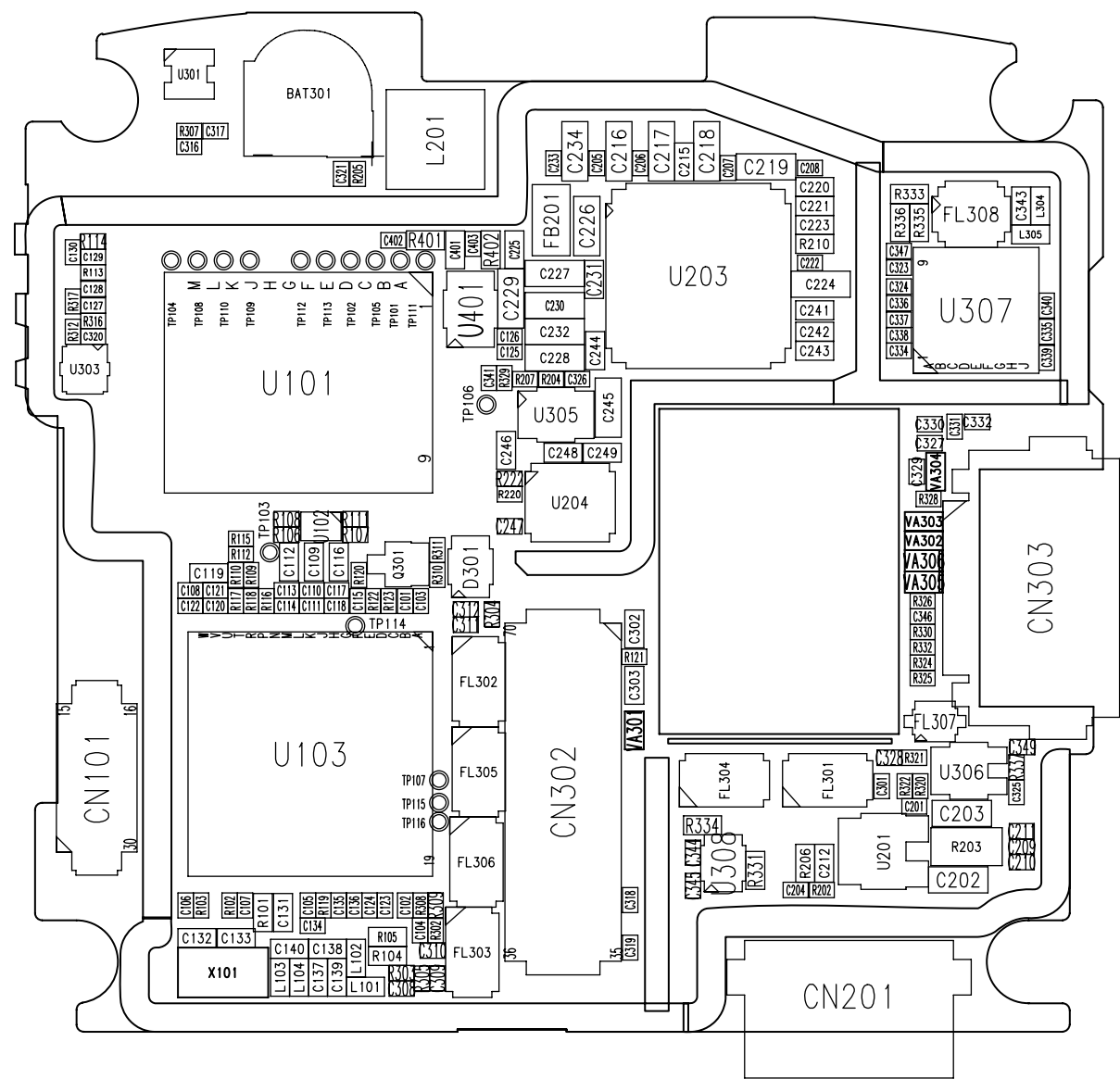
7. CIRCUIT DIAGRAM



7. CIRCUIT DIAGRAM

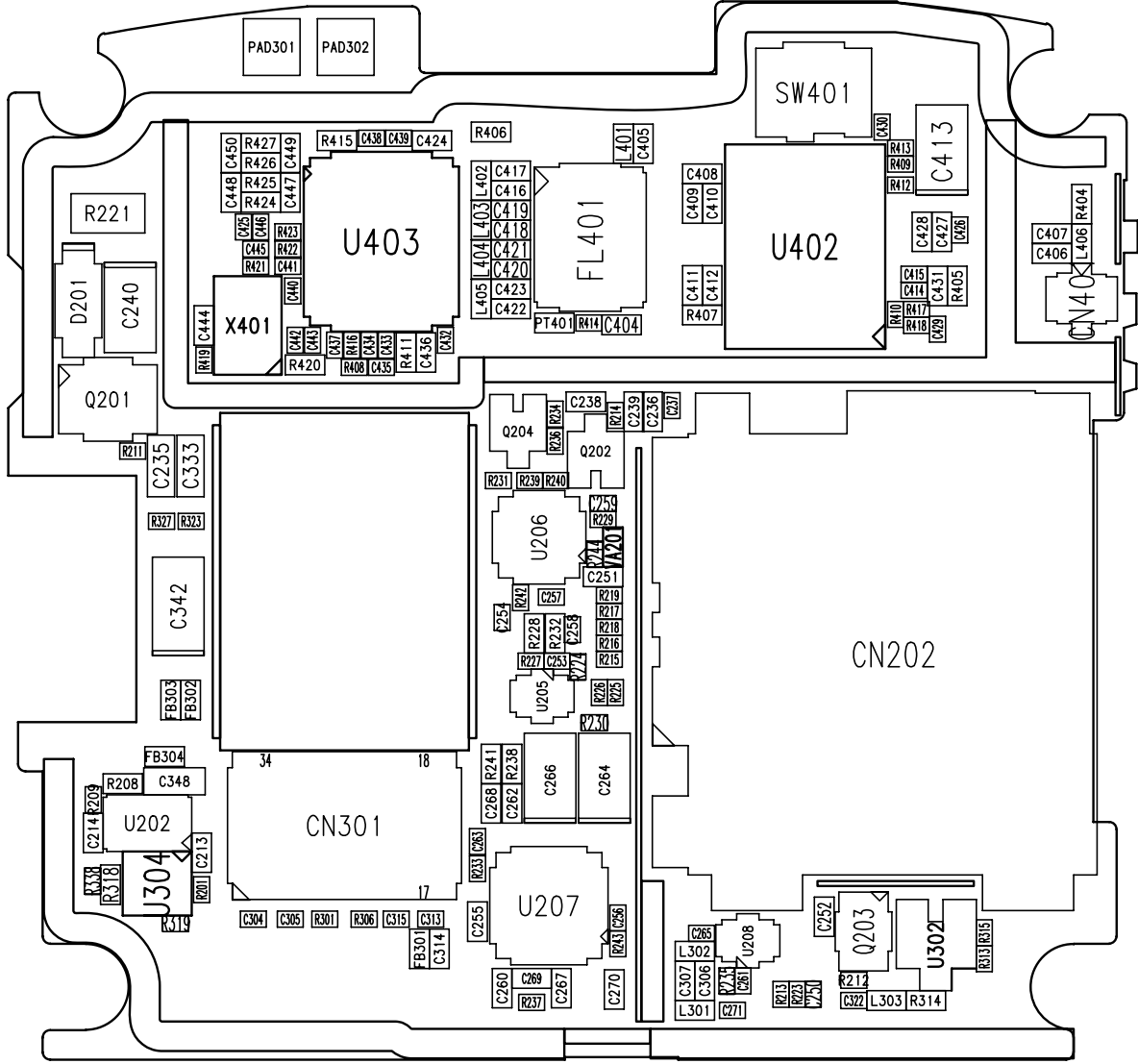


8. PCB LAYOUT



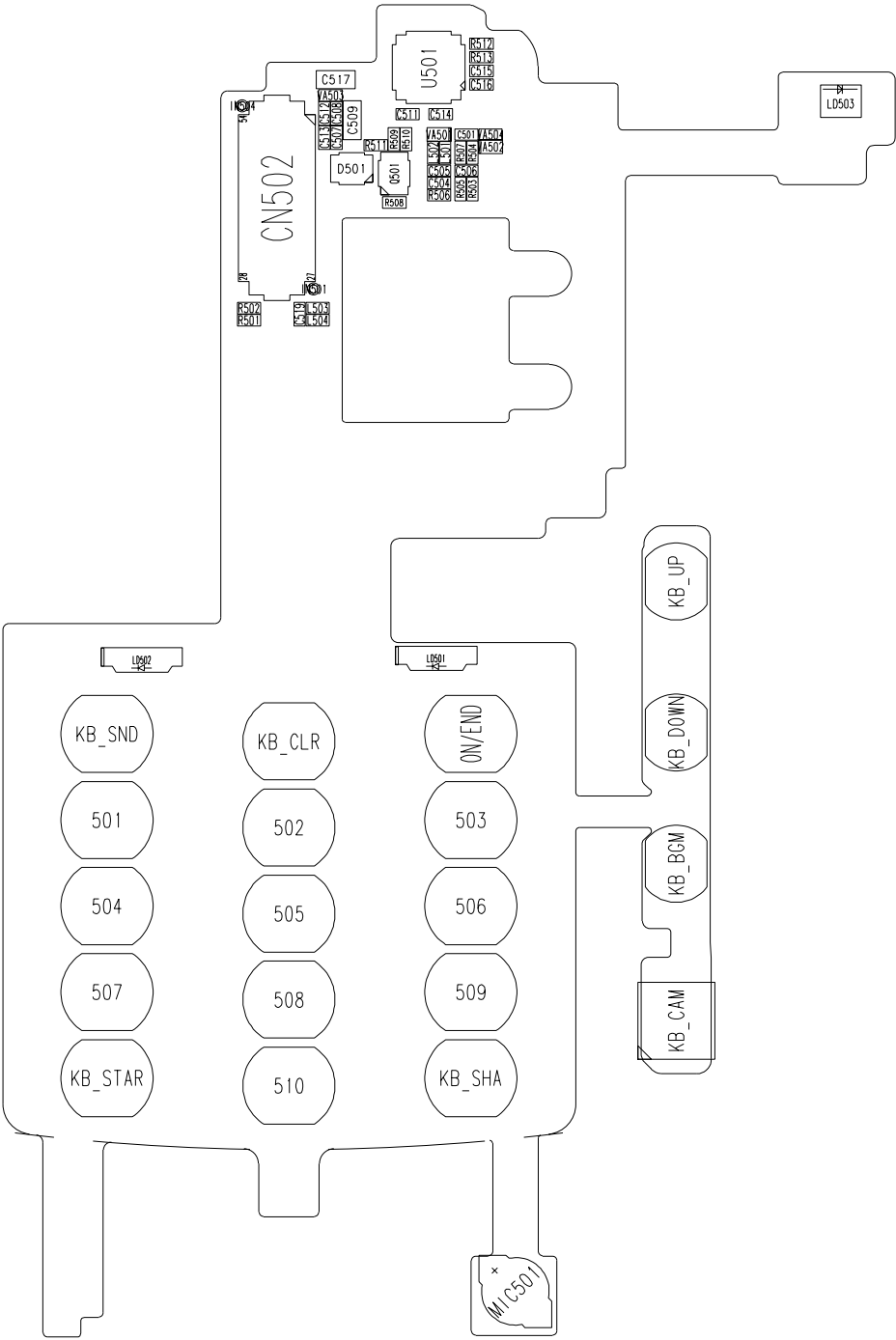
ME970-MAIN-SPFY0136901-1.2-BOTTOM

8. PCB LAYOUT



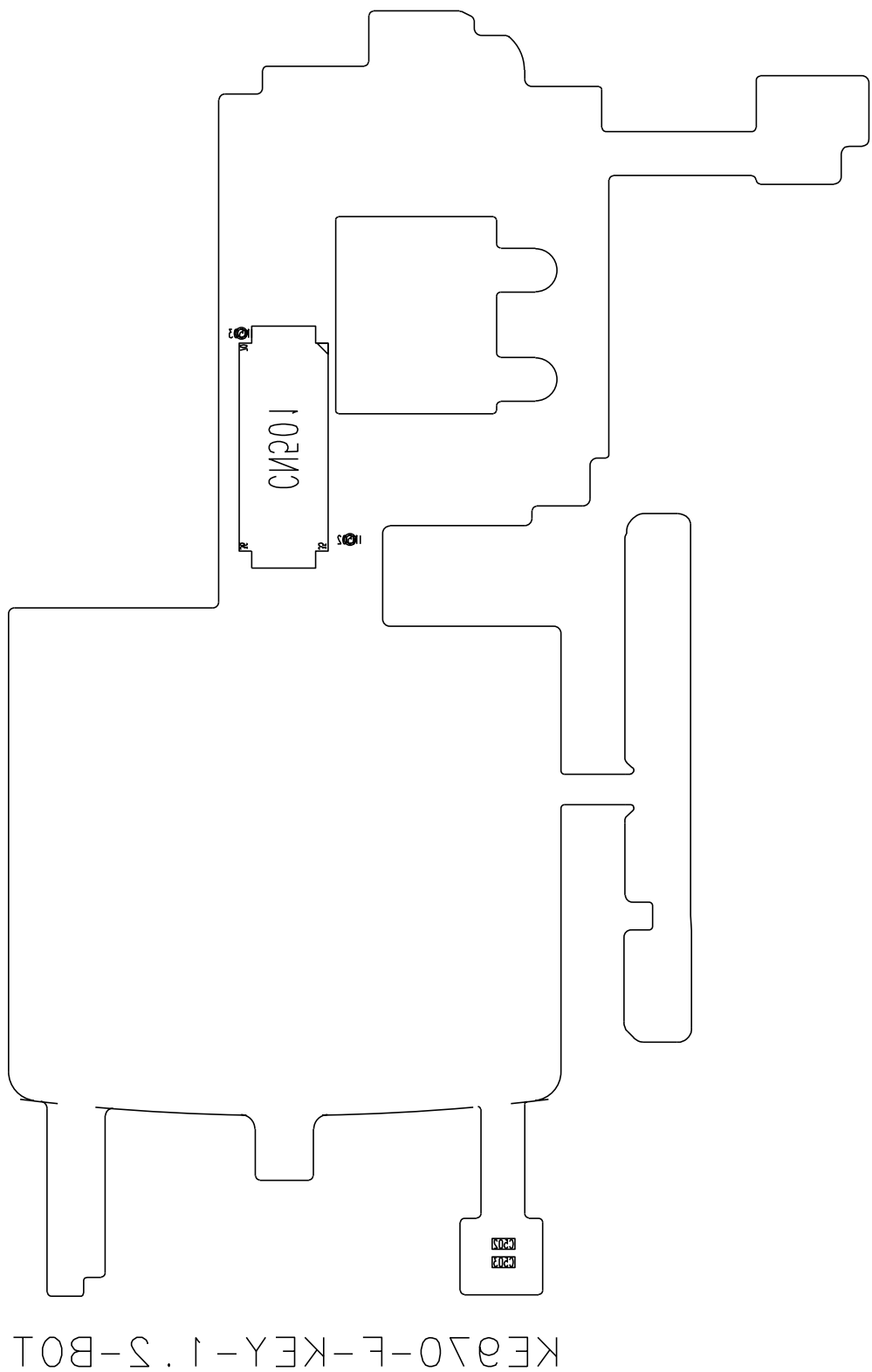
ME970-MAIN-SPFY0136901-1.2-TOP

8. PCB LAYOUT

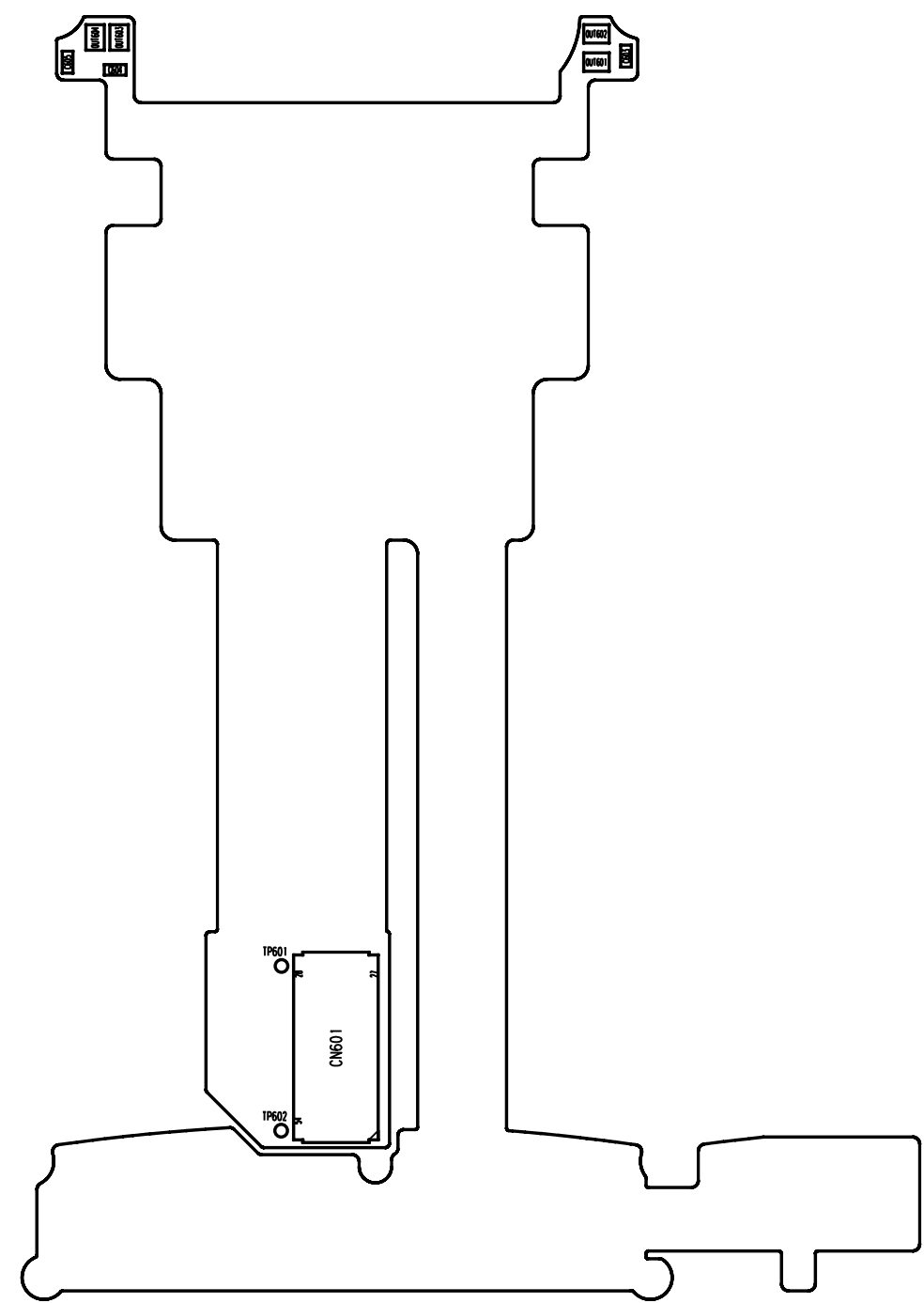


ME970-F-KEY-1.2-TOP

8. PCB LAYOUT

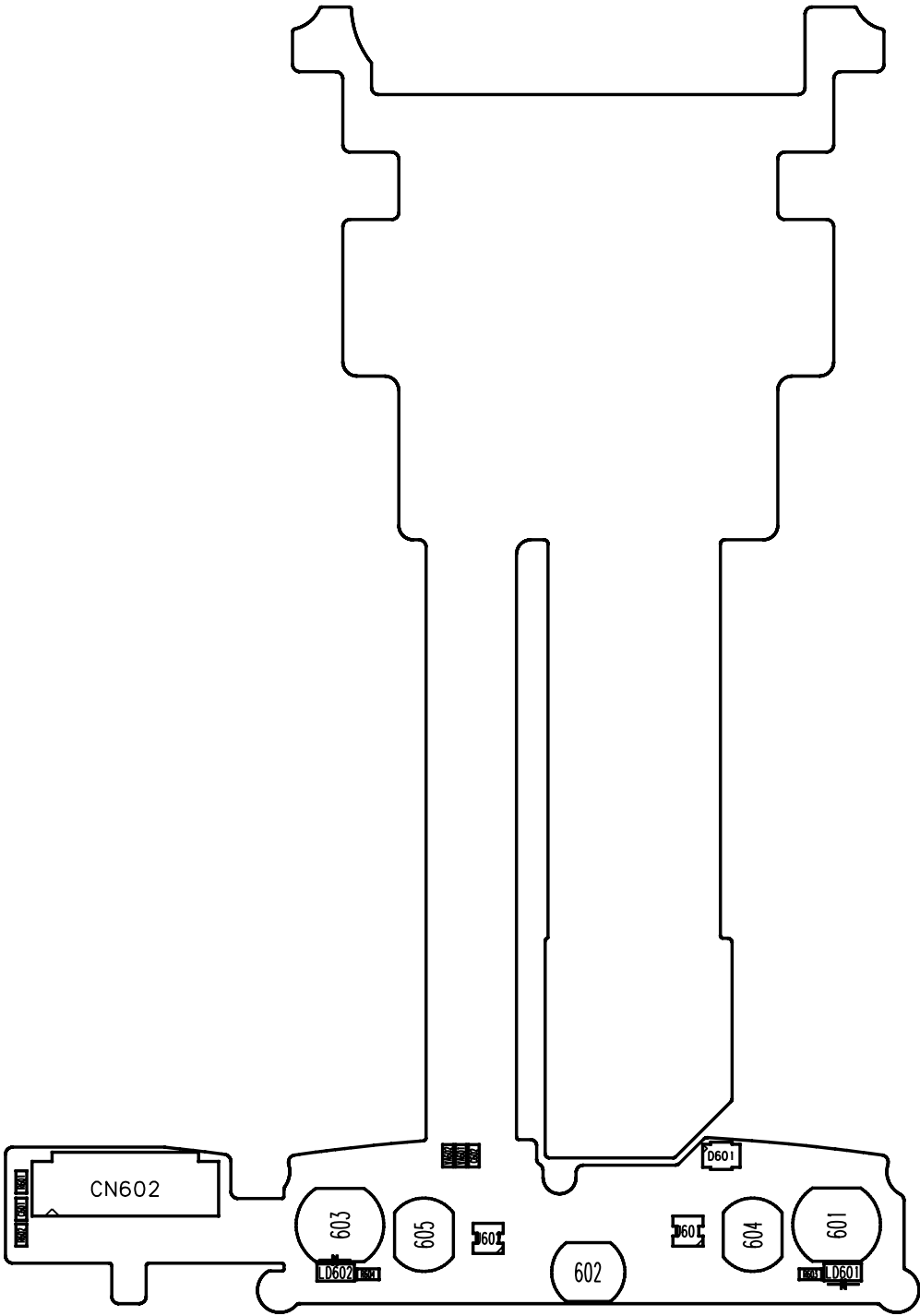


8. PCB LAYOUT



ME970-F_LCD-SPCY0088301-1.1

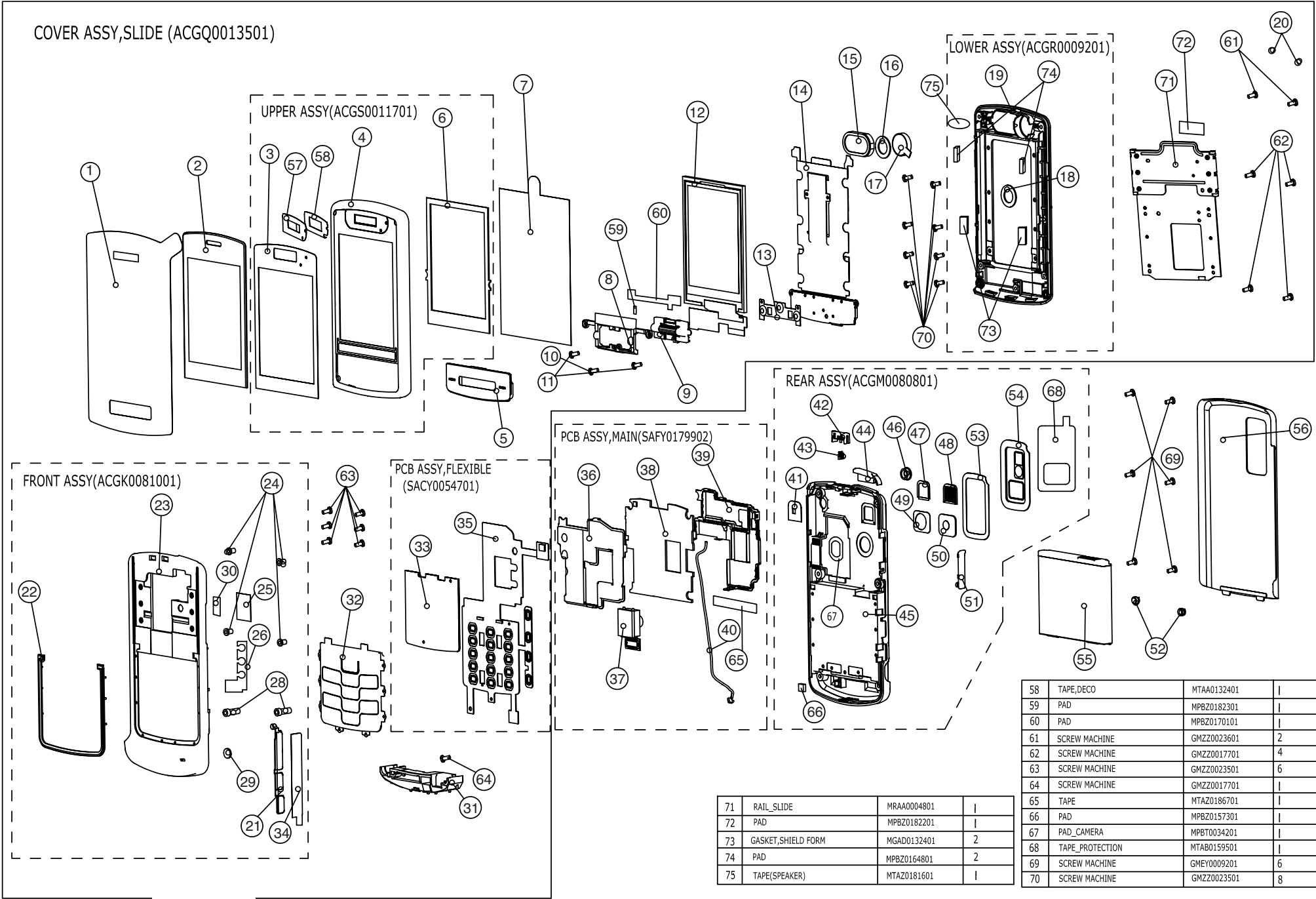
8. PCB LAYOUT



ME970-F_LCD-SPCY0088301-1.1

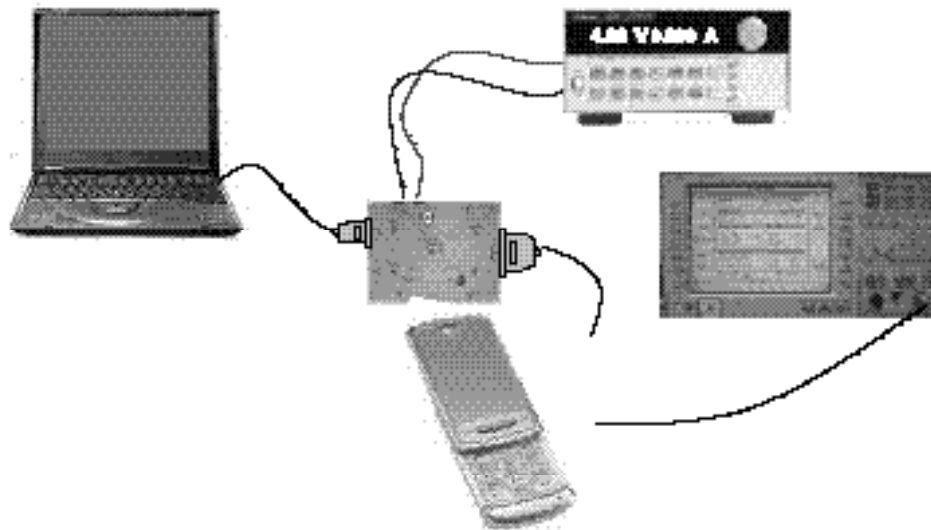
12. EXPLODED VIEW & REPLACEMENT PART LIST

12.1 EXPLODED VIEW



9. RF Calibration

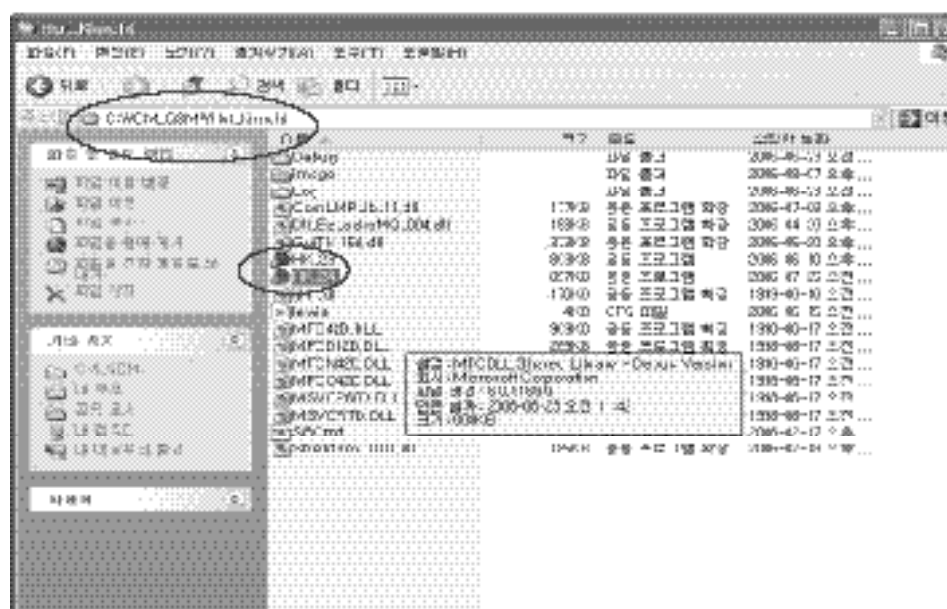
9.1 Test Equipment Setup



9.2 Calibration Steps

9.2.1. Turn on the Phone.

9.2.2. Execute “HK_24.exe”

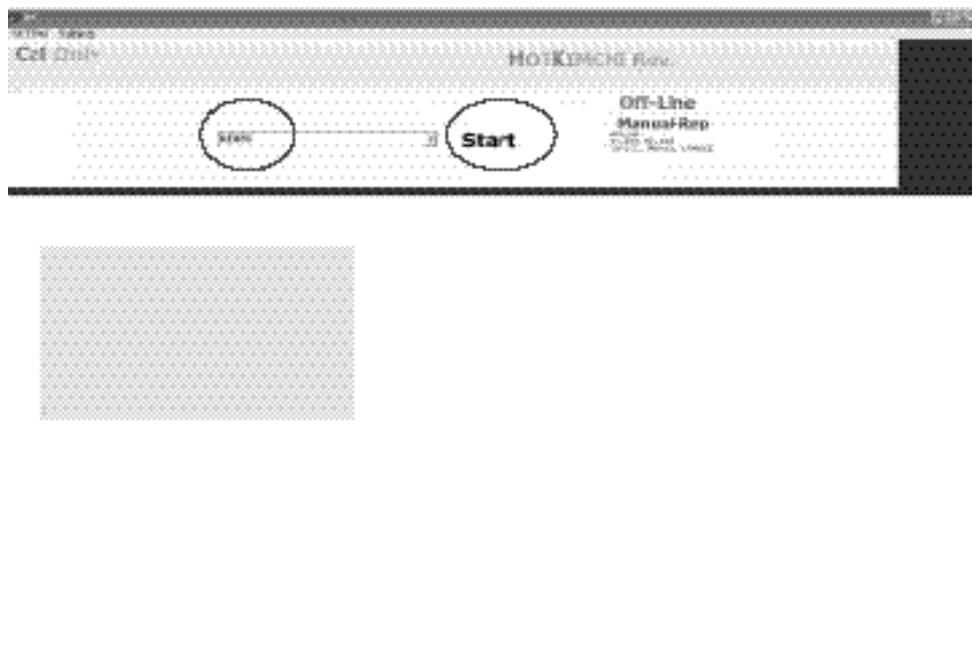




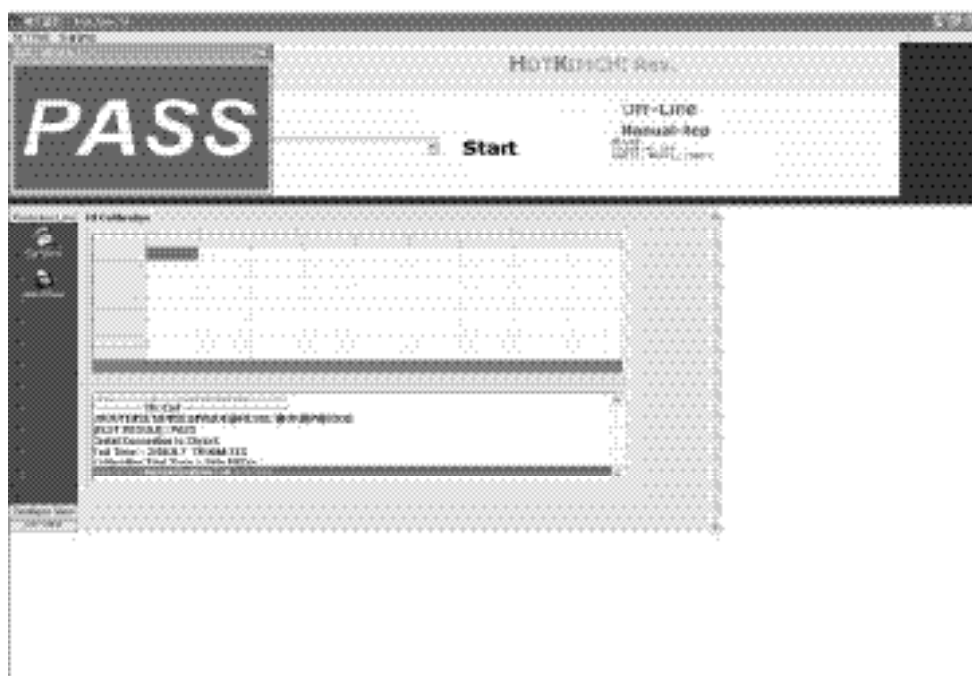
9. RF Calibration

9.2.7. Select “MODEL”.

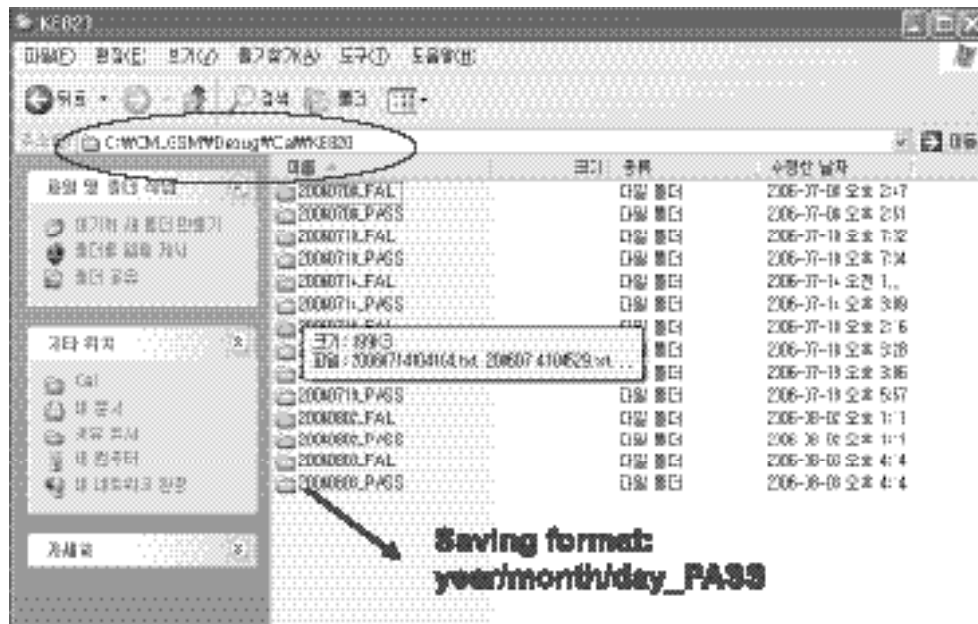
9.2.8. Click “START” for RF calibration



9.2.9. RF Calibration finishes.



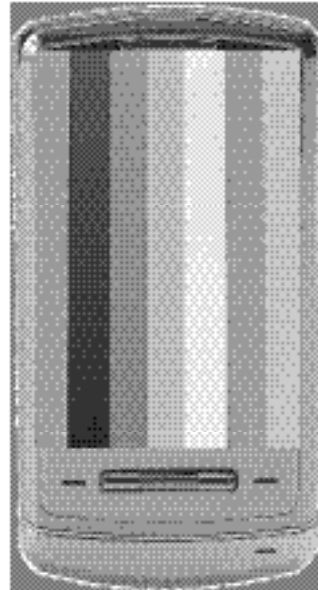
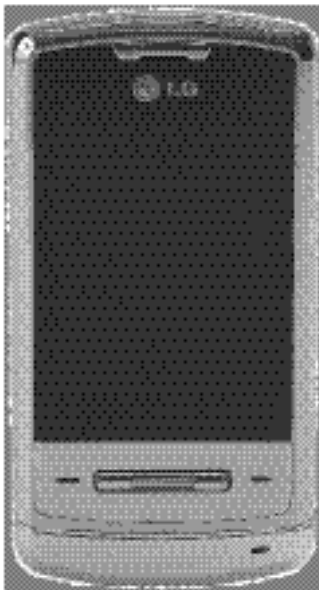
9.2.10. Calibration data will be saved to the following folder.



6. Download & S/W upgrade

Notices:

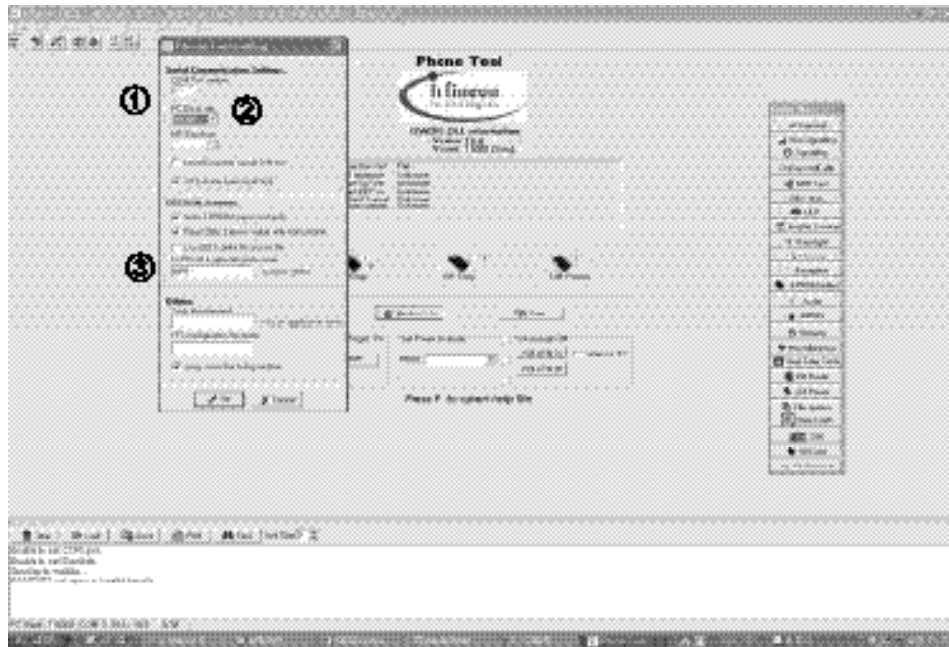
1. The state of Phone is “ test mode “ during the CALIBRATION.
2. Calibration program automatically changes either “normal mode” or “ptest mode”.
3. RF Calibration steps as follow:
TX Channel compensation: EGSM->DCS->PCS->EDGE EGSM->EDGE DCS->EDGE PCS
RX Channel compensation: EGSM->DCS->PCS
4. Phone Operation Mode



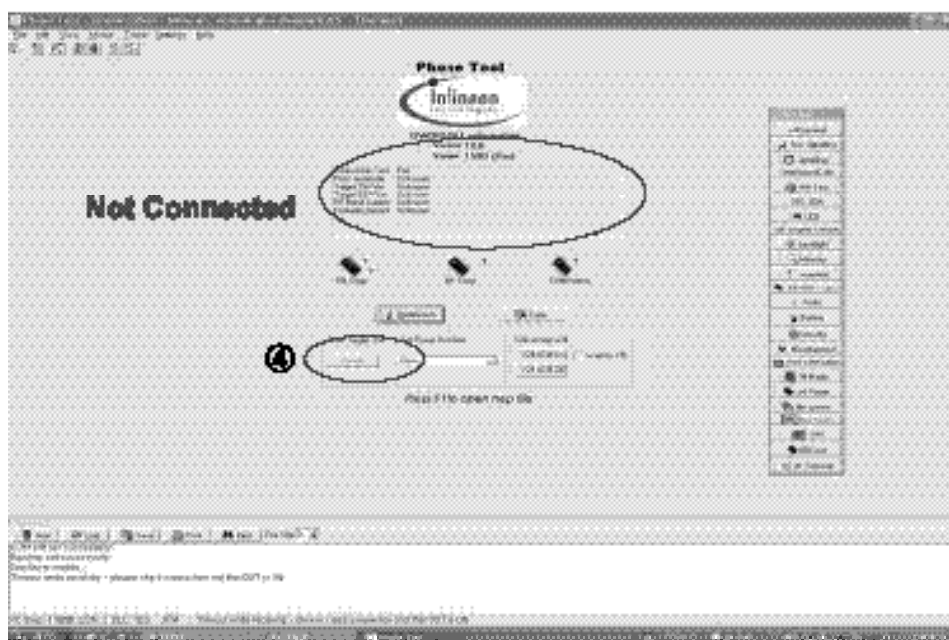
10. Stand along

10.1. Test Program Setting

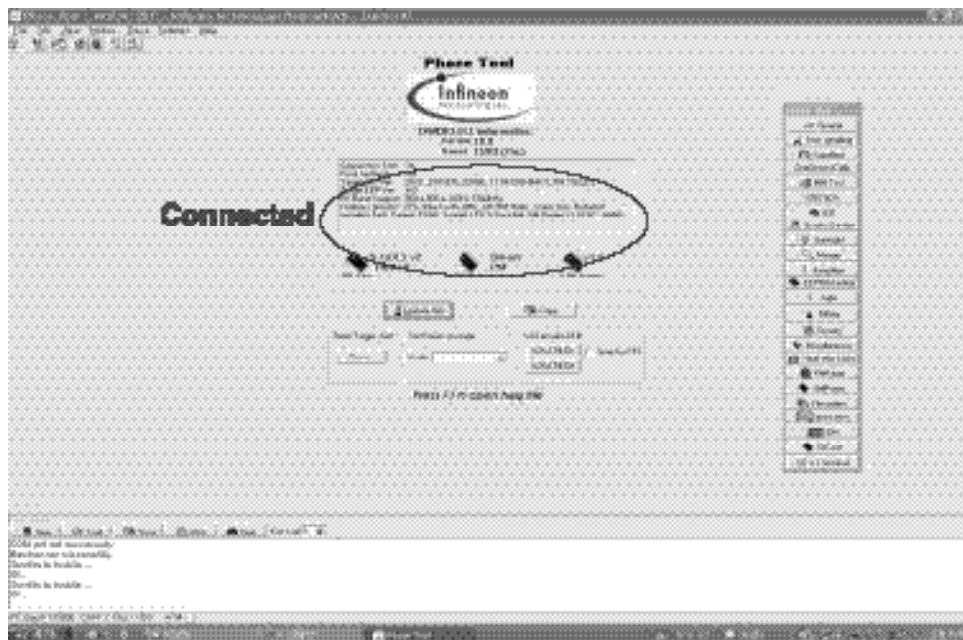
- ① Set COM Port.
- ② Check PC Baud rate.
- ③ Confirm EEPROM & Delta file prefix name.



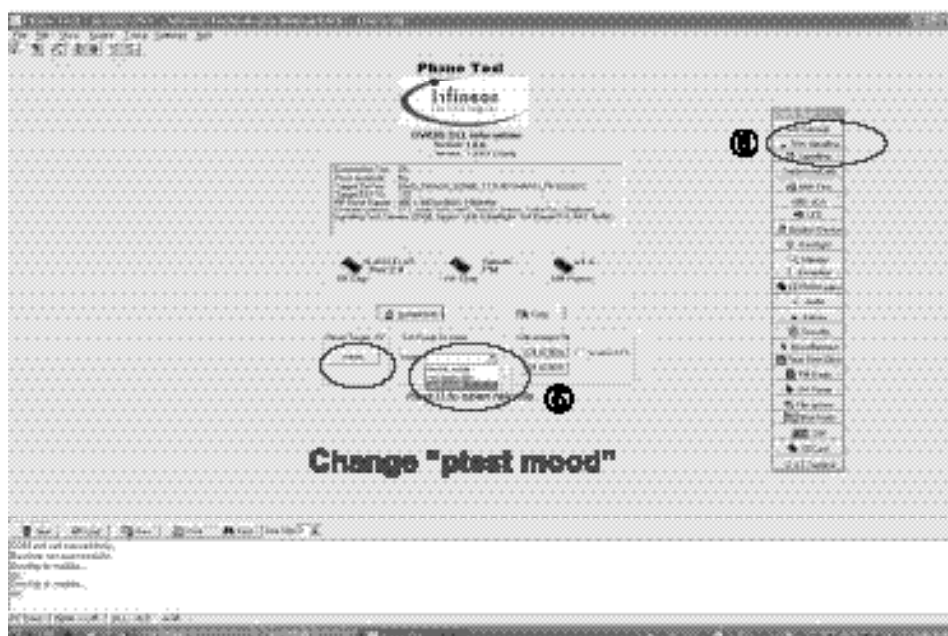
- ④ Click "Update Info" for communicating Phone and Test -Program.



10. Stand along

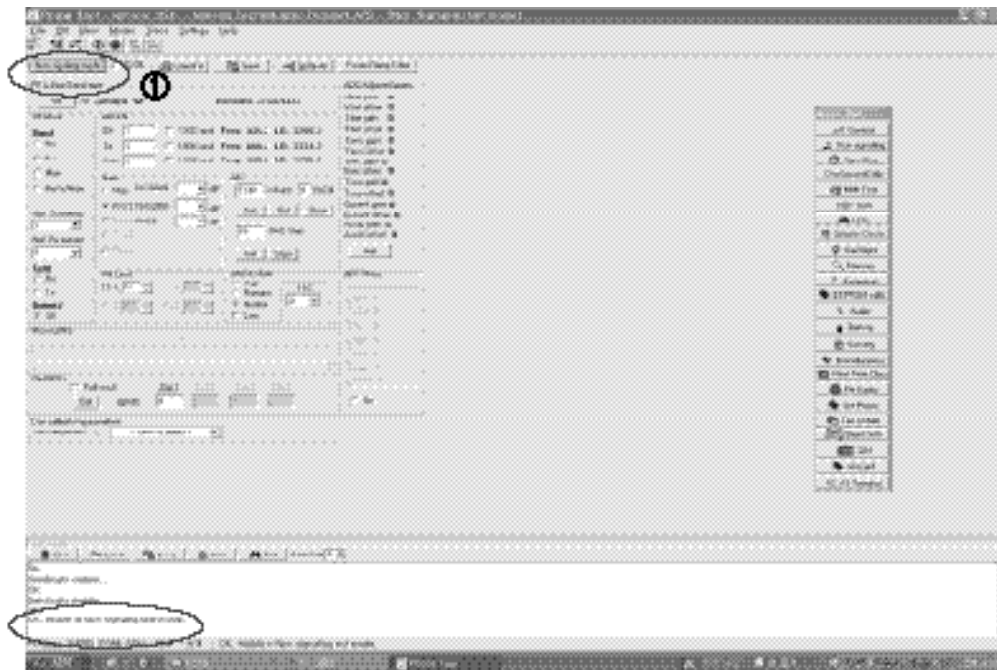


- ⑤ For the purpose of the Standalone Test, Change the Phone to “ptest mode” and then Click the “Reset” bar.
- ⑥ Select “Non signaling” in the Quick Bar menu. Then Standalone Test setup is finished.

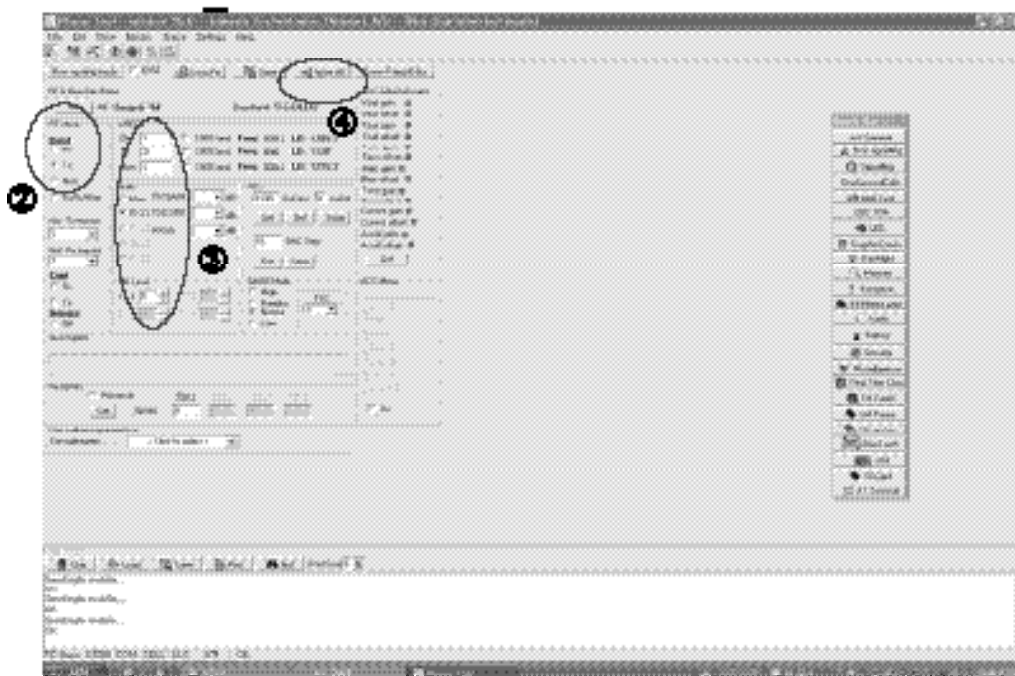


10.2. Tx Test

- ① Click “Non signaling mode” bar and then confirm “OK” text in the command line.



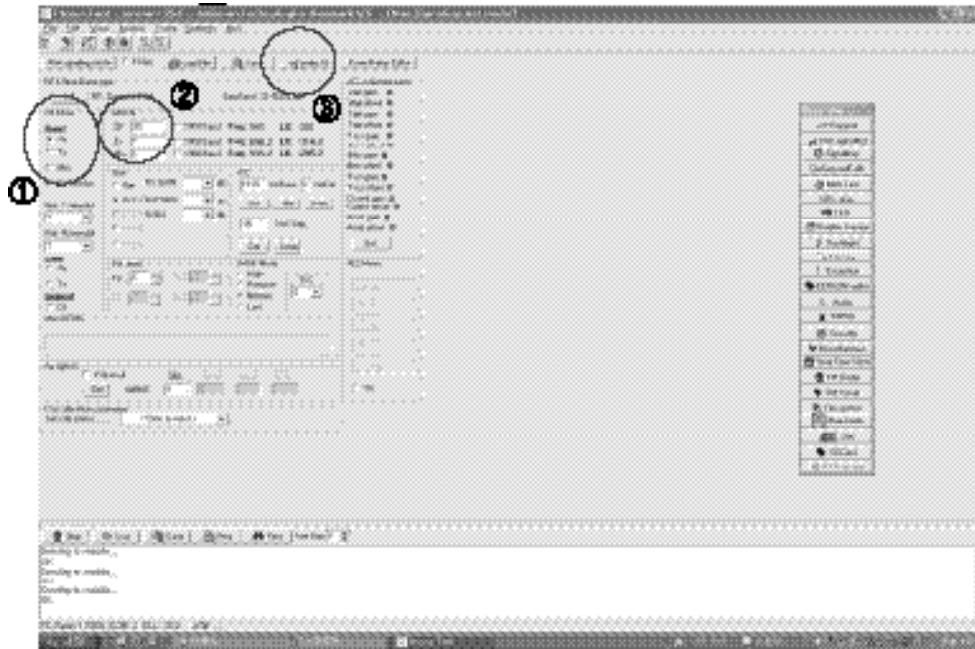
- ② Put the number of TX Channel in the ARFCN.
- ③ Select “Tx” in the RF mode menu and “PCL” in the PA Level menu.
- ④ Finally, Click “Write All” bar and try the efficiency test of Phone.



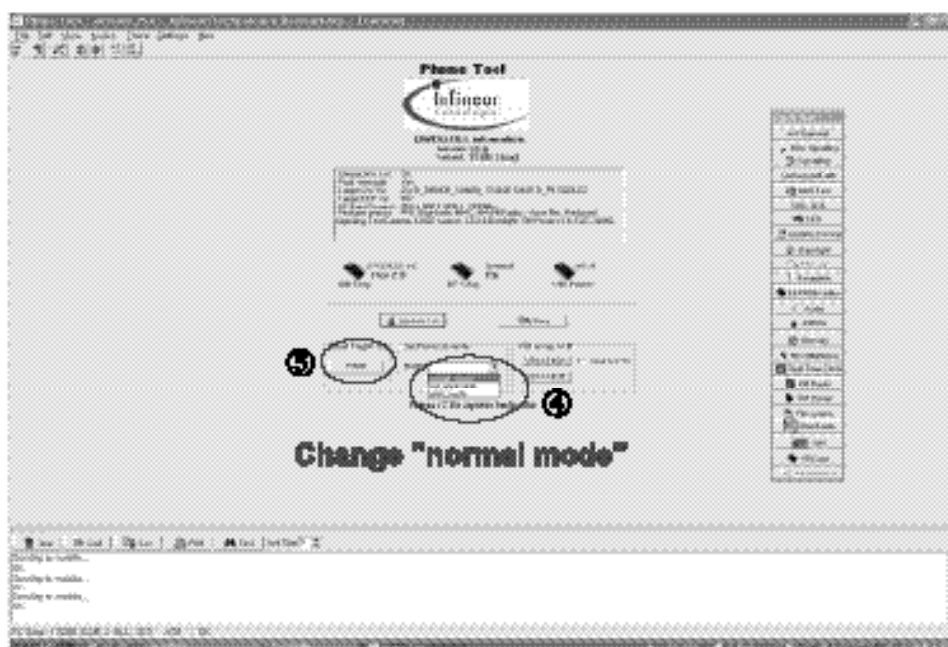
10. Stand along

10.3. Rx Test

- ① Put the number of RX Channel in the ARFCN.
- ② Select “Rx” in the RF mode menu.
- ③ Finally, Click “Write All” bar and try the efficiency test of Phone.



- ④ The Phone must be changed “normal mode” after finishing Test.
- ⑤ Change the Phone to “normal mode” and then Click the “Reset” bar.



11. ENGINEERING MODE

Engineering mode is designed to allow a service man/engineer to view and test the basic functions provided by a handset. The key sequence for switching the engineering mode on is "2945##" Select. Pressing END will switch back to non-engineering mode operation. Use Up and Down key to select a menu and press 'select' key to progress the test. Pressing back key will switch back to the original test menu.

[1] BB test

[1-1] Battery Info

[1-1-1] BattInfo

[1-2] Bluetooth Test

[1-2-1] Enter Test Mode

[1-2-1-1] Audio Test

[1-2-1-2] RF Test

[1-2-2] OnOff Test

[1-2-2-1] Bluetooth On

[1-2-2-1] Bluetooth Off

[1-2-3] Headset Test

[1-2-4] Communication Mode

[1-2-5] Xhtml compose print

[1-2-6] Xhtml Print Test

[2] Model Version test

[2-1] Version

[3] ENG MODE

[3-1] CELL ENVIRON

[3-2] PS Layer Info

[3-2-1] Mobility

[3-2-2] RadioRes

[3-2-3] Gprs

[3-3] LAYER1 INFO

[3-3-1] Close

[3-4] Reset Information

[3-4-1] Excpt

[3-5] Memory Configuration

[3-6] MenGenConf

[3-7] MemAllUse

[3-8] MemDetUse

[3-9] MemDump

[3-0] Change Frequency Band

[3-0-1] Close

[4] Call Timer

[5] Factory Reset

[6] MF Test

[6-1] All Auto Test

[6-2] Backlight

[6-2-1] Backlight On

[6-2-1] Backlight Off

[6-3] Audio

[6-3-1] Audio test

[6-4] Vibrator

[6-4-1] Vibrator on

[6-4-2] Vibrator off

[6-5] LCD

[6-5-1] Auto LCD

[6-6] Key pad

[6-7] Mic Speaker

[6-8] Camera

[6-8-1] Camera Main Preview

[6-8-2] Flash On

[6-8-2] Flash Off

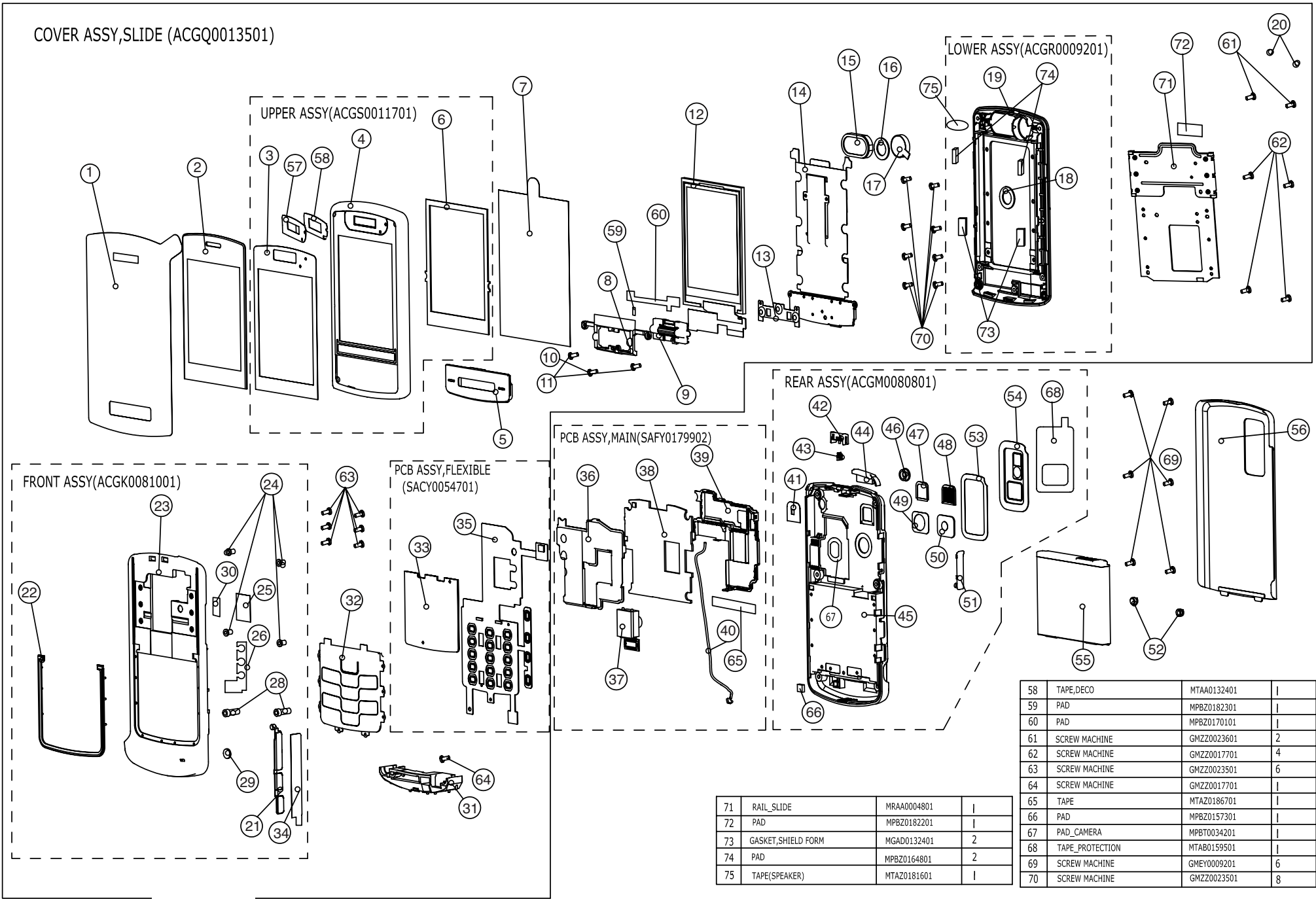
[7] DRM Engineering Mode

[7-1] GetAllRoTable

[7-2] GetRoTable

12. EXPLODED VIEW & REPLACEMENT PART LIST

12.1 EXPLODED VIEW



12. EXPLODED VIEW & REPLACEMENT PART LIST

12.2 Replacement Parts <Mechanic component>

Note: This Chapter is used for reference, Part order is ordered by SBOM standard on GCSC

Level	Location No	Description	Part Number	Spec	Color	Quantity
1		DSM(SLIDE)	TCLL0000001		Aluminum Silver	
2	AAKY00	ADDITION	AAKY0100102		Aluminum Silver	
3	MCJ400	COVER,BATTERY	MCJ40000001	PREPSL, STB,	Gray	03
2	APFY00	PHONE	APFY0000401	KB070_ELMAY	Aluminum Silver	
6	AC0M00	COVER ASBY, REAR	AC0M0000001	KB070_COVER ASBY, REAR	Aluminum Silver	
4	MC0C00	CAP, EARPHONE JACK	MC0C0000001	MOLD, Unfilled Rubber 9195A,	Without Color	01
4	MC0P00	CAP, MOBILE SWITCH	MC0P0000001	MOLD, Unfilled Rubber 9195A,	Black	40
4	MCJN00	COVER, REAR	MCJN0000001	MOLD, PC LUPOT 80-1094A,	Black	45
4	MDAD00	DECK,CAMERA	MDAD0000001	ELEOTROPFORMING, M,	Silver	04
4	MLAB00	LABEL, AS	MLAB0000001	HUMIDITY STICKER	Without Color	
4	ML0P00	LENS, FLASH	ML0P0000001	MOLD, PUMA H100M,	Transparent	40
4	ML0B00	LOCKER, BATTERY	ML0B0000001	MOLD, POM LUCEL FW700A,	Silver	42
4	MP0T00	PAD, CAMERA	MP0T0000001	COMPLEX, (empty),	Black	07
4	MP0B00	PAD	MP0B0100001	COMPLEX, (empty),	Black	06
4	MP0B01	PAD	MP0B0100001	COMPLEX, (empty),	Without Color	41
4	MD0B00	SPRING, COIL	MD0B0000001	COMPLEX, (empty),	Without Color	40
4	MTAN00	TAPE, DECK	MTAN0100001	COMPLEX, (empty),	Without Color	00
4	MTAB01	TAPE, PROTECTION	MTAB0100001	COMPLEX, (empty),	Without Color	00
4	MTAD00	TAPE, WINDOW	MTAD0000001	COMPLEX, (empty),	Without Color	40
4	MTA200	TAPE	MTA20100001	COMPLEX, (empty),	Without Color	47
4	MTA201	TAPE	MTA20100001	COMPLEX, (empty),	Without Color	
4	MY0A00	WINDOW, CAMERA	MY0A0000001	COMPLEX, (empty),	Black	00
3	AC0C00	COVER ASBY, SLIDE	AC0C0000001	KB070000070_COVER ASBY, SLIDE	Aluminum Silver	
4	AC0B00	COVER ASBY, FRONT	AC0B0000001	KB070_COVER ASBY, FRONT	Aluminum Silver	
5	MLJL00	BUTTON, SIDE	MLJL0000001	MOLD, PC LUPOT 80-1094A,	Gray	21
5	MDJN00	COVER, FRONT	MDJN0000001	MOLD, PC LUPOT 80-1094A,	Black	00
5	MDV000	DECK, FRONT	MDV00000001	MOLD, POM LUCEL FW700A,	Gray	02
5	MP0D00	FILTER, MIKE	MP0D0000001	COMPLEX, (empty),	Black	20
5	MLA200	BARRET	MLA20000001	COMPLEX, (empty),	Silver	00
5	ML0B00	INHERIT	ML0B0000001	COMPLEX, (empty),	Gold	20

12. EXPLODED VIEW & REPLACEMENT PART LIST

Level	Location ID	Description	Part Number	Specs	Color	Remark
3	MB3201	INSERT	MB320008001	COMPLEX, (empty),	Gold	34
3	MTA600	TAPE, PROTECTION	MTA60144001	COMPLEX, (empty),	Green	34
3	MTA200	TAPE	MTA20182701	COMPLEX, (empty),	Without Color	25
3	MTA201	TAPE	MTA20184901	COMPLEX, (empty),	Without Color	25
4	AC3900	COVER ARMY SLIDE (LOWER)	AC390002001		Aluminum Shear	
3	MDJ408	COVER, SLIDING (COVER)	MDJ40089401	BOLD, PO LIP CY 20-1084A,	Aluminum Shear	19
3	MSA000	BASKET, SHIELD FORM	MSA00188401	COMPLEX, (empty),	Aluminum Shear	79
3	MS30408	SLIDE, LEFT	MS304007001	BOLD, POM LUCID N108-LD,	Gray	
3	MS30808	SLIDE, RIGHT	MS308003001	BOLD, POM LUCID N108-LD,	Gray	
3	MC200	INSERT	MC200044001	CH0080, M1.4x0.3x0.8, 1.7T, L6P1	Without Color	
3	MB3201	INSERT	MB320008001	COMPLEX, (empty),	Without Color	
3	MSA400	MAGNET, SWITCH	MSA400044001	COMPLEX, (empty),	Without Color	
3	MP6100	PAD, MOTOR	MP610003001	COMPLEX, (empty),	Aluminum Shear	15
3	MP6200	PAD	MP620164001	COMPLEX, (empty),	Black	34
3	MTA200	TAPE	MTA201814001	COMPLEX, (empty),	Without Color	75
4	AC3900	COVER ARMY SLIDE (UPPER)	AC390011701		Shear	
3	MCJ400	COVER, SLIDING (UPPER)	MCJ40008001	PRESS, STS,	Without Color	4
3	MDJ400	DECO	MDJ40008001	PRESS, STS,	Without Color	
3	MP6200	PRISM	MP620011801	PRESS, STS,	Without Color	
3	MC200	INSERT	MC200044001	PRESS, STS,	Without Color	
3	MB3201	INSERT	MB320008001	COMPLEX, (empty),	Without Color	
3	MF6000	FILTER, SPEAKER	MF600008001	PRESS, STS,	Black	87
3	MP6000	PAD, LCD	MP600003001	COMPLEX, (empty),	Black	8
3	MTA400	TAPE, DECO	MTA40188401	COMPLEX, (empty),	Without Color	85
3	MTA000	TAPE, WINDOW	MTA00008001	COMPLEX, (empty),	Without Color	3
4	QMEY01	SCREW MACHINE	QMEY0018401	1.4 mm, 1.7 mm, M8X0.35 (N), +, -, (empty) (empty) .. (empty) (empty) (empty) (empty) (empty) (empty)	Black	10
4	QMEZ00	SCREW MACHINE	QMEZ0017701	1.4 mm, 3.0 mm, M8X0.35 (N), +, -, ..	Silver	11, 25, 34
4	QMEZ01	SCREW MACHINE	QMEZ0003001	1.4 mm, 1.8 mm, M8X0.35 (N), +, -, (empty) (empty) .. (empty) (empty) (empty) (empty) (empty) (empty)	Silver	23, 70
4	QMEZ02	SCREW MACHINE	QMEZ0008001	1.4 mm, 3.75 mm, M8X0.35 (N), +, -, (empty) (empty) .. (empty) (empty) (empty) (empty) (empty) (empty)		81
4	MSJ400	BUTTON, RAL	MSJ40008001	COMPLEX, (empty),	Without Color	32
4	MOCH08	DAP, SCREW	MOCH0180001	COMPLEX, (empty),	Shear	20
4	MDJ400	DECO	MDJ40008001	CASTING, Zn Alloy,	Silver	9
4	MSA000	BASKET, SHIELD FORM	MSA00139001	COMPLEX, (empty),	Gold	

<Main component>

Note: This Chapter is used for reference, Part order is ordered by SBOM standard on GCSC

Legend	Location No	Designation	Part Number	Specs	Color	Remarks
4	ENG00	ANTENNA,SMALL,PCB	ENG00002901	5.0, 5.0 dBS, ,Metallic, Internal, , 1/8WILE, 5.0, 50, 3.0		44
4	ENG100	SWITCH,ROTARY	ENG10000801	1 V,1 A,VERTICAL, 1 B,		9
4	SAC100	PCB ASSY,FLEXIBLE	SAC10004401	88in LCD FPCB		14
5	SAC000	PCB ASSY,FLEXIBLE,INSERT	SAC00004801	88in LCD FPCB		
6	MSA200	SHEET	MSA200-0801	COMPLEX, (mpg), , , , ,	Without Color	19
6	SAC000	PCB ASSY,FLEXIBLE,8MT	SAC00004901	88in LCD FPCB		
6	SAC000	PCB ASSY,FLEXIBLE,8MT BOTTOM	SAC00004901	88in LCD FPCB		
7	C001	CAP,CERAMIC,CHIP	EC00-000-0804	1 uF,6.3V,K,30R,TC,1005,R/TP		
7	C002	CAP,CERAMIC,CHIP	EC00-00001002	0.1 uF,10V,K,30R,HD,1005,R/TP		
7	CN000	CONNECTOR,FPC/FPC	ENG10010001	35 PIN,0.3 mm,ETC, , J-H=1.8		
7	D001	DIODE,TVS	EDTV00000004	SOT-883, 5 V,100 W,R/TP, Pb-FREE		
7	LD001	DIODE,LED,CHIP	EDLHD011001	WHITE,1005,R/TP,Pb-FREE(ZENER)		
7	LD002	DIODE,LED,CHIP	EDLHD011001	WHITE,1005,R/TP,Pb-FREE(ZENER)		
7	MR01	RES,CHIP,MANUAL	MR-00000000	100 Kohm,1/16W, , 1005, R/TP		
7	UR01	ID	EURY0011001	4 PIN,R/TP, 1.80X1.2 also Half ID for PD JOB Dial		
7	UR02	ID	EURY0011001	4 PIN,R/TP, 1.80X1.8 also Half ID for PD JOB Dial		
7	VAR01	VARIATOR	SEVY00000002	5.0 V, ,SMD,1005, 80pF		
7	VAR02	VARIATOR	SEVY00000002	5.0 V, ,SMD,1005, 80pF		
6	SAC000	PCB ASSY,FLEXIBLE,8MT TOP	SAC00004801	88in LCD FPCB		
7	C003	CAP,CERAMIC,CHIP	EC00-000-0804	1 uF,6.3V,K,30R,TC,1005,R/TP		
7	CN001	CONNECTOR,BOARD TO BOARD	ENG10000001	34 PIN,0.4 mm,ETC, , J-H=0.8, 8-sided		
5	SAC000	PCB ASSY,FLEXIBLE,INSERT	SAC00004801	Key FPCB Assy		
6	SAC000	PCB ASSY,FLEXIBLE,8MT BOTTOM	SAC00004901	Key FPCB Assy		
7	C004	CAP,CHIP,MANUAL	EC00-00000000	22 pF,50V, ,JMPQ,TC,1005,R/TP		
7	C005	CAP,CERAMIC,CHIP	EC00-00001002	47 pF,50V, ,JMPQ,TC,1005,R/TP		
7	CN001	CONNECTOR,BOARD TO BOARD	ENG10011001	70 PIN,0.4 mm,STRAIGHT, ALL FEMALE		
6	SAC000	PCB ASSY,FLEXIBLE,8MT TOP	SAC00004801	Key FPCB Assy		
7	C001	CAP,CERAMIC,CHIP	EC00-00001002	15 pF,50V, ,JMPQ,TC,1005,R/TP		
7	C004	CAP,CERAMIC,CHIP	EC00-00001002	15 pF,50V, ,JMPQ,TC,1005,R/TP		

12. EXPLODED VIEW & REPLACEMENT PART LIST

Quant	Location No	Description	Part Number	Spec	Color	Remark
7	0806	CAP,CERAMIC,CHIP	EOCH8000106	47 pF,50V,UMPO,TC,1008,R/TP		
7	0806	CAP,CERAMIC,CHIP	EOCH8000804	1 uF,6.3V,K,X5R,TC,1008,R/TP		
7	0807	CAP,CERAMIC,CHIP	EOCH8000804	1 uF,6.3V,K,X5R,TD,1008,R/TP		
7	0808	CAP,CERAMIC,CHIP	EOCH8000804	1 uF,6.3V,K,X5R,TC,1008,R/TP		
7	0809	CAP,CERAMIC,CHIP	EOCH8000804	10 uF,6.3V,M,X5R,TD,1008,R/TP		
7	0811	CAP,CERAMIC,CHIP	EOCH8000804	1 uF,6.3V,K,X5R,TC,1008,R/TP		
7	0812	CAP,CERAMIC,CHIP	EOCH8000804	1 uF,6.3V,K,X5R,TC,1008,R/TP		
7	0813	CAP,CERAMIC,CHIP	EOCH8000804	1 uF,6.3V,K,X5R,TD,1008,R/TP		
7	0814	CAP,CERAMIC,CHIP	EOCH8000804	1 uF,6.3V,K,X5R,TC,1008,R/TP		
7	0815	CAP,CERAMIC,CHIP	EOCH8000108	10 nF,16V,K,X7F,HD,1008,R/TP		
7	0816	CAP,CERAMIC,CHIP	EOCH8000804	1 uF,6.3V,K,X5R,TC,1008,R/TP		
7	0817	CAP,CERAMIC,CHIP	EOCH8000804	10 uF,6.3V,M,X5R,TD,1008,R/TP		
7	0818	CAP,CERAMIC,CHIP	EOCH8000108	47 pF,50V,UMPO,TC,1008,R/TP		
7	08602	DOWNSTOP,BOARD TO BOARD	EMET0000001	54 PIN,0.4 mm,ETC.,J-HOLD, Header		
7	0861	DIODE,TUBE	EDTY0000004	6CT-8B5,8 V,100 W,R/TP,FB-PWEE		
7	0862	SWITCH,TACT	EDCY0000001	1E V,0.0E A,HORIZONTAL,0.2 G,		
7	LB01	INDUCTOR,CHIP	ELCH0000002	270 mH,M,1008,R/TP,CHIP		
7	LB02	INDUCTOR,CHIP	ELCH0000002	270 mH,M,1008,R/TP,CHIP		
7	LB03	INDUCTOR,CHIP	ELCH0000000	100 mH,J,1008,R/TP,		
7	LB04	INDUCTOR,CHIP	ELCH0000000	100 mH,J,1008,R/TP,		
7	LD001	DIODE,LED,CHIP	EDLH0013001	WHITE,ETC,R/TP,REVERSE ; ; [empty] 2.8-3.7V,30mA, , ,100mA, [empty] [empty],2P		
7	LD002	DIODE,LED,CHIP	EDLH0013001	WHITE,ETC,R/TP,REVERSE ; ; [empty] 2.8-3.7V,30mA, , ,100mA, [empty] [empty],2P		
7	LD003	DIODE,LED,MODULE	EDLM0000001	WHITE,1 LED,2P1.2P,0.48,R/TP,FB-PWEE		
7	ME0001	MICROPHONE	QJMY0000000	PIN,42 dB,44,2MS Bridge Type		
7	0801	TRANSJT,NPN	EDCH0018001	8818,100 mW,R/TP,DUAL TRANSISTOR		
7	RE001	RES,CHIP	ERHY0000001	100 ohm,1/10W,J,1808,R/TP		
7	RE002	RES,CHIP	ERHY0000001	100 ohm,1/10W,J,1808,R/TP		
7	RE003	RES,CHIP	ERHY0000001	220 ohm,1/10W,J,1808,R/TP		
7	RE004	RES,CHIP,MAAGER	ERNH0000004	1 Mohm,1/10W,J,1808,R/TP		
7	RE005	RES,CHIP	ERHY0000001	480 ohm,1/10W,J,1808,R/TP		
7	RE007	RES,CHIP,MAAGER	ERNH0000004	1 Mohm,1/10W,J,1808,R/TP		
7	RE008	RES,CHIP	ERHY0000001	2700 ohm,1/10W,J,1008,R/TP		
7	RE009	RES,CHIP,MAAGER	ERNH0000004	1.2 ohm,1/10W,J,1808,R/TP		
7	RE010	RES,CHIP,MAAGER	ERNH0000004	7500 ohm,1/10W,J,1008,R/TP		
7	RE011	RES,CHIP,MAAGER	ERNH0000004	5100 ohm,1/10W,J,1808,R/TP		

Layer	Location ID	Description	Part Number	Specs	Color	Quantity
7	UB01	ID	BUEY0000001	TCPN , PIN/VTP , BLU=2 LDO+1 Flash LED Drg, 4x4		
7	VAD0	VARIABLE	SEVY0000001	18 V , RMD , 3uF , 1805		
7	VAD4	VARIABLE	SEVY0000001	18 V , RMD , 3uF , 1805		
6	BPCY00	PCB,FLEXIBLE	BPCY0000001	FCLTH , 0.25mm,5-14 HF ,MEMBERG MEY FPCB ,....		25
4	\$JMT0	VIBRATOR,MOTION	\$JMY0000004	3 V,30 mA,18P2.7 ... 3V ,55mA... ,1800....		17
3	ELCH00	INDUCTOR,CHIP	ELCH0000011	32 nH, 1008 ,R/T/P ,		
3	ELCH01	INDUCTOR,CHIP	ELCH0000007	5.9 nH, 1005 ,R/T/P ,		
3	ESMAY00	CONNF,RF SWITCH	ESMAY0000001	,RMD , dB,		
3	ERHZ00	RES,CHIP,MAKER	ERHZ0000001	0 ohm,1/16W , 1008 ,R/T/P		
3	SNCF00	ANTENNA,SMALL,FIXED	SNCF0000001	3.0 , 3.0 dBS , Internal , SSAN00018001008 , TRIPLE , 2.0 , 3.0 , 3.0		31
4	SUPY00	SPEAKER	SUPY0000001	AMBY , 3 ohm,38 dB , mm , ,730 ,18*10*HT ,M/M		15
4	SVLM00	LCD MODULE	SVLM0001001	64KHz ,240*320 ,37.5*63.8 ,30X ,TFT ,TM J-HM15CP200 .		12
3	SAFY00	PCB ASSY,MAIN	SAFY0000002			
4	SAFD00	PCB ASSY,MAIN,INVERT	SAFD0000001			
3	SNCY00	CAMERA	SNCY0001001	CMOS ,MEGA ,3M AF (FFOB , 1AF , 300000)		37
3	SWCC00	CABLE,DODONAL	SWCC0000001	91 mm,2 LINE , , [jump] [jump] [jump] [, WHITE , [jump]		40
4	SAFF00	PCB ASSY,MAIN,BMT	SAFF0001100			
3	SAFC00	PCB ASSY,MAIN,BMT BOTTOM	SAFC0000001			
6	BAT001	BATTERY,CELL,LITHIUM	BBCL0001001	2 V,6.5 mAh,CYLINDER ,Reddy type BEI ,Max T 1.67, PH 4.5, Pb-Free		
9	C101	CAP,CERAMIC,CHIP	ECC00000101	0.1 uF,6.3V ,K ,XSR ,TC ,0805 ,R/T/P		
9	C102	CAP,CERAMIC,CHIP	ECC00000101	0.1 uF,6.3V ,K ,XSR ,TO ,0805 ,R/T/P		
9	C103	CAP,CERAMIC,CHIP	ECC00000101	0.1 uF,6.3V ,K ,XSR ,TC ,0805 ,R/T/P		
9	C104	CAP,CERAMIC,CHIP	ECC00000101	0.1 uF,6.3V ,K ,XSR ,TO ,0805 ,R/T/P		
9	C105	CAP,CERAMIC,CHIP	ECC00000101	0.1 uF,6.3V ,K ,XSR ,TC ,0805 ,R/T/P		
9	C106	CAP,CERAMIC,CHIP	ECC00000101	0.1 uF,6.3V ,K ,XSR ,TO ,0805 ,R/T/P		
9	C107	CAP,CERAMIC,CHIP	ECC00000101	0.1 uF,6.3V ,K ,XSR ,TC ,0805 ,R/T/P		
9	C108	CAP,CERAMIC,CHIP	ECC00000101	0.1 uF,6.3V ,K ,XSR ,TC ,0805 ,R/T/P		
9	C109	CAP,CERAMIC,CHIP	ECC00000004	1 uF,6.3V ,K ,XSR ,TO ,1005 ,R/T/P		
9	C110	CAP,CERAMIC,CHIP	ECC00000101	0.1 uF,6.3V ,K ,XSR ,TC ,0805 ,R/T/P		
9	C111	CAP,CERAMIC,CHIP	ECC00000105	10 nF,16V ,K ,XSR ,TO ,0805 ,R/T/P		
9	C112	CAP,CERAMIC,CHIP	ECC00000004	1 uF,6.3V ,K ,XSR ,TC ,1005 ,R/T/P		
9	C113	CAP,CERAMIC,CHIP	ECC00000101	0.1 uF,6.3V ,K ,XSR ,TO ,0805 ,R/T/P		
9	C114	CAP,CERAMIC,CHIP	ECC00000105	10 nF,16V ,K ,XSR ,TC ,0805 ,R/T/P		
9	C115	CAP,CERAMIC,CHIP	ECC00000101	0.1 uF,6.3V ,K ,XSR ,TC ,0805 ,R/T/P		

12. EXPLODED VIEW & REPLACEMENT PART LIST

Eqpt	Location No	Description	Part Number	Spec	Qty	Remark
8	0116	CAP,CERAMIC,CHIP	BOCH0004804	1 uF,6.3V ,K,25R ,TC ,10% ,J/TP		
8	C117	CAP,CERAMIC,CHIP	EOCH000101	0.1 uF,6.3V ,K,25R ,TC ,0805 ,J/TP		
8	0118	CAP,CERAMIC,CHIP	BOCH000106	10 nF,16V ,K,25R ,TC ,0805 ,J/TP		
8	C119	CAP,CERAMIC,CHIP	EOCH0004804	1 uF,6.3V ,K,25R ,TC ,10% ,J/TP		
8	0120	CAP,CERAMIC,CHIP	BOCH000101	0.1 uF,6.3V ,K,25R ,TC ,0805 ,J/TP		
8	C121	CAP,CERAMIC,CHIP	BOCH000106	10 nF,16V ,K,25R ,TC ,0805 ,J/TP		
8	C122	CAP,CERAMIC,CHIP	EOCH000101	0.1 uF,6.3V ,K,25R ,TC ,0805 ,J/TP		
8	0123	CAP,CERAMIC,CHIP	BOCH000101	0.1 uF,6.3V ,K,25R ,TC ,0805 ,J/TP		
8	C124	CAP,CERAMIC,CHIP	EOCH000101	0.1 uF,6.3V ,K,25R ,TC ,0805 ,J/TP		
8	0125	CAP,CERAMIC,CHIP	BOCH000101	0.1 uF,6.3V ,K,25R ,TC ,0805 ,J/TP		
8	C126	CAP,CERAMIC,CHIP	EOCH000101	0.1 uF,6.3V ,K,25R ,TC ,0805 ,J/TP		
8	0127	CAP,CERAMIC,CHIP	BOCH000101	0.1 uF,6.3V ,K,25R ,TC ,0805 ,J/TP		
8	C128	CAP,CERAMIC,CHIP	BOCH000101	0.1 uF,6.3V ,K,25R ,TC ,0805 ,J/TP		
8	C129	CAP,CERAMIC,CHIP	EOCH000101	0.1 uF,6.3V ,K,25R ,TC ,0805 ,J/TP		
8	C130	CAP,CERAMIC,CHIP	BOCH000101	0.1 uF,6.3V ,K,25R ,TC ,0805 ,J/TP		
8	C131	CAP,CHIP,TAPE	EOCH0001211	220 uF,10V ,Z ,Y5V ,HD ,1805 ,J/TP		
8	0132	CAP,CERAMIC,CHIP	BOCH000112	15 pF,50V ,J,NP0,TC,1005,J/TP		
8	C133	CAP,CERAMIC,CHIP	EOCH000112	15 pF,50V ,J,NP0,TC,1005,J/TP		
8	0134	CAP,CERAMIC,CHIP	BOCH000101	0.1 uF,6.3V ,K,25R ,TC ,0805 ,J/TP		
8	C135	CAP,CERAMIC,CHIP	EOCH000101	0.1 uF,6.3V ,K,25R ,TC ,0805 ,J/TP		
8	0136	CAP,CERAMIC,CHIP	BOCH000101	0.1 uF,6.3V ,K,25R ,TC ,0805 ,J/TP		
8	C137	CAP,CERAMIC,CHIP	BOCH000179	22 nF,16V ,K,25R ,HD ,1805 ,J/TP		
8	C138	CAP,CERAMIC,CHIP	EOCH000179	22 nF,16V ,K,25R ,HD ,1805 ,J/TP		
8	0139	CAP,CERAMIC,CHIP	BOCH000112	15 pF,50V ,J,NP0,TC,1005,J/TP		
8	C140	CAP,CERAMIC,CHIP	EOCH000112	15 pF,50V ,J,NP0,TC,1005,J/TP		
8	0201	CAP,CERAMIC,CHIP	BOCH000101	0.1 uF,6.3V ,K,25R ,TC ,0805 ,J/TP		
8	C141	CAP,CERAMIC,CHIP	EOCH000602	0.2 uF,16V ,K,25R ,HD ,1805 ,J/TP		
8	0202	CAP,CERAMIC,CHIP	BOCH000602	0.2 uF,16V ,K,25R ,HD ,1805 ,J/TP		
8	C144	CAP,CERAMIC,CHIP	EOCH000106	100 pF,50V ,J, X7R ,TC ,0805 ,J/TP		
8	0203	CAP,CERAMIC,CHIP	BOCH000101	0.1 uF,6.3V ,K,25R ,TC ,0805 ,J/TP		
8	C145	CAP,CERAMIC,CHIP	BOCH000101	0.1 uF,6.3V ,K,25R ,TC ,0805 ,J/TP		
8	0204	CAP,CERAMIC,CHIP	EOCH000101	0.1 uF,6.3V ,K,25R ,TC ,0805 ,J/TP		
8	C147	CAP,CERAMIC,CHIP	EOCH000101	0.1 uF,6.3V ,K,25R ,TC ,0805 ,J/TP		
8	0205	CAP,CERAMIC,CHIP	BOCH000101	0.1 uF,6.3V ,K,25R ,TC ,0805 ,J/TP		
8	C148	CAP,CERAMIC,CHIP	EOCH000614	10 pF,25V ,J, X7R ,HD ,0805 ,J/TP		
8	0210	CAP,CERAMIC,CHIP	BOCH000606	27 pF,25V ,J, NP0 ,TC ,0805 ,J/TP		
8	C111	CAP,CERAMIC,CHIP	EOCH000606	47 pF,25V ,J, NP0 ,TC ,0805 ,J/TP		
8	0212	CAP,CERAMIC,CHIP	BOCH0004804	1 uF,6.3V ,K,25R ,TC ,10% ,J/TP		

12. EXPLODED VIEW & REPLACEMENT PART LIST

Level	Location No	Description	Part Number	Specs	Color	Remark
0	0215	CAP,CERAMIC,CHIP	BOCH8000195	2.2 uF,50V ,J ,XDR ,TO ,1805 ,RTP		
0	0216	CAP,CERAMIC,CHIP	BOCH8000002	2.2 uF,10V ,K ,XDR ,HD ,1805 ,RTP		
0	0217	CAP,CERAMIC,CHIP	BOCH8000002	2.2 uF,10V ,K ,XDR ,HD ,1805 ,RTP		
0	0218	CAP,CERAMIC,CHIP	BOCH8000002	2.2 uF,10V ,K ,XDR ,HD ,1805 ,RTP		
0	0219	CAP,CERAMIC,CHIP	BOCH8000002	2.2 uF,10V ,K ,XDR ,HD ,1805 ,RTP		
0	0220	CAP,CERAMIC,CHIP	BOCH8000195	2.2 uF,50V ,J ,XDR ,TC ,1805 ,RTP		
0	0221	CAP,CERAMIC,CHIP	BOCH8000195	2.2 uF,50V ,J ,XDR ,TC ,1805 ,RTP		
0	0222	CAP,CERAMIC,CHIP	BOCH8000101	0.1 uF,50V ,K ,XDR ,TO ,0805 ,RTP		
0	0223	CAP,CERAMIC,CHIP	BOCH8000195	2.2 uF,50V ,J ,XDR ,TC ,1805 ,RTP		
0	0224	CAP,CERAMIC,CHIP	BOCH8000002	2.2 uF,10V ,K ,XDR ,HD ,1805 ,RTP		
0	0225	CAP,CERAMIC,CHIP	BOCH8000195	2.2 uF,50V ,J ,XDR ,TC ,1805 ,RTP		
0	0226	CAP,CERAMIC,CHIP	BOCH8000004	10 uF,50V ,J ,XDR ,TO ,1805 ,RTP		
0	0227	CAP,CERAMIC,CHIP	BOCH8000901	10 uF,4V ,J ,XDR ,TC ,1805 ,RTP		
0	0228	CAP,CERAMIC,CHIP	BOCH8000002	2.2 uF,10V ,K ,XDR ,HD ,1805 ,RTP		
0	0229	CAP,CERAMIC,CHIP	BOCH8000004	10 uF,50V ,J ,XDR ,TC ,1805 ,RTP		
0	0230	CAP,CERAMIC,CHIP	BOCH8000901	10 uF,4V ,J ,XDR ,TC ,1805 ,RTP		
0	0231	CAP,CERAMIC,CHIP	BOCH8000195	2.2 uF,50V ,J ,XDR ,TO ,1805 ,RTP		
0	0232	CAP,CERAMIC,CHIP	BOCH8000002	2.2 uF,10V ,K ,XDR ,HD ,1805 ,RTP		
0	0233	CAP,CERAMIC,CHIP	BOCH8000101	0.1 uF,50V ,K ,XDR ,TO ,0805 ,RTP		
0	0234	CAP,CERAMIC,CHIP	BOCH8000002	2.2 uF,10V ,K ,XDR ,HD ,1805 ,RTP		
0	0241	CAP,CERAMIC,CHIP	BOCH8000195	2.2 uF,50V ,J ,XDR ,TO ,1805 ,RTP		
0	0242	CAP,CERAMIC,CHIP	BOCH8000195	2.2 uF,50V ,J ,XDR ,TC ,1805 ,RTP		
0	0243	CAP,CERAMIC,CHIP	BOCH8000195	2.2 uF,50V ,J ,XDR ,TC ,1805 ,RTP		
0	0244	CAP,CERAMIC,CHIP	BOCH8000195	2.2 uF,50V ,J ,XDR ,TO ,1805 ,RTP		
0	0245	CAP,CERAMIC,CHIP	BOCH8000002	2.2 uF,10V ,K ,XDR ,HD ,1805 ,RTP		
0	0246	CAP,CERAMIC,CHIP	BOCH8000004	1 uF,50V ,J ,XDR ,TO ,0805 ,RTP		
0	0247	CAP,CERAMIC,CHIP	BOCH8000012	1000 uF,25V ,K ,XDR ,HD ,0805 ,RTP		
0	0248	CAP,CERAMIC,CHIP	BOCH8000004	1 uF,50V ,J ,XDR ,TO ,0805 ,RTP		
0	0249	CAP,CERAMIC,CHIP	BOCH8000004	1 uF,50V ,J ,XDR ,TC ,0805 ,RTP		
0	0301	CAP,CERAMIC,CHIP	BOCH8000195	100 uF,50V ,J ,XDR ,TO ,0805 ,RTP		
0	0302	CAP,CERAMIC,CHIP	BOCH8000004	1 uF,50V ,J ,XDR ,TC ,0805 ,RTP		
0	0303	CAP,CERAMIC,CHIP	BOCH8000112	15 uF,35V ,UNP,TC,1005,RTP		
0	0304	CAP,CERAMIC,CHIP	BOCH8000012	1000 uF,25V ,K ,XDR ,HD ,0805 ,RTP		
0	0305	CAP,CERAMIC,CHIP	BOCH8000012	1000 uF,25V ,K ,XDR ,HD ,0805 ,RTP		
0	0310	CAP,CERAMIC,CHIP	BOCH8000101	0.1 uF,50V ,K ,XDR ,TO ,0805 ,RTP		
0	0311	CAP,CERAMIC,CHIP	BOCH8000012	1000 uF,25V ,K ,XDR ,HD ,0805 ,RTP		
0	0312	CAP,CERAMIC,CHIP	BOCH8000004	10 uF,50V ,J ,XDR ,TO ,0805 ,RTP		

12. EXPLODED VIEW & REPLACEMENT PART LIST

Legend	Location No	Description	Part Number	Spec	Qty	Remark
8	C316	CAP,CERAMIC,CHIP	ECCH8008101	0.1 uF,50V ,K,35R ,TO ,0808 ,R/TP		
8	C317	CAP,CERAMIC,CHIP	ECCH8008101	0.1 uF,50V ,K,35R ,TC ,0808 ,R/TP		
8	C318	CAP,CERAMIC,CHIP	ECCH8008101	0.1 uF,50V ,K,35R ,TO ,0808 ,R/TP		
8	C319	CAP,CERAMIC,CHIP	ECCH8008101	0.1 uF,50V ,K,35R ,TC ,0808 ,R/TP		
8	C320	CAP,CERAMIC,CHIP	ECCH8008101	0.1 uF,50V ,K,35R ,TO ,0808 ,R/TP		
8	C321	CAP,CERAMIC,CHIP	ECCH8008101	0.1 uF,50V ,K,35R ,TC ,0808 ,R/TP		
8	C322	CAP,CERAMIC,CHIP	ECCH8008101	0.1 uF,50V ,K,35R ,TC ,0808 ,R/TP		
8	C323	CAP,CERAMIC,CHIP	ECCH8008101	0.1 uF,50V ,K,35R ,TO ,0808 ,R/TP		
8	C324	CAP,CERAMIC,CHIP	ECCH8008101	0.1 uF,50V ,K,35R ,TO ,0808 ,R/TP		
8	C325	CAP,CERAMIC,CHIP	ECCH8008103	100 pF,50V ,J ,35R ,TC ,0808 ,R/TP		
8	C326	CAP,CERAMIC,CHIP	ECCH8008101	0.1 uF,50V ,K,35R ,TO ,0808 ,R/TP		
8	C327	CAP,CERAMIC,CHIP	ECCH8008106	27 pF,50V ,J ,35R ,TC ,0808 ,R/TP		
8	C328	CAP,CERAMIC,CHIP	ECCH8008103	100 pF,50V ,J ,35R ,TO ,0808 ,R/TP		
8	C329	CAP,CERAMIC,CHIP	ECCH8008110	82 nF,50V ,K,35R ,TC ,0808 ,R/TP		
8	C330	CAP,CERAMIC,CHIP	ECCH8008114	10 pF,50V ,D ,35R ,HD ,0808 ,R/TP		
8	C331	CAP,CERAMIC,CHIP	ECCH8008110	82 nF,50V ,K,35R ,TC ,0808 ,R/TP		
8	C332	CAP,CERAMIC,CHIP	ECCH8008106	27 pF,50V ,J ,35R ,TC ,0808 ,R/TP		
8	C334	CAP,CERAMIC,CHIP	ECCH8008101	0.1 uF,50V ,K,35R ,TO ,0808 ,R/TP		
8	C335	CAP,CERAMIC,CHIP	ECCH8008101	0.1 uF,50V ,K,35R ,TC ,0808 ,R/TP		
8	C336	CAP,CERAMIC,CHIP	ECCH8008101	0.1 uF,50V ,K,35R ,TO ,0808 ,R/TP		
8	C337	CAP,CERAMIC,CHIP	ECCH8008101	0.1 uF,50V ,K,35R ,TC ,0808 ,R/TP		
8	C338	CAP,CERAMIC,CHIP	ECCH8008101	0.1 uF,50V ,K,35R ,TO ,0808 ,R/TP		
8	C339	CAP,CERAMIC,CHIP	ECCH8008106	10 nF,16V ,K,35R ,TC ,0808 ,R/TP		
8	C340	CAP,CERAMIC,CHIP	ECCH8008112	1000 pF,50V ,K ,35R ,HD ,0808 ,R/TP		
8	C341	CAP,CERAMIC,CHIP	ECCH8008101	0.1 uF,50V ,K,35R ,TO ,0808 ,R/TP		
8	C342	CAP,CHIP,SAUGER	ECCH8008103	2 pF,50V ,C ,35R ,TC ,1008 ,R/TP		
8	C344	CAP,CERAMIC,CHIP	ECCH8008103	100 pF,50V ,J ,35R ,TO ,0808 ,R/TP		
8	C345	CAP,CERAMIC,CHIP	ECCH8008101	0.1 uF,50V ,K,35R ,TC ,0808 ,R/TP		
8	C346	CAP,CERAMIC,CHIP	ECCH8008101	0.1 uF,50V ,K,35R ,TO ,0808 ,R/TP		
8	C347	CAP,CERAMIC,CHIP	ECCH8008106	100 pF,50V ,J ,35R ,TC ,0808 ,R/TP		
8	C348	CAP,CERAMIC,CHIP	ECCH8008112	1000 pF,50V ,K ,35R ,HD ,0808 ,R/TP		
8	C401	CAP,CERAMIC,CHIP	ECCH8008146	1 nF,50V ,K,35R ,HD,1008 ,R/TP		
8	C402	CAP,CERAMIC,CHIP	ECCH8008103	100 pF,50V ,J ,35R ,TC ,0808 ,R/TP		
8	C403	CAP,CERAMIC,CHIP	ECCH8008103	100 pF,50V ,J ,35R ,TO ,0808 ,R/TP		
8	C4201	CONNECTOR,LETC	ENRY0019401	3 PIN,3.0 mm,LETC , J1-53		
8	C4202	CONNECTOR,BOARD TO BOARD	ENRY0017301	70 PIN,0.4 mm,STRAIGHT ,AU ,MALE		
8	C4203	CONNECTOR,I/O	ENRY0040301	18 PIN,0.4 mm,ITC , ,1.3 08mm		

12. EXPLODED VIEW & REPLACEMENT PART LIST

Level	Location No	Description	Part Number	Specs	Color	Remark
0	D001	DIODE,T4E	SDTY0000004	90T-885, 2 V, 100 W / RTP , PB-FREE		
0	F001	FILTER,HEAD,CHIP	0FTR0001003	330 ohm, 5018 ,		
0	FL002	FILTER,EMPPOWER	0FEY0010001	5MΩ ,TDFM,1000nm & 10pF / 150V		
0	FL006	FILTER,EMPPOWER	0FEY0010001	5MΩ ,TDFM,1000nm & 10pF / 150V		
0	FL004	FILTER,EMPPOWER	0FEY0010001	5MΩ ,TDFM,1000nm & 10pF / 150V		
0	FL005	FILTER,EMPPOWER	0FEY0010001	5MΩ ,TDFM,1000nm & 10pF / 150V		
0	FL009	FILTER,EMPPOWER	0FEY0010001	5MΩ ,TDFM,1000nm & 10pF / 150V		
0	FL008	FILTER,DESELECTIO	0FDY0001001	3450 MHz,2.0V, 35 ,SMD ,Pb-free ,RoHS2.0_Dielectric		
0	L101	INDUCTOR,CHIP	ELCH0001002	220 nH, 1005 ,RTP ,CHIP		
0	L102	INDUCTOR,CHIP	ELCH0001002	220 nH, 1005 ,RTP ,CHIP		
0	L103	INDUCTOR,CHIP	ELCH0000009	100 nH, 1005 ,RTP ,		
0	L104	INDUCTOR,CHIP	ELCH0000009	100 nH, 1005 ,RTP ,		
0	L001	INDUCTOR,SMD,POWER	ELCP0000104	10 uH, 1.5A, 1.5 ,RTP ,power Inductor 500mA		
0	L004	INDUCTOR,CHIP	ELCH0001001	1.2 nH, 1005 ,RTP ,PB-FREE		
0	L005	INDUCTOR,CHIP	ELCH0001001	1.2 nH, 1005 ,RTP ,PB-FREE		
0	Q001	TRAJT, NPN	EQBN00007001	EMT3 , 2.15 W / RTP , LOW FREQUENCY		
0	R101	RES,CHIP,MAXIOL	0FHY0000000	4.7 ohm, 1/16W , 1005 ,RTP		
0	R102	RES,CHIP	0FHY0000017	22 Kohm, 1/16W(0.005W) , 1005 ,RTP		
0	R103	RES,CHIP	0FHY0000022	2.2 Kohm, 1/16W(0.005W) , 1005 ,RTP		
0	R104	RES,CHIP	0FHY0000010	990 Kohm, 1/16W , 1005 ,RTP		
0	R105	RES,CHIP,MAXIOL	0FHY0000004	100 Kohm, 1/16W , 1005 ,RTP		
0	R107	RES,CHIP	0FHY0000001	5 ohm, 1/16W(0.005W) , 1005 ,RTP		
0	R108	RES,CHIP	0FHY0000001	5 ohm, 1/16W(0.005W) , 1005 ,RTP		
0	R109	RES,CHIP	0FHY0000008	54 ohm, 1/16W(0.005W) , 1005 ,RTP		
0	R110	RES,CHIP	0FHY0000005	24 ohm, 1/16W(0.005W) , 1005 ,RTP		
0	R112	RES,CHIP	0FHY0000008	10 Kohm, 1/16W(0.005W) , 1005 ,RTP		
0	R113	RES,CHIP	0FHY0000004	1 Kohm, 1/16W(0.005W) , 1005 ,RTP		
0	R114	RES,CHIP	0FHY0000008	100 Kohm, 1/16W(0.005W) , 1005 ,RTP		
0	R116	RES,CHIP	0FHY0000008	5.6 Kohm, 1/16W(0.005W) , 1005 ,RTP		
0	R117	RES,CHIP	0FHY0000005	24 ohm, 1/16W(0.005W) , 1005 ,RTP		
0	R118	RES,CHIP	0FHY0000008	54 ohm, 1/16W(0.005W) , 1005 ,RTP		
0	R121	RES,CHIP	0FHY0000001	5 ohm, 1/16W(0.005W) , 1005 ,RTP		
0	R122	RES,CHIP	0FHY0000004	1 Kohm, 1/16W(0.005W) , 1005 ,RTP		
0	R002	RES,CHIP	0FHY0000004	1 Kohm, 1/16W(0.005W) , 1005 ,RTP		
0	R008	RES,CHIP	0FHY0011001	47 ohm, 1/16W , 1005 ,RTP		
0	R004	RES,CHIP	0FHY0000010	990 Kohm, 1/16W(0.005W) , 1005 ,RTP		
0	R005	RES,CHIP	0FHY0000017	22 Kohm, 1/16W(0.005W) , 1005 ,RTP		

12. EXPLODED VIEW & REPLACEMENT PART LIST

Quant	Location No	Description	Part Number	Spec	Color	Remark
8	R210	RES,CHIP	ERHY00080278	80K ohm,1/16W,1,1805,R/TP		
6	R220	RES,CHIP	ERHY00080008	100 Kohm,180W(0.00W) J ,0003 ,R/TP		
8	R222	RES,CHIP	ERHY00080008	100 Kohm,120W(0.00W) J ,0003 ,R/TP		
6	R208	RES,CHIP	ERHY00080008	100 Kohm,180W(0.00W) J ,0008 ,R/TP		
8	R308	RES,CHIP	ERHY00080116	22K ohm,120W(0.00W) J ,0003 ,R/TP		
6	R204	RES,CHIP	ERHY00080008	100 ohm,120W(0.00W) J ,0008 ,R/TP		
8	R306	RES,CHIP	ERHY00080116	22K ohm,120W(0.00W) J ,0003 ,R/TP		
6	R207	RES,CHIP	ERHY00080008	100 Kohm,120W(0.00W) J ,0008 ,R/TP		
8	R309	RES,CHIP	ERHY00080008	100 Kohm,120W(0.00W) J ,0003 ,R/TP		
8	R210	RES,CHIP	ERHY00080008	100 Kohm,120W(0.00W) J ,0003 ,R/TP		
6	R211	RES,CHIP	ERHY00080008	10 Kohm,120W(0.00W) J ,0003 ,R/TP		
8	R212	RES,CHIP	ERHY00080008	100 Kohm,120W(0.00W) J ,0003 ,R/TP		
6	R220	RES,CHIP	ERHY00080007	1 Mohm,120W(0.00W) J ,0008 ,R/TP		
8	R321	RES,CHIP	ERHY00080004	4.7 ohm,120W(0.00W) J ,0003 ,R/TP		
6	R223	RES,CHIP	ERHY00080116	820 Kohm,120W(0.00W) J ,0008 ,R/TP		
8	R324	RES,CHIP	ERHY00080005	10 Kohm,120W(0.00W) J ,0003 ,R/TP		
8	R226	RES,CHIP	ERHY00080008	10 Kohm,120W(0.00W) J ,0003 ,R/TP		
6	R320	RES,CHIP	ERHY00080007	4.7 Kohm,120W(0.00W) J ,0003 ,R/TP		
8	R228	RES,CHIP	ERHY00080116	2.2 Kohm,120W(0.00W) J ,0003 ,R/TP		
6	R229	RES,CHIP	ERHY00080008	100 Kohm,180W(0.00W) J ,0008 ,R/TP		
8	R230	RES,CHIP	ERHY00080004	1 Kohm,120W(0.00W) J ,0003 ,R/TP		
6	R234	RES,CHIP,MARKED	ERH-2200000008	100 Kohm,1/16W J ,1008 ,R/TP		
8	R337	RES,CHIP	ERHY00080004	1 Kohm,120W(0.00W) J ,0003 ,R/TP		
6	R401	RES,CHIP	ERHY00080001	100 ohm,1/16W J ,1008 ,R/TP		
8	R402	RES,CHIP	ERHY00080001	100 ohm,1/16W J ,1006 ,R/TP		
8	U101	ID	ELHY0802101	80A ,108 PIN,R/TP ,18 Nor+888888888888 ,1 8V 50(50mA)		
6	U108	IC	ELHY0814001	80A ,208 PIN,R/TP ,JED08 BASE BAND 2-8008		
8	U201	IC	ELHY0809001	80T01-8 J 8 PIN,R/TP ,2.8V Base voltage(8000), 80000 800000		
8	U208	ID	ELHY0809101	PQ-VQPM-48 ,48 PIN,R/TP ,P800 ,P8 Free		
6	U204	IC	ELHY0804001	07H ,30 PIN,R/TP ,JL020.3V 800mA Dual LDO		
8	U206	ID	ELHY0807001	80C78 J 8 PIN,R/TP ,Comparator, pin compatible to ELHY08077001		
6	U401	IC	ELHY0817001	80C78 J 8 PIN,R/TP ,Dual Buffer, P8 Free		
8	W301	VARIABLE	2EVY0800002	5.0 V ,0.003 ,1005 ,50pF		
8	W308	VARIABLE	2EVY0800001	10 V ,0.002 ,3pF ,1005		
6	W309	VARIABLE	2EVY0800001	10 V ,0.002 ,3pF ,1005		

12. EXPLODED VIEW & REPLACEMENT PART LIST

Level	Location ID	Description	Part Number	Specs	Qty	Remark
0	W306	DIODE,TVS	EDTY0009101	600-928 ,5 V,180 nW,R/T/P ,1.0*0.8*0.4		
0	W306	DIODE,TVS	EDTY0009101	600-928 ,5 V,180 nW,R/T/P ,1.0*0.8*0.4		
0	X101	5-TAL	ES00Y0000701	82.788 KHz,80 PPM,12.5 pF,70 KHz,5ND ,0.2*1.8*0.9		
0	SAFD00	PCB ASSEMBLY/MOUNT TOP	SAFD0000001			
0	C201	CAP,CERAMIC,CHIP	EC00H0004004	1 uF,6.3V ,K ,X5R ,TC ,1005 ,R/T/P		
0	C026	CAP,CERAMIC,CHIP	EC00H0007001	4.7 nF,10V ,Z ,Y5V ,HD ,1805 ,R/T/P		
0	C026	CAP,CHIP,MAKER	EC20H0000000	27 pF,50V ,J ,NPO ,TC ,1805 ,R/T/P		
0	C040	CAP,TANTAL,CHIP,MAKER	ECT00000000	88 uF,6.3V ,M ,STD ,3216 ,R/T/P		
0	C050	CAP,CERAMIC,CHIP	EC00H0000102	1000 pF,50V ,K ,X7R ,HD ,0805 ,R/T/P		
0	C051	CAP,CERAMIC,CHIP	EC00H0001103	22 pF,50V ,J,NPO,TC,1005,R/T/P		
0	C056	CAP,CHIP,MAKER	EC20H0000000	1 uF,6.3V ,Z ,Y5V ,HD ,1005 ,R/T/P		
0	C054	CAP,CERAMIC,CHIP	EC00H0000000	27 pF,50V ,J ,NPO ,TC ,0805 ,R/T/P		
0	C058	CAP,CERAMIC,CHIP	EC00H0004004	1 uF,6.3V ,K ,X5R ,TC ,1005 ,R/T/P		
0	C056	CAP,CERAMIC,CHIP	EC00H0001001	0.1 uF,6.3V ,K ,X5R ,TC ,0805 ,R/T/P		
0	C057	CAP,CERAMIC,CHIP	EC00H0001001	0.1 uF,6.3V ,K ,N5R ,TC ,0805 ,R/T/P		
0	C058	CAP,CERAMIC,CHIP	EC00H0000000	27 pF,50V ,J ,NPO ,TC ,0805 ,R/T/P		
0	C058	CAP,CERAMIC,CHIP	EC00H0001003	100 pF,50V ,J ,X7R ,TC ,0805 ,R/T/P		
0	C050	CAP,CHIP,MAKER	EC20H0000001	88 nF,10V ,K ,X7R ,HD ,1805 ,R/T/P		
0	C051	CAP,CERAMIC,CHIP	EC00H0001103	100 pF,50V ,J ,X7R ,TC ,0805 ,R/T/P		
0	C056	CAP,CERAMIC,CHIP	EC00H0001001	0.1 uF,6.3V ,K ,N5R ,TC ,0805 ,R/T/P		
0	C054	CAP,TANTAL,CHIP,MAKER	ECT000000001	100 uF,6V ,M ,J ,EGR ,3216 ,R/T/P		
0	C058	CAP,CERAMIC,CHIP	EC00H0001003	100 pF,50V ,J ,X7R ,TC ,0805 ,R/T/P		
0	C056	CAP,TANTAL,CHIP,MAKER	ECT000000001	100 uF,6V ,M ,J ,EGR ,3216 ,R/T/P		
0	C057	CAP,CHIP,MAKER	EC20H0001210	470 nF,10V ,Z ,Y5V ,HD ,1805 ,R/T/P		
0	C058	CAP,CHIP,MAKER	EC20H0001211	200 nF,10V ,Z ,Y5V ,HD ,1805 ,R/T/P		
0	C058	CAP,CERAMIC,CHIP	EC00H0004004	1 uF,6.3V ,K ,X5R ,TC ,1005 ,R/T/P		
0	C070	CAP,CHIP,MAKER	EC20H0000001	88 nF,10V ,K ,X7R ,HD ,1805 ,R/T/P		
0	C071	CAP,CERAMIC,CHIP	EC00H0001103	100 pF,50V ,J ,X7R ,TC ,0805 ,R/T/P		
0	C004	CAP,CERAMIC,CHIP	EC00H0001001	0.1 uF,6.3V ,K ,N5R ,TC ,0805 ,R/T/P		
0	C005	CAP,CERAMIC,CHIP	EC00H0001001	0.1 uF,6.3V ,K ,X5R ,TC ,0805 ,R/T/P		
0	C006	CAP,CERAMIC,CHIP	EC00H0001102	15 pF,50V ,J,NPO,TC,1005,R/T/P		
0	C007	CAP,CHIP,MAKER	EC20H0000000	27 pF,50V ,J ,NPO ,TC ,1805 ,R/T/P		
0	C010	CAP,CERAMIC,CHIP	EC00H0001001	0.1 uF,6.3V ,K ,N5R ,TC ,0805 ,R/T/P		
0	C014	CAP,CERAMIC,CHIP	EC00H0001106	0.2 uF,6.3V ,M ,N5R ,TC ,1805 ,R/T/P		
0	C016	CAP,CERAMIC,CHIP	EC00H0001001	0.1 uF,6.3V ,K ,X5R ,TC ,0805 ,R/T/P		
0	C003	CAP,CERAMIC,CHIP	EC00H0001001	0.1 uF,6.3V ,K ,N5R ,TC ,0805 ,R/T/P		

12. EXPLODED VIEW & REPLACEMENT PART LIST

Quant	Location No	Description	Part Number	Spec	Color	Remark
8	C338	CAP,CERAMIC,CHIP	BOCH8007901	10 uF,4V ,M ,XPR ,TC ,1808 ,RTP		
6	C348	CAP,TANTAL,CHIP,MARKER	ECT25000919	88 uF,10V ,M ,STD ,2E10 ,RTP		
8	C349	CAP,CERAMIC,CHIP	BOCH8008002	2.2 uF,16V ,K ,XPR ,HD ,1808 ,RTP		
6	C404	CAP,CERAMIC,CHIP	EOCH8000110	10 pF,50V,D,NP8,TC,1808,RTP		
8	C408	CAP,CERAMIC,CHIP	BOCH8000115	22 pF,50V ,J,NP8,TC,1808,RTP		
6	C407	CAP,CERAMIC,CHIP	BOCH8000701	1.2 pF,50V ,C ,NP0,TC ,1808 ,RTP		
8	C408	CAP,CHIP,MARKER	ECT25000919	100 pF,50V ,J ,NP8 ,TC ,1808 ,RTP		
6	C408	CAP,CHIP,MARKER	BOCH8001008	9.5 pF,50V ,B ,NP0 ,TC ,1808 ,RTP		
8	C413	CAP,TANTAL,CHIP,MARKER	ECT25004293	88 uF,6.3V ,M ,STD ,2E10 ,RTP		
8	C414	CAP,CERAMIC,CHIP	BOCH8008103	100 pF,50V ,J ,X7R ,TC ,8808 ,RTP		
6	C416	CAP,CERAMIC,CHIP	EOCH8008103	100 pF,50V ,J ,X7R ,TC ,8808 ,RTP		
8	C416	CAP,CERAMIC,CHIP	BOCH8008406	2.5 pF,50V ,D ,X7R ,TC ,1808 ,RTP		
6	C417	CAP,CERAMIC,CHIP	BOCH8008408	9.5 pF,50V ,C ,X7R ,TC ,1808 ,RTP		
8	C418	CAP,CERAMIC,CHIP	EOCH8008801	2.2 pF,50V ,C ,NP0,TC ,1808 ,RTP		
6	C419	CAP,CERAMIC,CHIP	BOCH8000801	9.2 pF,50V ,C ,NP0,TC ,1808 ,RTP		
8	C420	CAP,CHIP,MARKER	ECT25000922	1.5 pF,50V ,C ,NP0,TC ,1808 ,RTP		
8	C461	CAP,CHIP,MARKER	BOCH8008803	1.5 pF,50V ,D ,NP0,TC ,1808 ,RTP		
6	C468	CAP,CERAMIC,CHIP	EOCH8000701	1.2 pF,50V ,C ,NP0,TC ,1808 ,RTP		
8	C468	CAP,CERAMIC,CHIP	BOCH8000701	1.2 pF,50V ,D ,NP0,TC ,1808 ,RTP		
6	C484	CAP,CERAMIC,CHIP	EOCH8000182	9.1 uF,10V ,K ,XPR ,HD ,1808 ,RTP		
8	C488	CAP,CERAMIC,CHIP	BOCH8000101	9.1 uF,6.3V ,K ,XPR ,TC ,8808 ,RTP		
6	C487	CAP,CERAMIC,CHIP	BOCH8008404	1 uF,6.3V ,K ,XPR ,TC ,1808 ,RTP		
8	C488	CAP,CHIP,MARKER	ECT25000928	27 pF,50V ,J ,NP0 ,TC ,1808 ,RTP		
6	C488	CAP,CERAMIC,CHIP	BOCH8008106	100 pF,50V ,J ,X7R ,TC ,8808 ,RTP		
8	C433	CAP,CERAMIC,CHIP	EOCH8008101	9.1 uF,6.3V ,K ,XPR ,TC ,8808 ,RTP		
8	C434	CAP,CERAMIC,CHIP	BOCH8008101	9.1 uF,6.3V ,K ,XPR ,TC ,8808 ,RTP		
6	C436	CAP,CERAMIC,CHIP	EOCH8008101	9.1 uF,6.3V ,K ,XPR ,TC ,8808 ,RTP		
8	C436	CAP,CERAMIC,CHIP	BOCH8008802	47000 pF,16V ,K ,B ,HD ,1808 ,RTP		
6	C467	CAP,CERAMIC,CHIP	EOCH8008812	1000 pF,50V ,K ,X7R ,HD ,8808 ,RTP		
8	C438	CAP,CERAMIC,CHIP	BOCH8000101	9.1 uF,6.3V ,K ,XPR ,TC ,8808 ,RTP		
6	C488	CAP,CERAMIC,CHIP	BOCH8008101	9.1 uF,6.3V ,K ,XPR ,TC ,8808 ,RTP		
8	C440	CAP,CERAMIC,CHIP	EOCH8008103	100 pF,50V ,J ,X7R ,TC ,8808 ,RTP		
6	C441	CAP,CERAMIC,CHIP	BOCH8008812	1000 pF,50V ,K ,X7R ,HD ,8808 ,RTP		
8	C442	CAP,CERAMIC,CHIP	EOCH8008806	22 pF,25V ,J ,NP0 ,TC ,8808 ,RTP		
8	C444	CAP,CHIP,MARKER	BOCH8008802	1 uF,6.3V ,Z ,Y1V ,HD ,1808 ,RTP		
6	C446	CAP,CERAMIC,CHIP	EOCH8008101	9.1 uF,6.3V ,K ,XPR ,TC ,8808 ,RTP		
8	C446	CAP,CERAMIC,CHIP	BOCH8008101	9.1 uF,6.3V ,K ,XPR ,TC ,8808 ,RTP		

Level	Location ID	Description	Part Number	Specs	Color	Remarks
4	CN002	CONN,SOCKET	EMBY0017001	14 PIN,HTO , 2.54 mm,Micro-50, UMI Dupl Socket		
5	CN001	CONNECTOR,BOARD TO BOARD	EMBY0010001	84 PIN,0.4 mm,STRAIGHT ,AU 0.1MM HEIGHT		
5	CN001	CONN,RF SWITCH	EMBY0000001	10dB , 1.8 dB,		
4	D001	DIODE,SWITCHING	EMBY0017701	100V-125 ,10 V,1 A,R/TP ,,,,,,,,,, (Smpto ,Jmply) ,SP ,1		
5	F0001	FILTER,HEAD,CHIP	SPFH-10007102	10 class,1000 ,Forte Band		
5	F0002	FILTER,HEAD,CHIP	SPFH-10007102	10 class,1000 ,Forte Band		
5	F0003	FILTER,HEAD,CHIP	SPFH-10007102	10 class,1000 ,Forte Band		
5	F0004	FILTER,HEAD,CHIP	SPFH-10007102	10 class,1000 ,Forte Band		
4	FL001	FILTER,SEPARATOR	SPFY0000001	900,900 ,1000,1000 ,2.5 dB,3.5 dB,45 ,45 ,45,ETC ,L-603,3001.5 Bna, Quad PGM		
5	L001	INDUCTOR,CHIP	ELCH-10000000	100 nH,J ,1000 ,R/TP ,		
5	L002	INDUCTOR,CHIP	ELCH-10000000	100 nH,J ,1000 ,R/TP ,		
5	L003	INDUCTOR,CHIP	ELCH-10000000	100 nH,J ,1000 ,R/TP ,		
5	L004	INDUCTOR,CHIP	ELCH-10000000	100 nH,J ,1000 ,R/TP ,		
4	L001	INDUCTOR,CHIP	ELCH-10000000	100 nH,J ,1000 ,R/TP ,		
4	L002	INDUCTOR,CHIP	ELCH-1001410	02 nH,J ,1000 ,R/TP ,PREFREE		
5	L003	INDUCTOR,CHIP	ELCH-10007102	10 nH,J ,1000 ,R/TP ,Pn Free		
4	L004	INDUCTOR,CHIP	ELCH-10010000	0.6 nH,J ,1000 ,R/TP ,PREFREE		
5	L005	INDUCTOR,CHIP	ELCH-10010000	0.6 nH,J ,1000 ,R/TP ,PREFREE		
4	PT001	THERMISTOR	NETY0000001	NTC ,10000 ohm,5MD ,1000 ,0000-0000 ,J ,R/T, P,PREFREE		
4	Q001	TR,FET,J-CHANNEL	ECFF0000001	100P-0 ,1.14 W,-00 V,-0.7 A,R/TP ,J-Channel FET		
5	Q002	TR,FET,J-CHANNEL	ECFF0001001	30T-200 ,20 W,1.0 V,-00 A,R/TP ,J-Channel MOSFET, Pn Free		
5	R000	RES,CHIP	ERHY0000000	20 Kohm,100W(0.0000) ,J ,0000 ,R/TP		
5	R011	RES,CHIP	ERHY0000000	100 Kohm,100W(0.0000) ,J ,0000 ,R/TP		
5	R012	RES,CHIP	ERHY0000000	100 Kohm,100W(0.0000) ,J ,0000 ,R/TP		
5	R010	RES,CHIP	ERHY0000000	100 Kohm,100W(0.0000) ,J ,0000 ,R/TP		
5	R014	RES,CHIP	ERHY0000000	4.7 Kohm,100W(0.0000) ,J ,0000 ,R/TP		
4	R010	RES,CHIP	ERHY0000000	100 Kohm,100W(0.0000) ,J ,0000 ,R/TP		
5	R010	RES,CHIP	ERHY0000000	100 Kohm,100W(0.0000) ,J ,0000 ,R/TP		
4	R017	RES,CHIP	ERHY0000000	100 Kohm,100W(0.0000) ,J ,0000 ,R/TP		
5	R010	RES,CHIP	ERHY0000000	100 Kohm,100W(0.0000) ,J ,0000 ,R/TP		
4	R010	RES,CHIP	ERHY0000000	100 Kohm,100W(0.0000) ,J ,0000 ,R/TP		
5	R001	RES,CHIP	ERHY00000710	0.10 ohm,100W ,F ,0010 ,R/TP		
5	R002	RES,CHIP	ERHY00000004	4.7 class,100W(0.0000) ,J ,0000 ,R/TP		
4	R004	RES,CHIP	ERHY0000000	100 ohm,100W(0.0000) ,J ,0000 ,R/TP		
5	R005	RES,CHIP	ERHY0000000	10 Kohm,100W(0.0000) ,J ,0000 ,R/TP		

12. EXPLODED VIEW & REPLACEMENT PART LIST

Level	Location ID	Description	Part Number	Specs	Color	Remark
8	R420	RES,CHIP,MAKER	ERN-230080801	820 ohm,1/4WV,1,1808,R/TP		
8	R421	RES,CHIP	ERUHY0008002	10 ohm,1/8WV(0.05W),1,0803,R/TP		
8	R422	RES,CHIP	ERUHY0008001	8 ohm,1/8WV(0.05W),1,0803,R/TP		
8	R423	RES,CHIP	ERUHY0008001	8 ohm,1/8WV(0.05W),1,0803,R/TP		
8	R424	RES,CHIP,MAKER	ERN-230080801	8 ohm,1/8WV,1,1808,R/TP		
8	R425	RES,CHIP,MAKER	ERN-230080801	8 ohm,1/8WV,1,1808,R/TP		
8	R426	RES,CHIP,MAKER	ERN-230080801	8 ohm,1/8WV,1,1808,R/TP		
8	R427	RES,CHIP,MAKER	ERN-230080801	8 ohm,1/8WV,1,1808,R/TP		
8	SPFV30	PCB,MAIN	SPFV30130001	FR-4,0.8 mm,STANDARD-10,.....		30
8	SW401	CONN,RF SWITCH	SWHY0002004	STANDARD,180,0.8 dB,LINE MODEL		
8	U203	IC	EUHY0002001	EFM,3 PIN,R/TP,1.4-4mA charger IC, 0.1L,2x3 DFN, Pb-free		
8	U205	IC	EUHY0002001	WQFN,18 PIN,R/TP,Small package Dual SPDT analog switch, Pb-Free		
8	U206	ID	EUHY0014001	QFN,18 PIN,R/TP,		
8	U207	IC	EUHY0008001	Output voltage audio subsystem with 3D,34 PIN,R/TP,180 audio amp		
8	U208	IC	EUHY0002001	WQFN,18 PIN,R/TP,Small package Dual SPDT analog switch, Pb-Free		
8	U209	IC	EUHY0103001	80T-22,3 PIN,R/TP,DO MOTOR DRIVER / INTEGRATED RELAY		
8	U204	ID	EUHY0010001	QFN,10 PIN,R/TP,CAP		
8	U402	PAM	SPFV3012001	QFN,16, A, dB, dB, JMD,		
8	U403	ID	EUHY0274001	VQFN,48 PIN,R/TP,APPS, EDGE TRANSDUCER		
8	W401	VARIABLE	SEVY0004001	10 V,180,3pF,1805		
8	X401	VCT032	ERN-230080801	20 MHz,3 PPM,10 pF,840,0.2*0.2*0.05,0.25mm al-20 in 0.75, AFC 0.5V to 2.5V, Supply 2.5V		

12. EXPLODED VIEW & REPLACEMENT PART LIST

12.3 Accessory

Note: This Chapter is used for reference, Part order is ordered by SBOM standard on GCSC

Level	Location No	Description	Part Number	Specs	Color	Quantity
3	88P100	BATTERY PACK(LI-ION)	88P10000000	3.7 V,300 mAh,1 CELL,PRISMATIC ,CHRY FJT BATT, Janyapak, Europe Label, Pb-Free ± 3.7 V001 0.30 PRISMATIC /300mAh , ALLTEL SILVER ,Janyapak ,CHRY Side & Folder	AIRY BLUE	125
3	88DY00	DATA CABLE	88DY0010001	USB-A/USB-C , 18pin USB Data Cable		
4	88HY00	EAR PHONE/EAR MIKE SET	88HY0000000	3.5mm , 16 OHM , 100dB ,FO , 1000HZ , Janyak , 18pin MINI CONNECTOR , SILVER, L&L MARK		
3	88AD00	ADAPTOR,AC-DC	88AD0001000	100-240V ,50/60 Hz,4.8 V,0.9 A,CB & CE , 18pin plug		

Note
